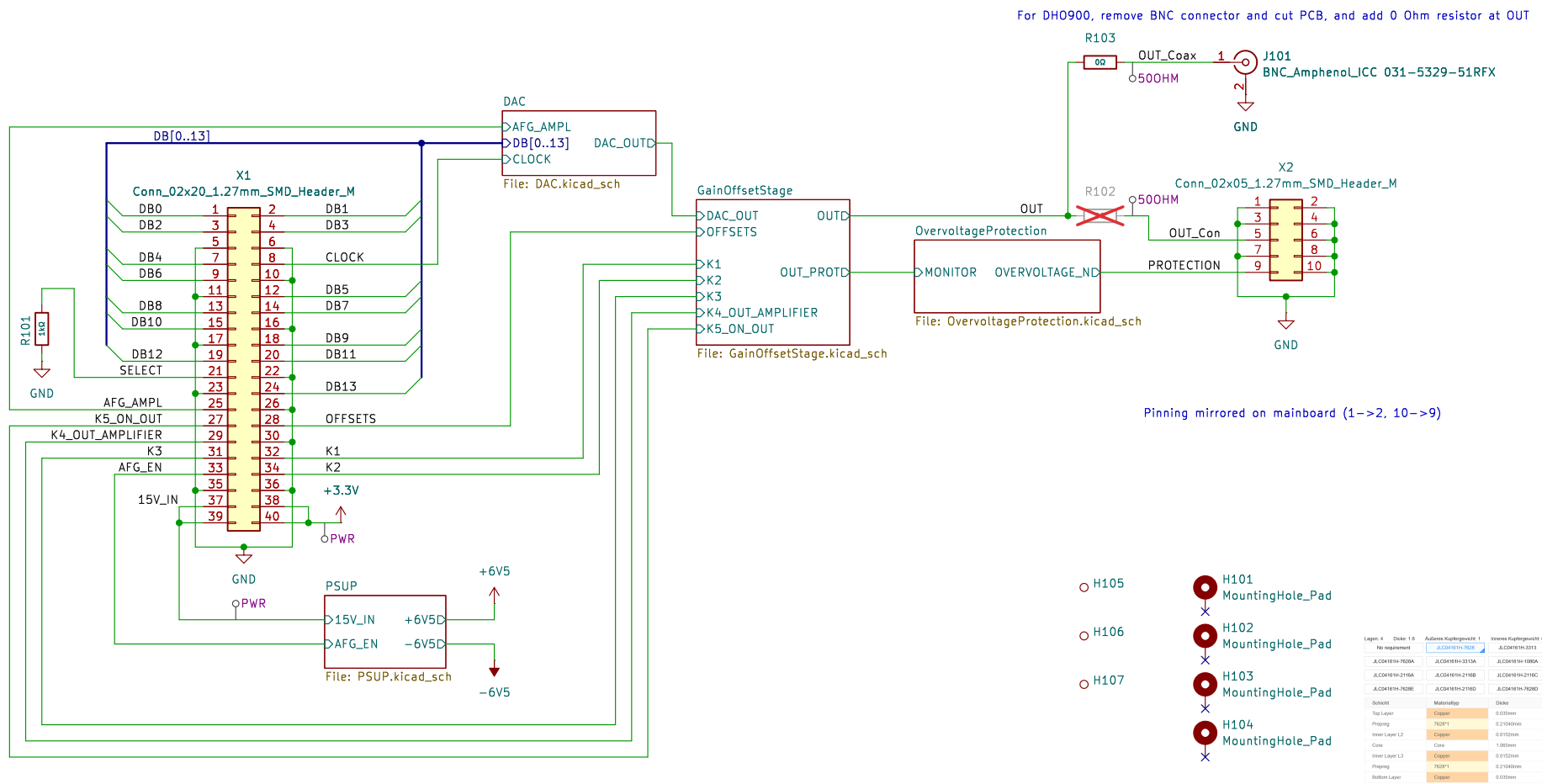
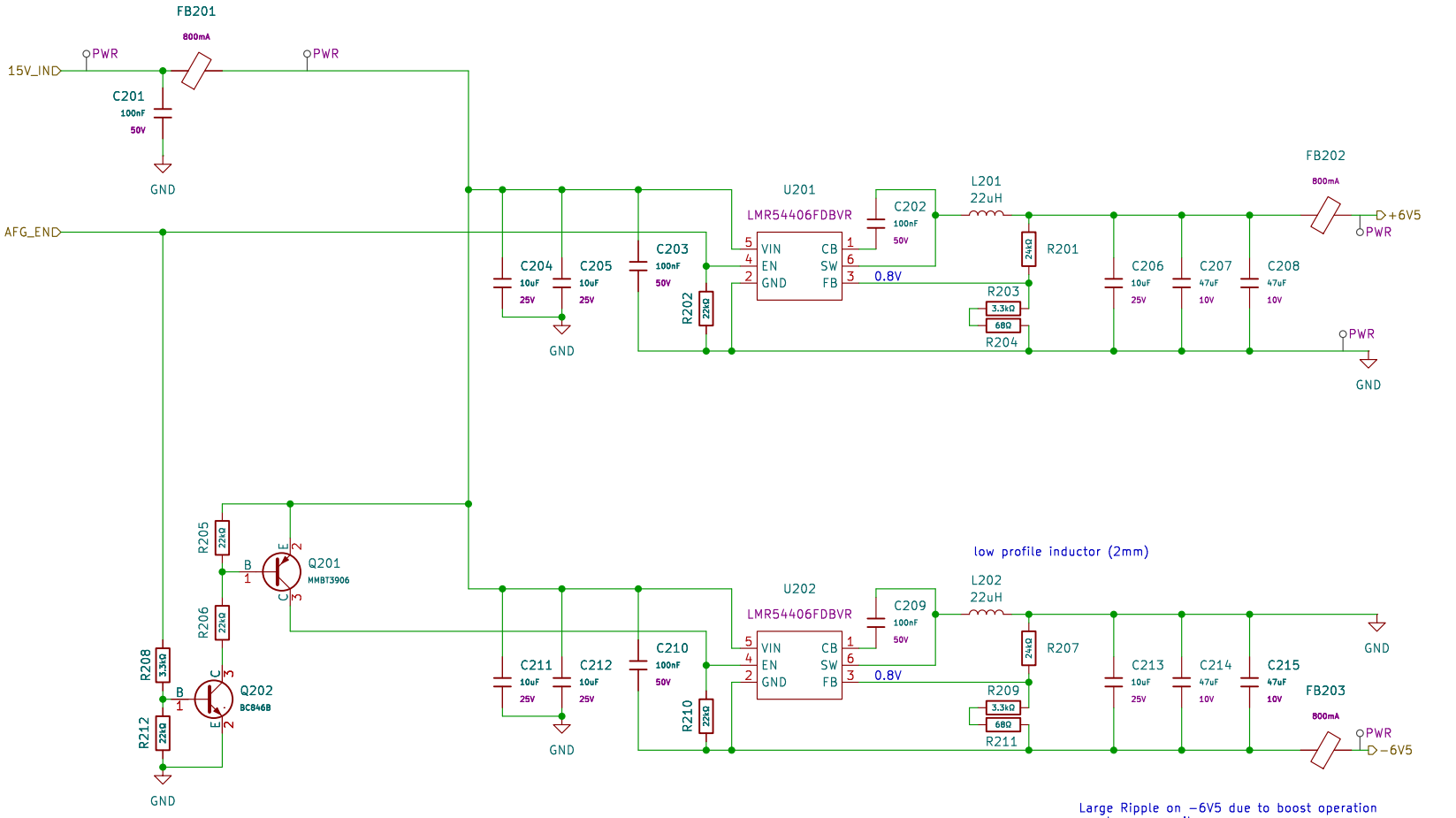
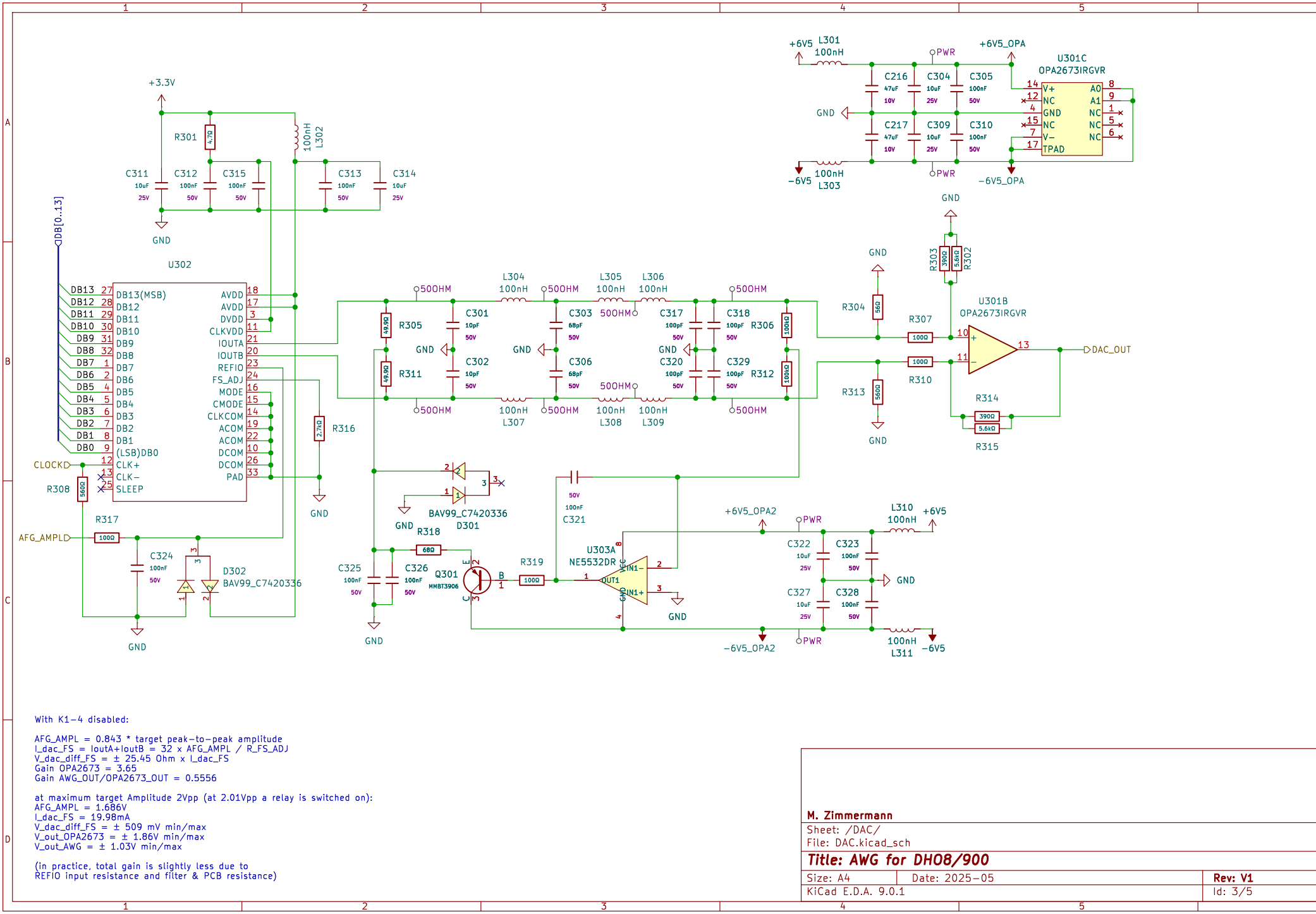


M. Zimmermann		
Sheet: /		
File: AWG.kicad_sch		
Title: AWG for DHO8/900		
Size: A4	Date: 2025-05	**Rev: V1**
KiCad E.D.A. 9.0.1		Id: 1/5



M. Zimmermann		
Sheet: /PSUP/		
File: PSUP.kicad_sch		
Title: AWG for DH08/900		
Size: A4	Date: 2025-05	**Rev: V1**
KiCad E.D.A. 9.0.1		Id: 2/5



With K1−4 disabled:

AFG_AMPL = 0.843 * target peak−to−peak amplitude
I_dac_FS = IoutA+IoutB = 32 x AFG_AMPL / R_FS_ADJ
V_dac_diff_FS = ± 25.45 Ohm x I_dac_FS
Gain OPA2673 = 3.65
Gain AWG_OUT/OPA2673_OUT = 0.5556

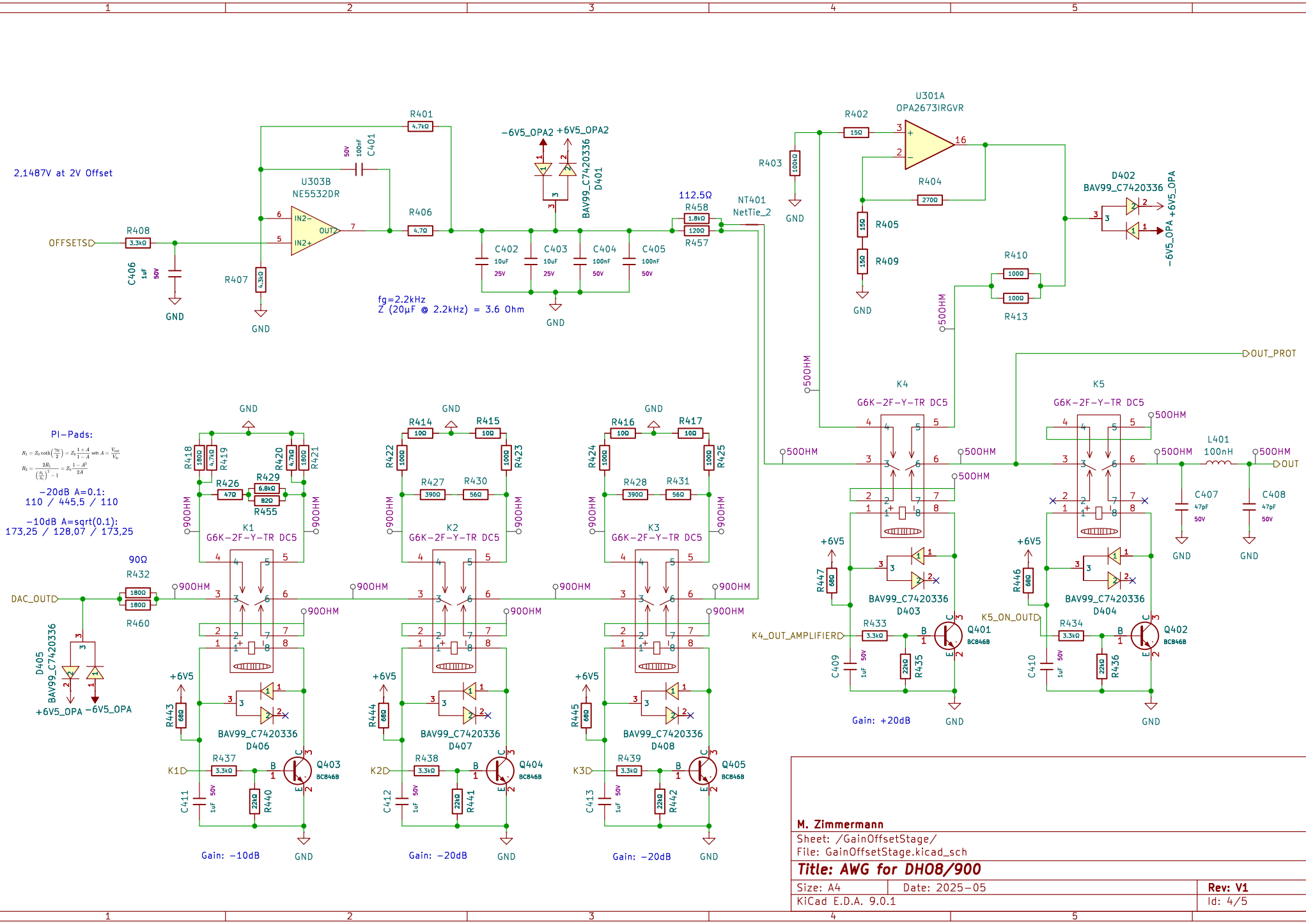
at maximum target Amplitude 2Vpp (at 2.01Vpp a relay is switched on):
AFG_AMPL = 1.686V
I_dac_FS = 19.98mA
V_dac_diff_FS = ± 509 mV min/max
V_out_OPA2673 = ± 1.86V min/max
V_out_AWG = ± 1.03V min/max

(in practice, total gain is slightly less due to
REFIO input resistance and filter & PCB resistance)

M. Zimmermann
Sheet: /DAC/
File: DAC.kicad_sch

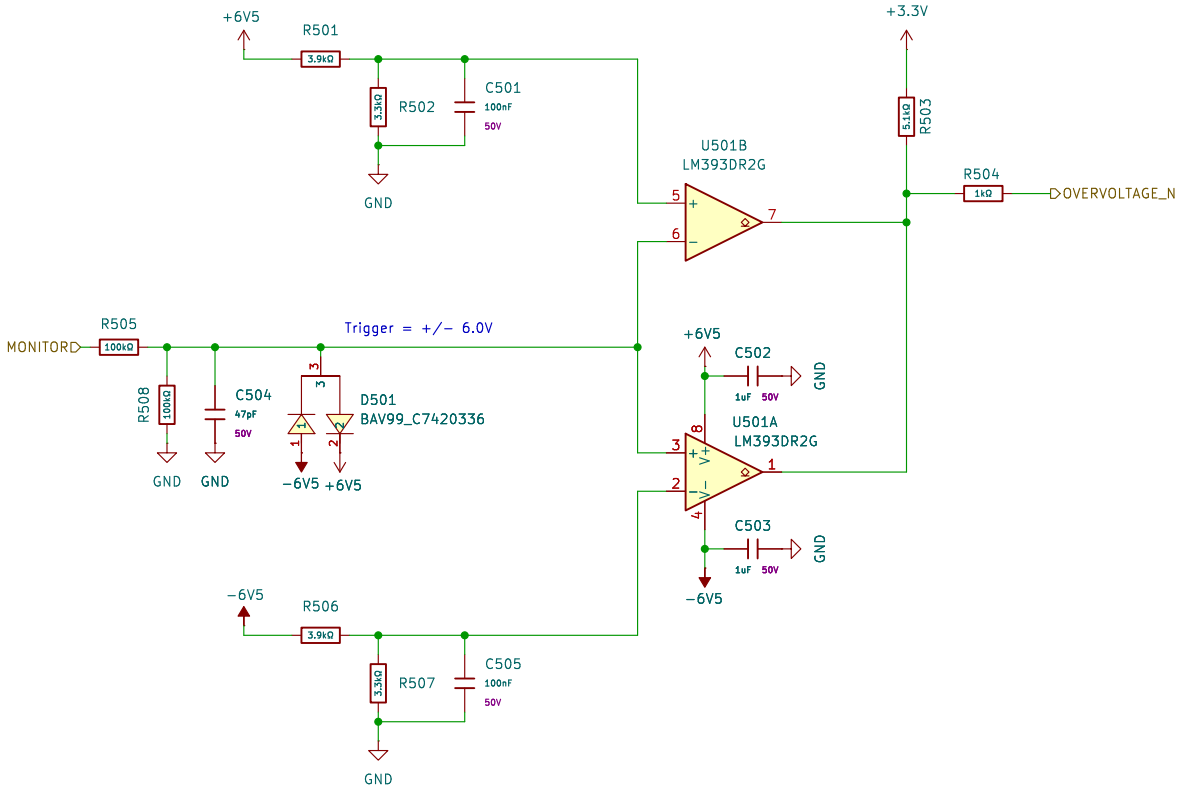
Title: AWG for DHO8/900

Size: A4	Date: 2025−05	**Rev: V1**
KiCad E.D.A. 9.0.1		Id: 3/5



M. Zimmermann	
Sheet: /GainOffsetStage/	
File: GainOffsetStage.kicad_sch	
Title: AWG for DH08/900	
Size: A4	Date: 2025−05
KiCad E.D.A. 9.0.1	Id: 4/5

Rev: V1



M. Zimmermann		
Sheet: /OvervoltageProtection/		
File: OvervoltageProtection.kicad_sch		
Title: AWG for DH08/900		
Size: A4	Date: 2025-05	**Rev: V1**
KiCad E.D.A. 9.0.1		Id: 5/5