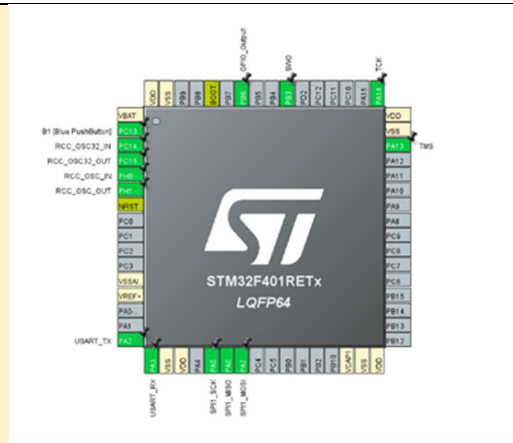


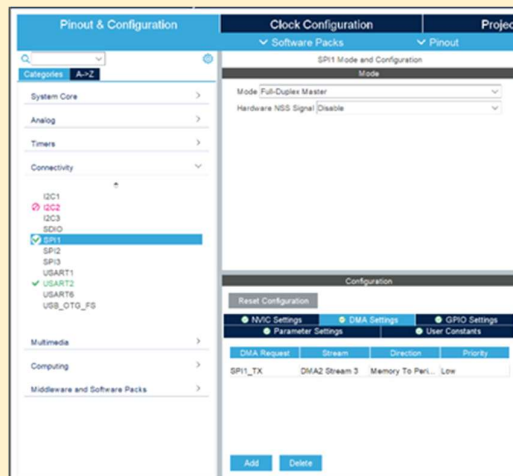
Mark	/11
------	-----

Team name:	A14		
Homework number:	HOMEWORK 09		
Due date:	16/11/25		
Contribution	NO	Partial	Full
Mattia Di Mauro			x
Francesca Biondi			x
Lorenzo Castelli			x
Carmen Maria Niro			x
Pietro Albrigi			x
Notes: none			

Project name	HOMEWORK 9		
Not done	Partially done (major problems)	Partially done (minor problems)	Completed
			x
<p>The goal of this project was to transmit a letter to an LED matrix using SPI and a timer interrupt, alternating between two letters over time. The implementation required the configuration of GPIO pins, SPI communication, DMA transfers and two timers operating at different frequencies to handle the refresh of the display and the switching between characters.</p> <p>In the CubeMX configuration, the pin PB6 was set as a general-purpose output. This pin corresponds to the RCLK signal, which acts as a latch for the LED matrix driver: after each SPI transmission, a short pulse on this pin updates the outputs of the matrix, making the new data visible. The pins PA5, PA6 and PA7 were instead configured as SPI1 interface lines, specifically for the SCK, MISO and MOSI functions. This configuration allows the STM32 to send serial data to the LED matrix driver.</p>			



The SPI1 peripheral was set in master mode with a prescaler value of four. The SPI transmission was enabled in DMA mode, allowing data to be sent automatically without involving the CPU.



Two timers were used to coordinate the operation of the system. Timer 2 was configured to generate an interrupt every 4 milliseconds, determining the refresh rate of the matrix, while Timer 3 generated an interrupt every 1 second, used to alternate between two different characters. The priority of Timer 3 was set to 1 to ensure accurate and regular display updates.

Pinout & Configuration

Categories: A-Z

System Core

Analog

Timers

RTC

TIM1

TIM2

TIM4

TIM5

TIM9

TIM10

TIM11

Connectivity

Multimedia

Computing

Middleware and Software Packs

Clock Configuration

Software Packs

Pinout

Project

TIM3 Mode and Configuration

Mode

Slave Mode (Disable)

Trigger Source (Disable)

Clock Source (Internal Clock)

Channel1 (Disable)

Channel2 (Disable)

Channel3 (Disable)

Channel4 (Disable)

Combined Channels (Disable)

Use ETR as Clearing Source

JCR activation

One Pulse Mode

Configuration

Reset Configuration

User Constants

NVIC Settings

DMA Settings

Parameter Settings

Configure the below parameters

Counter Settings

Prescaler (PSC - 16 bits v... 8399)

Counter Mode (Up)

Counter Period (AutoReload... 39)

Internal Clock Division (CKD) No Division

auto-reload preloaded (Disable)

Trigger Output (TRGO) Parameters

Master/Slave Mode (MSM) (Disable (Trigger input effect not delay...))

Trigger Event Selection (Reset (UG bit from TIMx_EGR))

Pinout & Configuration

Software Packs

Pinout

Project

TIM3 Mode and Configuration

Mode

Slave Mode (Disable)

Trigger Source (Disable)

Clock Source (Internal Clock)

Channel1 (Disable)

Channel2 (Disable)

Channel3 (Disable)

Channel4 (Disable)

Combined Channels (Disable)

Use ETR as Clearing Source

JCR activation

One Pulse Mode

Configuration

Reset Configuration

User Constants

NVIC Settings

DMA Settings

Parameter Settings

Configure the below parameters

Counter Settings

Prescaler (PSC - 16 bits v... 8399)

Counter Mode (Up)

Counter Period (AutoReload... 3999)

Internal Clock Division (CKD) No Division

auto-reload preloaded (Disable)

Trigger Output (TRGO) Parameters

Master/Slave Mode (MSM) (Disable (Trigger input effect not delay...))

Trigger Event Selection (Reset (UG bit from TIMx_EGR))

Configuration

NVIC

Code generation

Debug monitor	<input checked="" type="checkbox"/>	0
Pendable request for system service	<input checked="" type="checkbox"/>	0
Time base: System tick timer	<input checked="" type="checkbox"/>	0
PVD interrupt through EXTI line 16	<input type="checkbox"/>	0
Flash global interrupt	<input type="checkbox"/>	0
RCC global interrupt	<input type="checkbox"/>	0
TIM2 global interrupt	<input checked="" type="checkbox"/>	0
TIM3 global interrupt	<input checked="" type="checkbox"/>	1
SPI1 global interrupt	<input type="checkbox"/>	0
USART2 global interrupt	<input type="checkbox"/>	0
EXTI line[15:10] interrupts	<input type="checkbox"/>	0
DMA2 stream3 global interrupt	<input checked="" type="checkbox"/>	0
FPU global interrupt	<input type="checkbox"/>	0

```

57 /* USER CODE BEGIN PV */
58
59 uint8_t FONT_A[5] = {
60     0b111100,
61     0b0010010,
62     0b0010001,
63     0b0010010,
64     0b111100
65 };
66
67 uint8_t FONT_B[5] = {
68     0b111111,
69     0b1001001,
70     0b1001001,
71     0b1001001,
72     0b0110110
73 };
74
75 uint8_t COL[5] = {
76     0b00000001,
77     0b00000010,
78     0b00000100,
79     0b00001000,
80     0b00010000
81 };
82
83 uint8_t tx2[2];
84 uint8_t col_idx=0;
85 int font=0;
86
87 /* USER CODE END PV */

```

A dedicated function named RCLK_Pulse() was implemented to manage the latching mechanism. This function generates a short pulse on the RCLK pin by toggling it high and then low. Every time a new pair of bytes is transmitted over SPI, this pulse transfers the data from the shift registers to the output registers of the LED driver, updating the LEDs that are currently active.

```

104 void RCLK_Pulse(void)
105 {
106     HAL_GPIO_WritePin(RCLK_GPIO_Port, RCLK_Pin, GPIO_PIN_RESET);
107     HAL_GPIO_WritePin(RCLK_GPIO_Port, RCLK_Pin, GPIO_PIN_SET);
108     HAL_GPIO_WritePin(RCLK_GPIO_Port, RCLK_Pin, GPIO_PIN_RESET);
109 }

```

The SPI communication was handled using DMA, and the completion of each transmission triggered the callback function HAL_SPI_TxCpltCallback(). Within this callback, the RCLK pulse function was called to latch the transmitted data, and the column index variable was incremented modulo five to move to the next column. In this way, each SPI transmission corresponds to the update of one column of the currently displayed letter.

```

111 void HAL_SPI_TxCpltCallback(SPI_HandleTypeDef *hspi)
112 {
113     if (hspi==&hspi1)
114     {
115         RCLK_Pulse();
116         col_idx=(col_idx+1)%5;
117     }
118 }

```

The overall behavior of the system was controlled through the timer interrupt callback HAL_TIM_PeriodElapsedCallback(). When the interrupt was generated by Timer 2, the program prepared the data for the next transmission. Depending on the value of a variable named font, the system selected either the letter "A" or the letter "B". The corresponding column data was copied from the chosen font array into a two-byte buffer, with the first byte representing the LED pattern and the second byte selecting the active column. This buffer was then transmitted through SPI using the DMA function HAL_SPI_Transmit_DMA(&hspi1, tx2, 2). The DMA mechanism automatically handled the transfer, and upon completion, the SPI callback was invoked to latch the data and proceed to the next column.

```

437 /* USER CODE BEGIN 4 */
438
439 void HAL_TIM_PeriodElapsedCallback(TIM_HandleTypeDef *htim)
440 {
441     if (htim == &htim2)
442     {
443         if(font)
444         {
445             tx2[0] = FONT_B[col_idx];
446             tx2[1] = COL[col_idx];
447         }
448         else
449         {
450             tx2[0] = FONT_A[col_idx];
451             tx2[1] = COL[col_idx];
452         }
453
454
455         HAL_SPI_Transmit_DMA(&hspi1, tx2, 2);
456     }
457
458     if(htim==&htim3)
459     {
460         if(font)
461         {
462             font=0;
463         }
464         else
465         {
466             font=1;
467         }
468     }
469 }
470
471
472 /* USER CODE END 4 */

```

When the interrupt was generated by Timer 3, the font variable was toggled between 0 and 1, effectively alternating the displayed character every second. This mechanism produced the visual effect of switching between two letters, such as "A" and "B", on the LED matrix.

The code was then tested on the board and confirmed to work as expected.

Professor comments: