

A

A

B

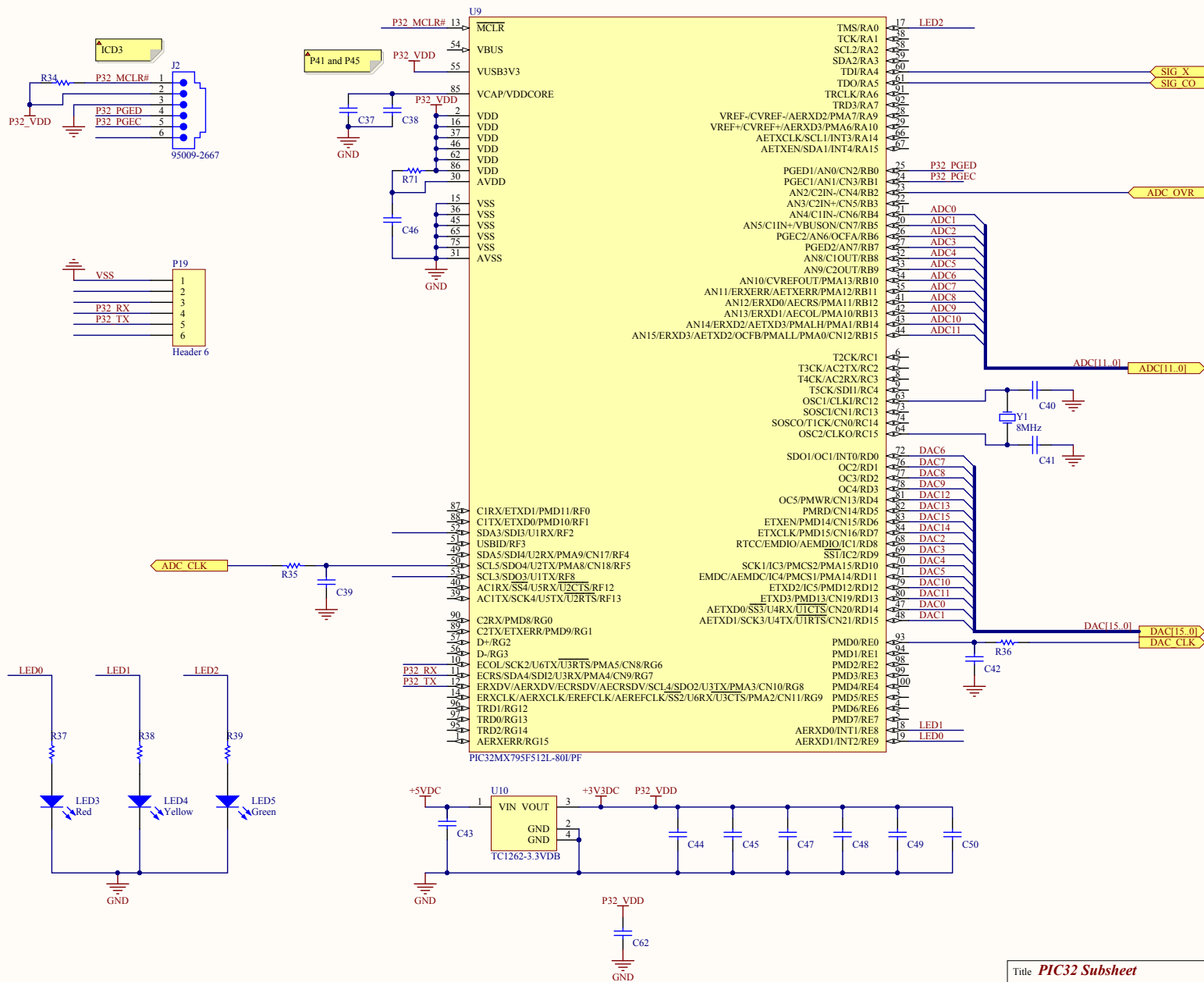
B

C

C

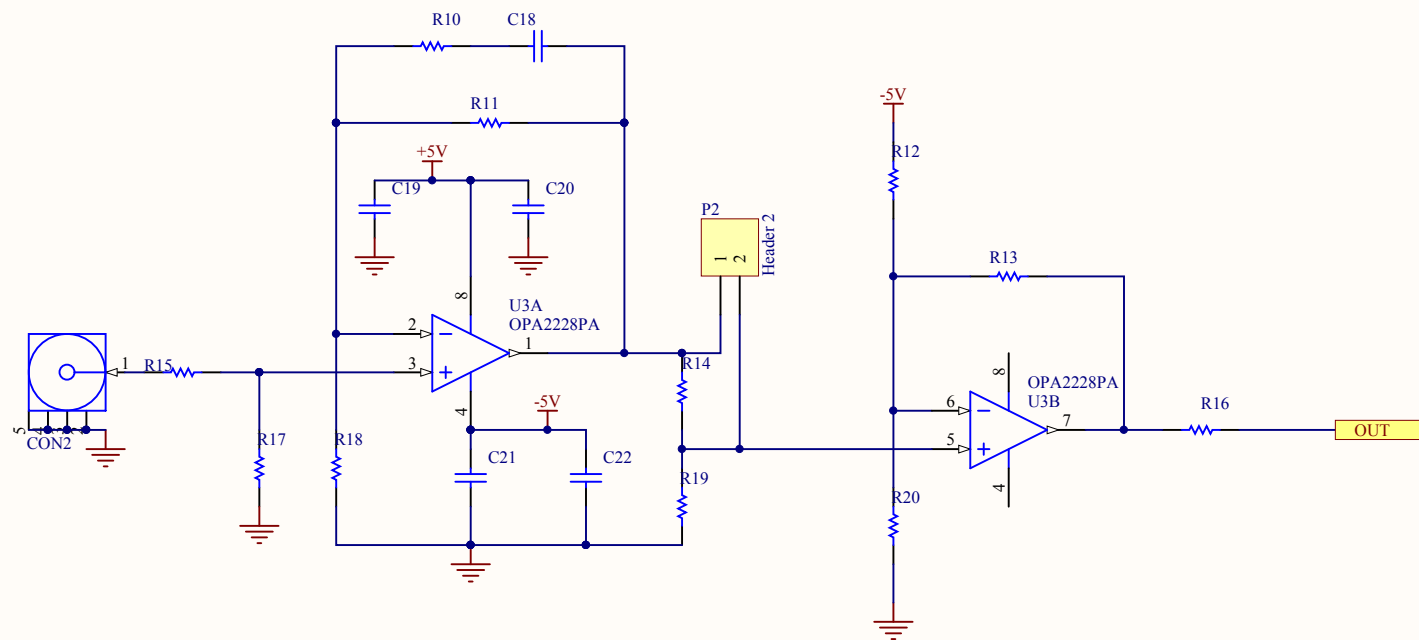
D

D

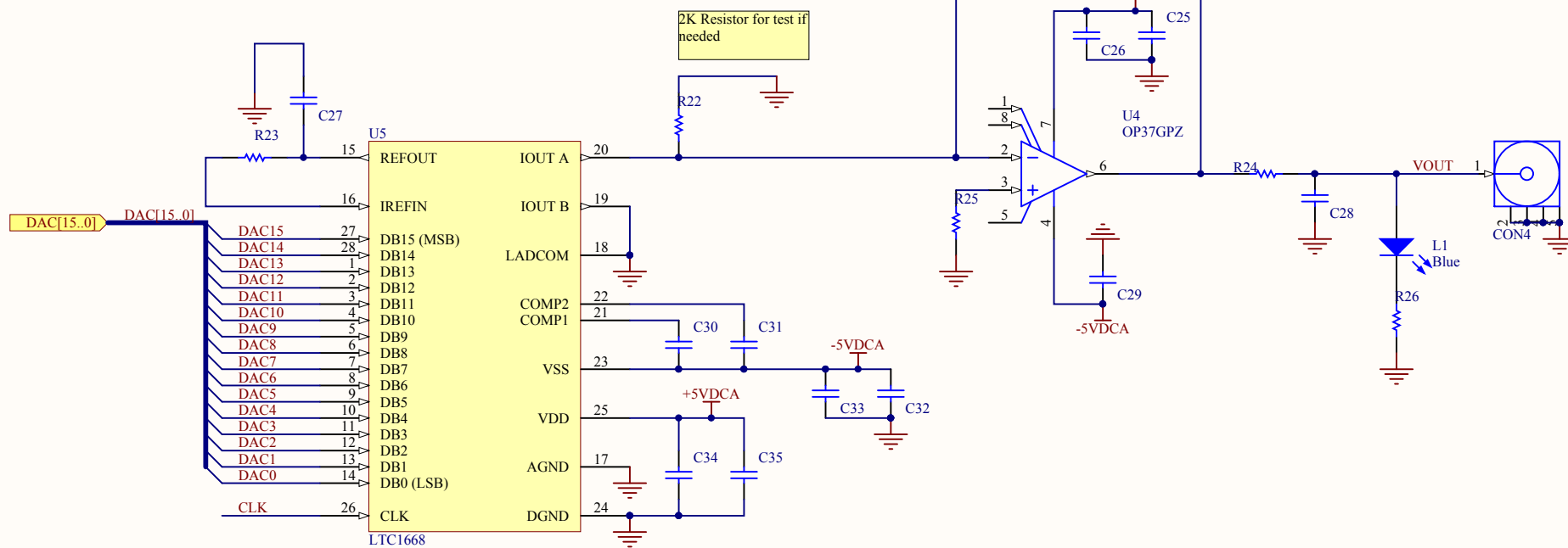
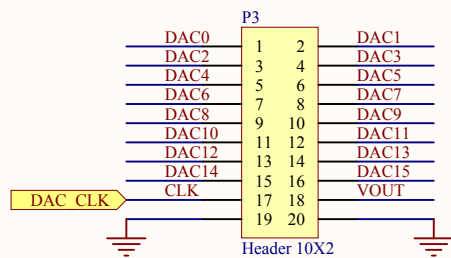


Title <b>PIC32 Subsheet</b>			
Size: <b>A3</b>	Number: <b>293</b>	Revision: <b>A</b>	Author : <b>Matthew Southgate</b>
Date: <b>08/05/2014</b>	Time: <b>11:12:31</b>	Sheet1 of <b>=</b>	Engineer : <b>Matthew Southgate</b>
File: <b>B293A PIC32 SchDoc</b>			





Title <b>CoX Amp</b>			
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Date: 08/05/2014	Time: 11:12:31	Sheet * of *	Engineer : Matthew Southgate
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Title **DAC**

Size: **A4**

Number: **293**

Revision: **A**

Author : **Matthew Southgate**

Date: **08/05/2014**

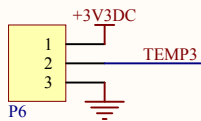
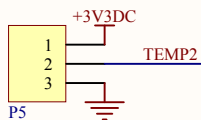
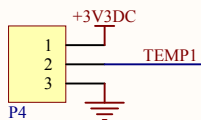
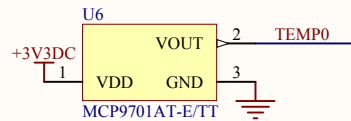
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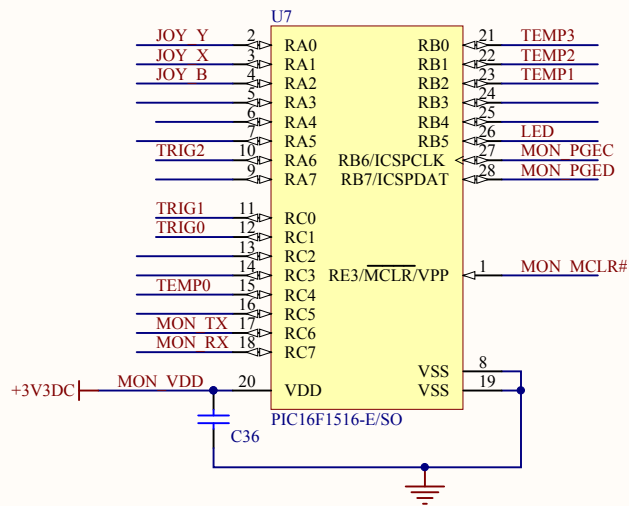
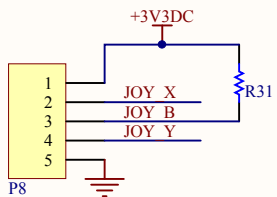
Engineer : **Matthew Southgate**

File: **B293A DAC.SchDoc**

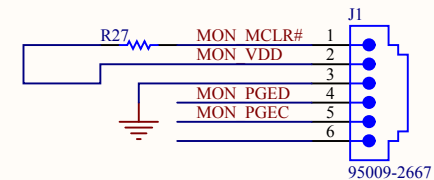
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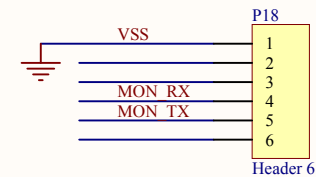
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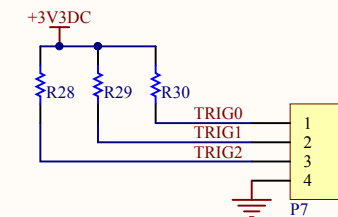
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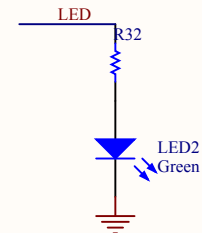
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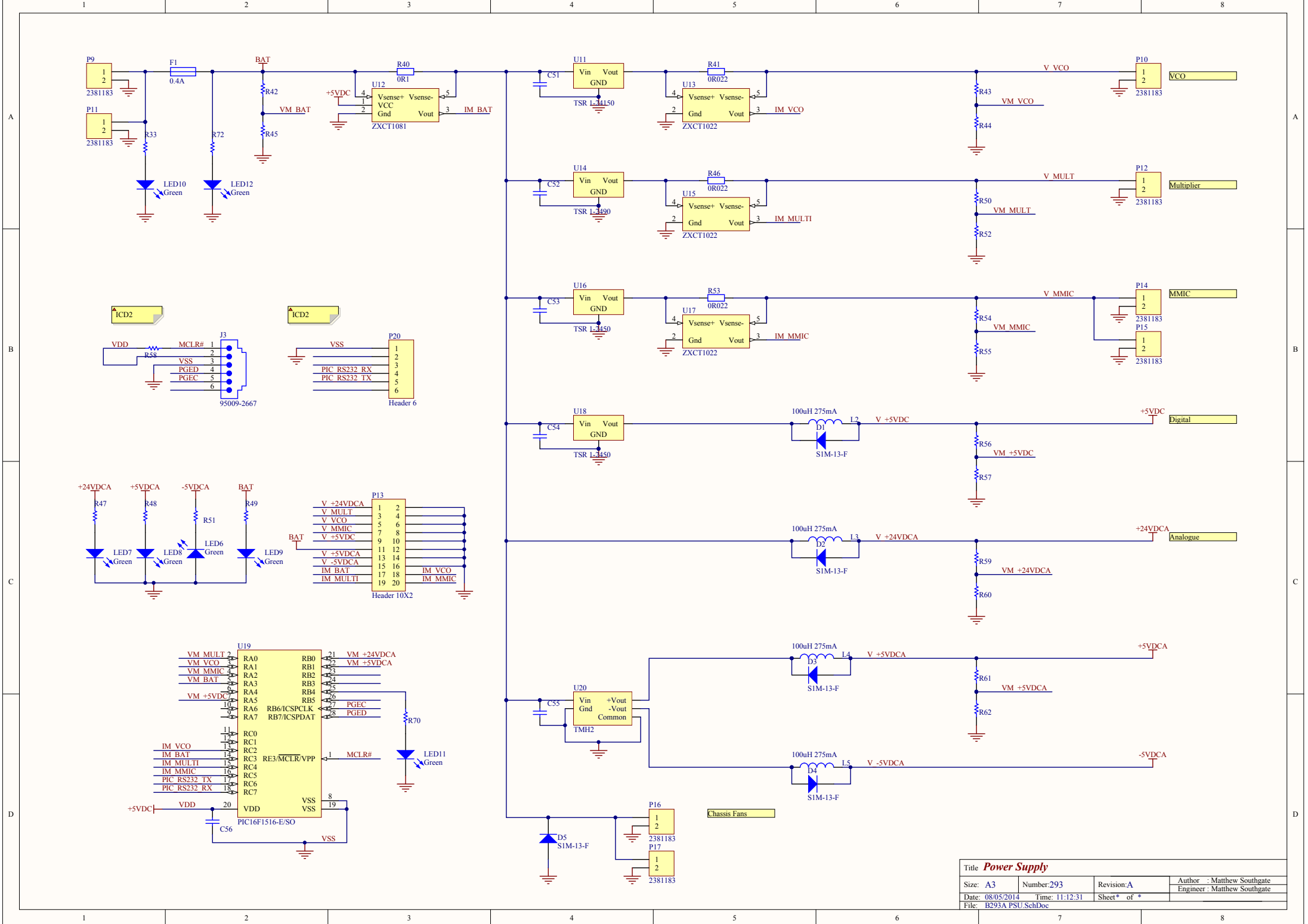
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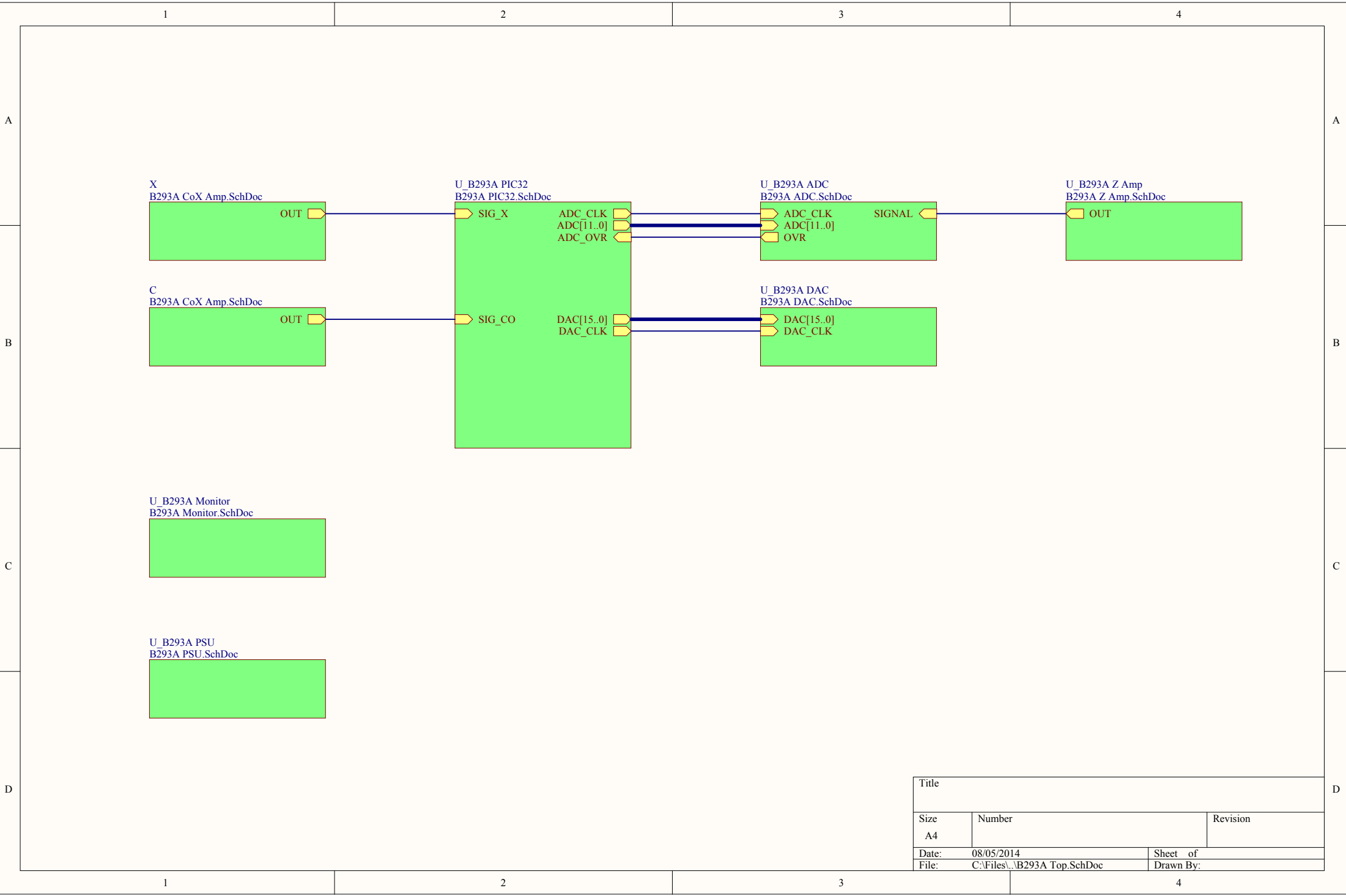
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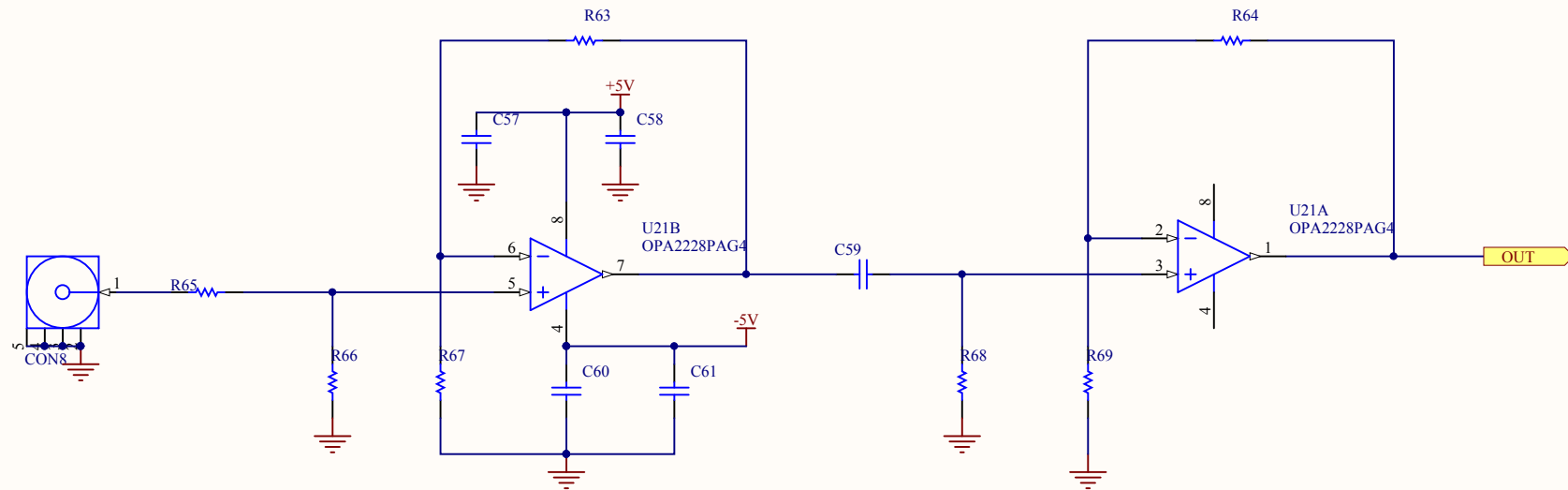
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Title <b>Power Supply</b>			
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Date: <b>08/05/2014</b>	Time: <b>11:12:31</b>	Sheet* of *	Engineer : <b>Matthew Southgate</b>
File: <b>B293A PSU.SchDoc</b>			

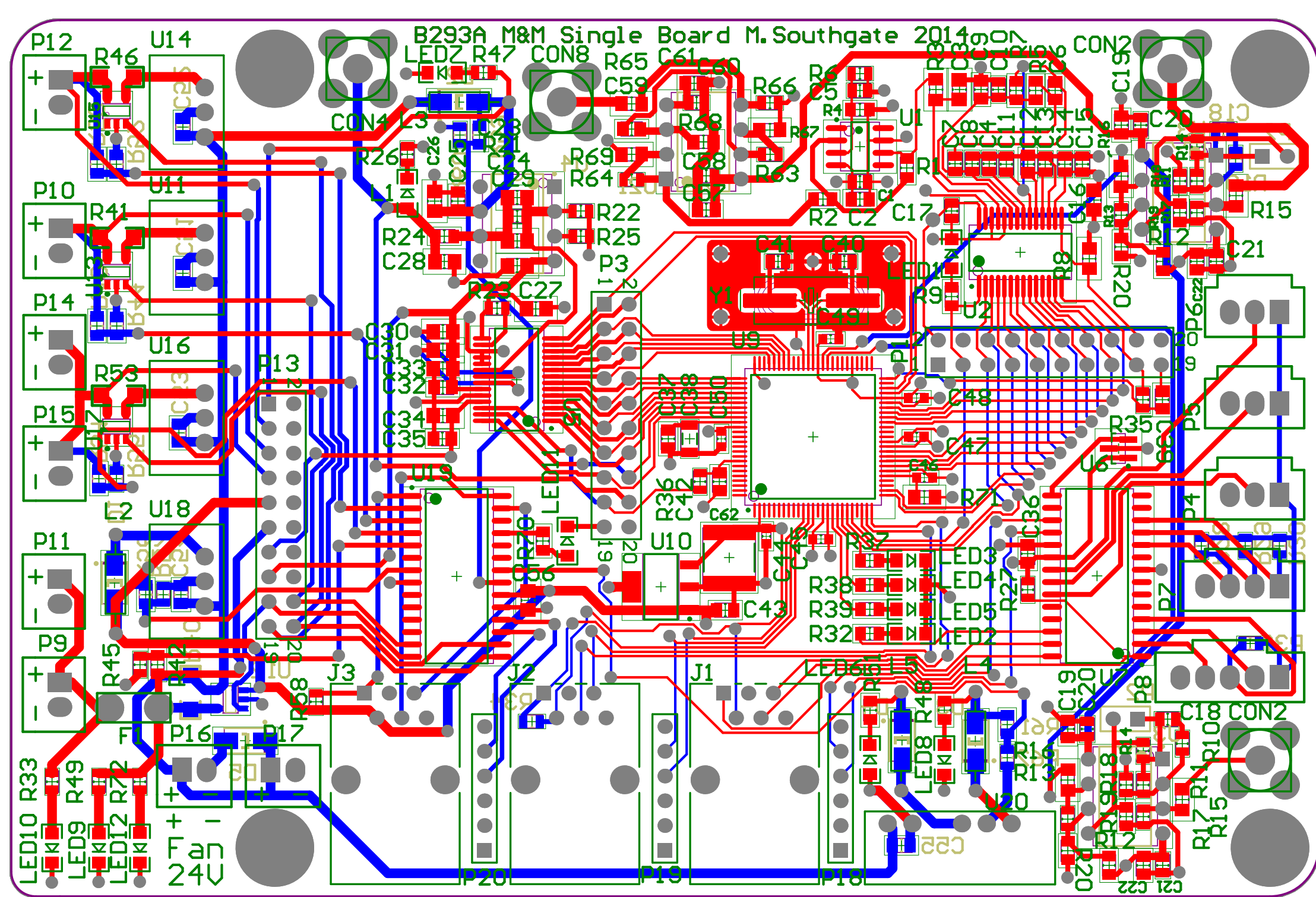


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A4		
Date:	08/05/2014	Sheet of
File:	C:\Files\B293A Top.SchDoc	Drawn By:



Title <b>Z Amp</b>			
Size: <b>A4</b>	Number: <b>293</b>	Revision: <b>A</b>	Author : Matthew Southgate
Date: 08/05/2014	Time: 11:12:32	Sheet * of *	Engineer : Matthew Southgate
File: <b>B293A Z Amp.SchDoc</b>			





Boards