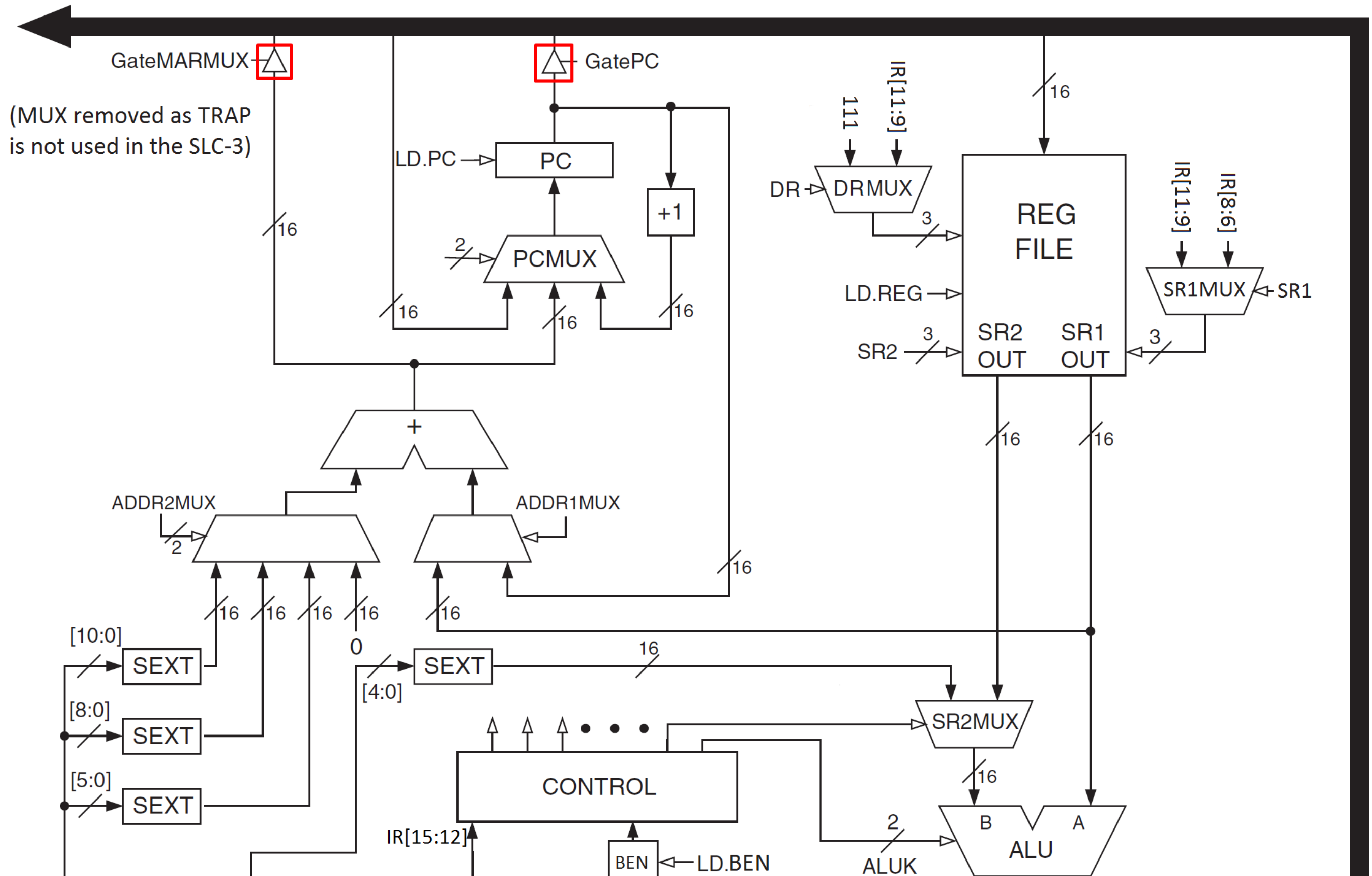
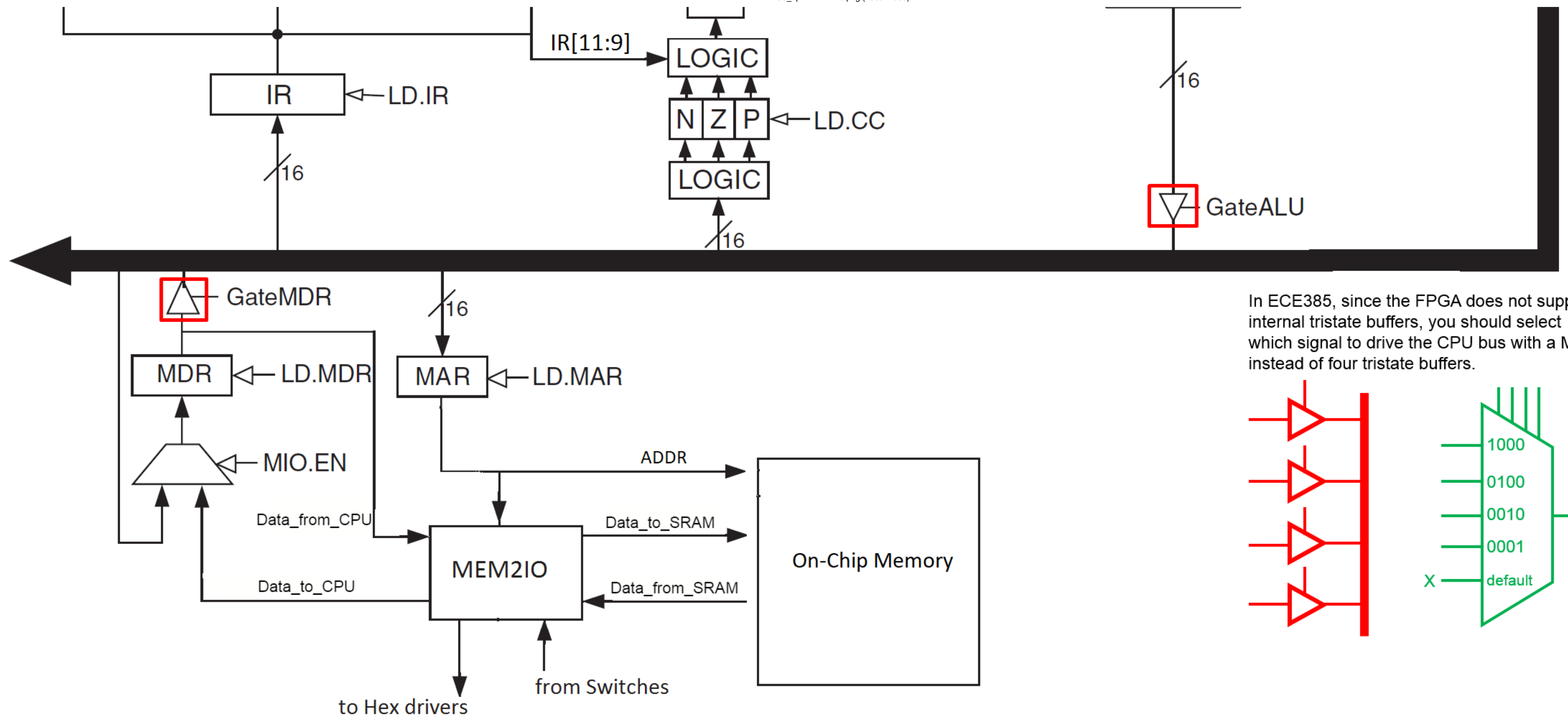


SLC-3 Updated Datapath for ECE 385





In ECE385, since the FPGA does not support internal tristate buffers, you should select which signal to drive the CPU bus with a MUX, instead of four tristate buffers.

