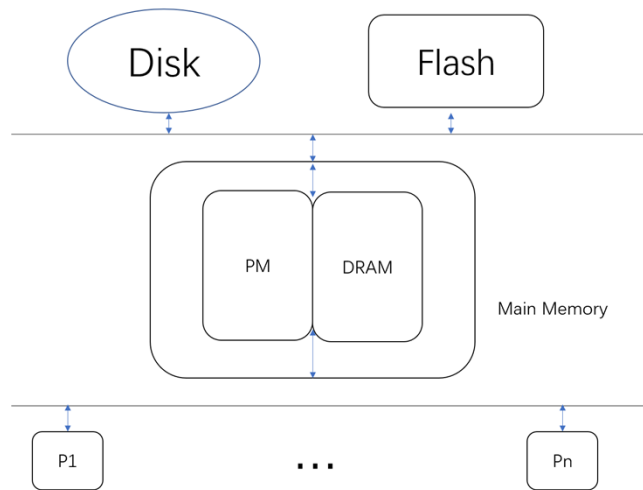(a) DRAM is the cache of PM

(b) Use DRAM and PM in parallel