

Moheb Youssef

San Diego, CA

-Email me on Indeed: <http://www.indeed.com/r/Moheb-Youssef/be57ef72e77470b8>

Highly skilled Electrical Engineer with 8 years of circuit board designing, design assurance, and system safety experience in the medical and aerospace industries.

Work Experience

Sr. Electrical Engineer

General Atomics- ASI - Poway, CA

2017 to Present

Held responsibility for the development and management of electrical hardware requirements for avionics box features and functions.

Captured electrical hardware requirements from the system specifications, design standards, safety analysis, and other sources.

Supported RDD MQ9 UAV to enhance the quality of hardware requirement specifications (HRS), and to remove any ambiguity associated with the technical functionality for the HRS.

Decomposed subsystem requirement specifications to hardware requirement specification tracing parents and children requirements for different LRUs.

Documented derived hardware requirements from the design process and design artifacts.

Streamlined departmental documentation on a continuing basis including but not limited to DDS, ICD, DVT, PTP, and CR/CN documents for MQ9 and CPB aircraft platforms.

Provided on-going support for other design engineering teams assisting with circuit board testing, troubleshooting, and designing.

Supported operation of engineering systems by evaluating and interpreting system safety needs for aircraft system and subsystems.

Developed system safety technical documentation, such as FMECA, FHA, FTA, SAR, as well as system and software requirements safety analysis.

Electrical Engineer

Vyaire Medical - Yorba Linda, CA

2013 to 2017

Designed and prototyped a transpulmonary pressure module (esophageal monitor) for Vyaire Medical next generation ventilator.

Created schematic, assembly, and fabrication drawings to support circuit board designing using Altium designer to Developed and executed design verification test (DVT) protocols and functional test protocols for circuit board subassemblies.

Evaluated and designed circuit boards utilizing analog circuitry and digital logic (including programmable logic).

Provided supporting subassembly documents including but not limited to HDS, HRS, de-rating analysis, and FMEA analysis.

Evaluated and implemented design changes through ECO and EVA processes.
Operated on extensive circuit board testing and troubleshooting utilizing variable test equipment (oscilloscope, function generator, programmable load, current probes, DMM).
Executed VAVE analysis for circuit board components to support various builds minimizing the risk associated with outsourcing and obsolescence.

Supported the development for communication bus protocols and interfaces (I2C, SPI, Ethernet, RS-232, TCP, and USB) that was utilized for testing/characterization for EVT plans.
Analyzed circuit board components for REACH, RoHS, and conflict material compliance.
Worked on releasing electronic components used on various circuit boards subassemblies for CareFusion ventilators (ECHO, AVEA, and SIPAP).

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Engineer Trainee

Delta Med-Tech International - Fullerton, CA
2009 to 2013

Trained on troubleshooting technical problems for different medical devices with emphasis on dialysis machines.
Developed skills to assist in completely refurbishing dialysis machines.

Education

Bachelor of Science in Electrical Engineering

California State University
May 2013

Skills

- Altium Designer
- ORCAD (PSPICE)
- Microsoft Project
- 3D Modeling
- DOORS
- Simulink
- Soldering
- C++
- MATLAB
- Microsoft Excel
- SMT Board Rework
- Labview
- Microsoft Visio
- PTC Windchill
- SVN

Links

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