

William Lorentz

Frederick, MD

-Email me on Indeed: <http://www.indeed.com/r/William-Lorentz/5501bea70f615dff>

Work Experience

Senior RF Hardware Design Engineer

jacobs - Severn, MD

November 2020 to Present

Oversee, support, create and troubleshoot RF Designs intended for the Intelligence Community. Low Noise Amplifiers, Mixers, Integrated PLL's and High Power Amplifiers (1 to 5 Watts).

Senior RF Engineer

KBR-Wyle - Columbia, MD

September 2019 to November 2020

Project Management, Electrical and RF Engineering support for Quick Reaction projects supporting the US Government.

RF Engineer

W-2 Contract RF Design Engineer assigned to Northrop Grumman - Linthicum, MD

January 2019 to September 2019

RF Engineering support of the Phased Array Antenna product line.

RF Engineer

W-2 Contract RF Design Engineer assigned to Lockheed Martin - Syracuse, NY

April 2017 to December 2018

NY

RF design and analysis supporting Electronic Warfare and RADAR programs. Main tools were Matlab and Genesys.

Principal RF Engineer

intelligent RF Solutions - Sparks, MD

August 2015 to April 2017

RF design engineer supporting the development of 2 to 18 GHz RF Down Converter products. Performing spread sheet cascade analysis, linear simulation, schematic capture and initial parts placement. Involved in various aspects of RF, IF and synthesizer sections of the Down Converter design. Currently serving as technical lead for the latest variant of iRF's Down Converter product.

TAD PGS, W-2 Contract RF Design Engineer

ITT - Clifton, NJ

September 2009 to August 2015

40 watt 2 to 6 GHz GaN microstrip balanced amplifier design. RF/Microwave signal distribution system design using connectorized broadband components (solid-state switches, amplifiers, power dividers, directional couplers and limiting amplifiers). Power Amplifier Multi-Chip Assembly design using GaN and

GaAs MMIC die. Generate component performance specifications, block diagrams, schematic diagrams, part lists and test plans. Simulate system performance with spreadsheets and Agilent ADS.

RF Design Engineer

Aeroflex / Weinschel Subsystem Engineering - Frederick, MD
February 2008 to September 2009

Design of signal conditioning and distribution systems. Generate unit / component specifications, block diagrams, source control documents, schematics, simulations, test plans/procedures and design review packages. Verify component and first article performance using standard RF test equipment.

Sr. Hardware Engineer

PCTEL
September 2006 to February 2008

Oversight of the RF boards for the EX product line of measuring receivers covering 900 through 2100 MHz. Board includes analog RF front end through A/D converter. Responsible for validating the RF performance of first article systems and supporting FCC compliance testing. Support ongoing product improvements including schematic capture, bills of material and test procedures.

RF Engineer

Comtech Mobile Datacom Corporation
May 2005 to September 2006

Perform RF circuit design and analysis supporting existing and future L-band mobile satellite data transceivers. Generate component specifications, block diagrams and test plans/procedures. Responsible for the redesign of various RF sections (3 watt Power Amplifier, PIN Diode switch and Low Noise Amplifier) of the transceiver due to part obsolescence and performance improvement.

Sr. Electrical Engineer / CommEX LLC, Sr. RF Design Engineer

Rockwell Collins
January 2001 to May 2005

CommEX purchased by Rockwell Collins)

System Engineer in charge of the development of a new 50 kHz to 6 GHz DSP receiver. Responsible for the overall chassis design, integration and documentation for two N-Channel VHF/UHF Tuners. Hardware design and development of the RF circuitry (filters, amplifiers and RF signal path switching) for a series of HF Up-Converters (CX-7130 and CX-7141).

Receiver Development Department, Engineer IV / Member

BAE SYSTEMS
July 1995 to December 2000

of the Technical Staff

Design VHF/UHF receiver RF signal path circuitry with Genesys and company proprietary software tools. Project Engineer for several receiver projects with technical and schedule responsibilities. Prepared design documents to support assigned projects. Specified, selected and integrated cellular base station wideband transceiver ancillary equipment.

Education

Master of Science in Electrical Engineering

Capitol College

2005

Bachelor of Science in Electrical Engineering Technology

University of Pittsburgh

1987

Associates Degree in Avionics Systems Technology

Community College of the Air Force

1981

Military Service

Branch: United States Air Force

Service Country: United States

Rank: Staff Sergeant

January 1977 to January 1981

Electronic Warfare Systems Specialist supporting Test Programs at the 4950th Test Wing Wright Patterson AFB Dayton, OH

Commendations:

U S Air Force Commendation Medal