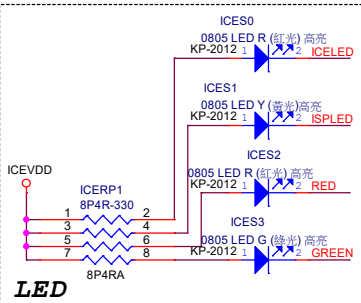
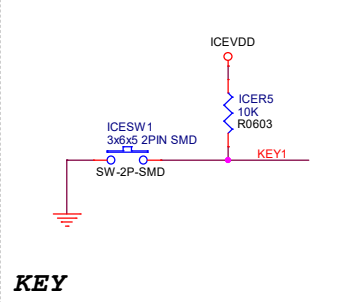
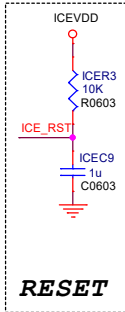
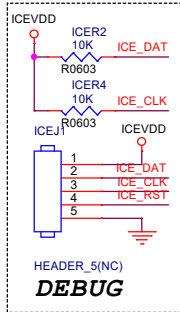
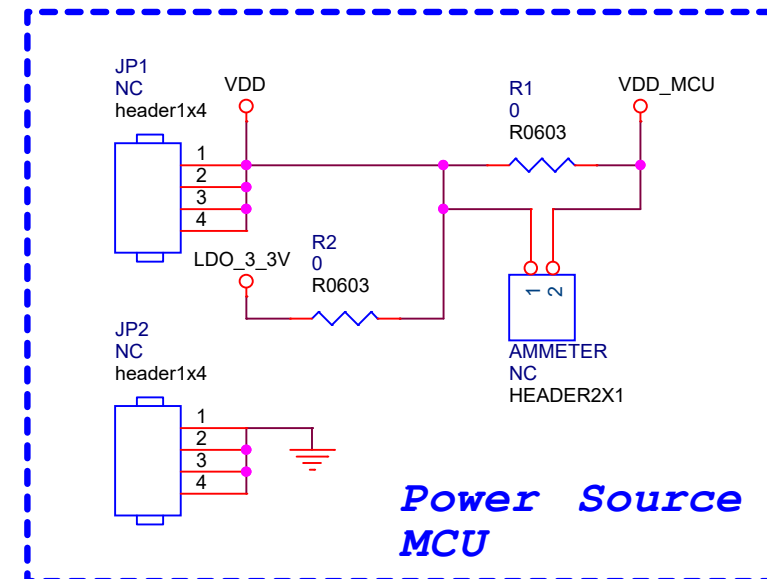
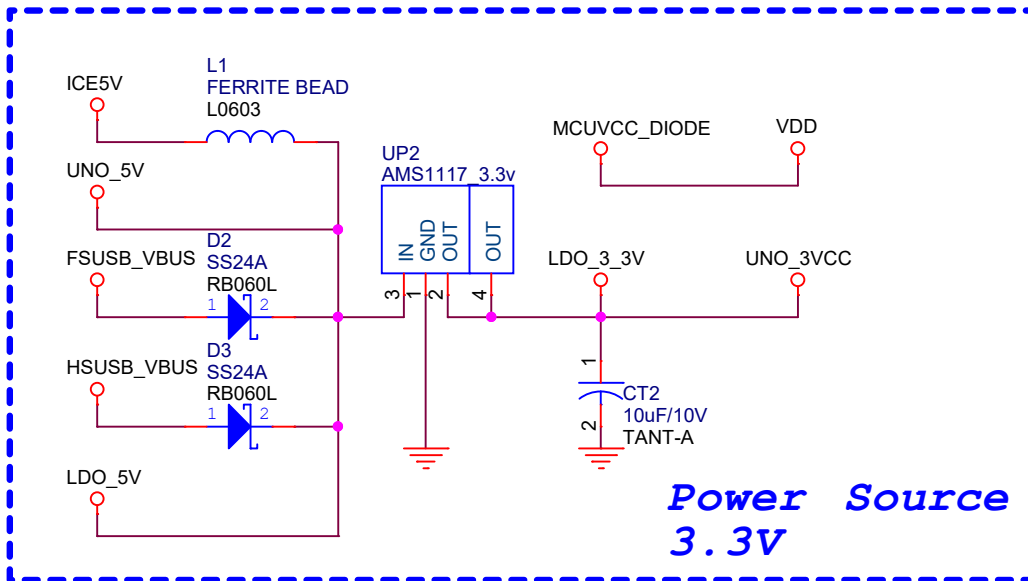
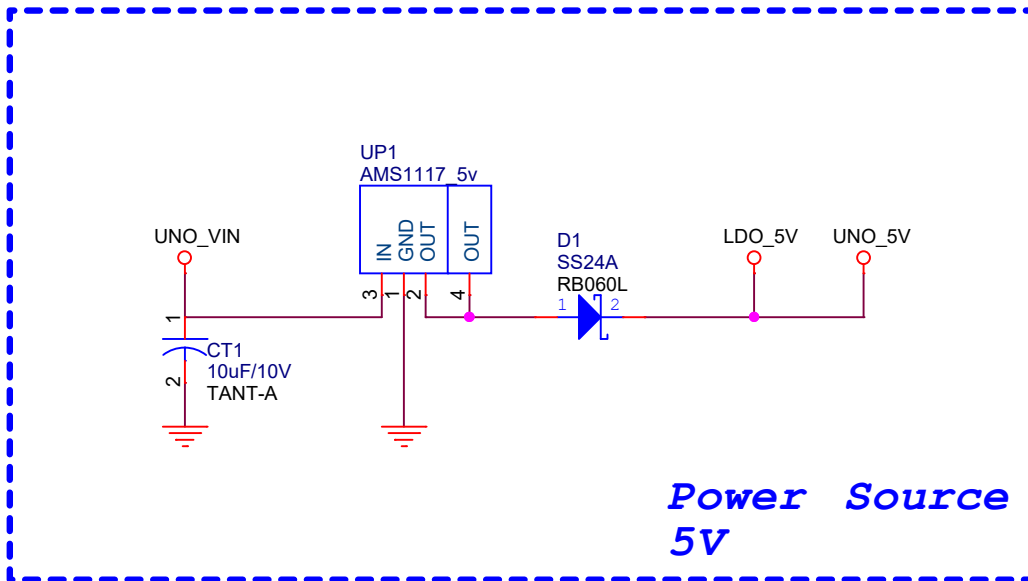
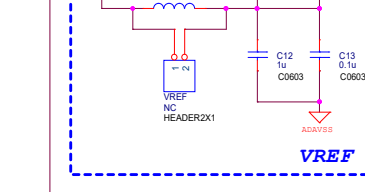
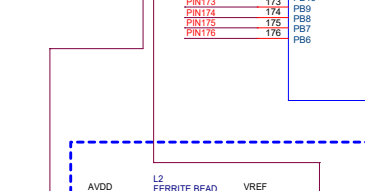
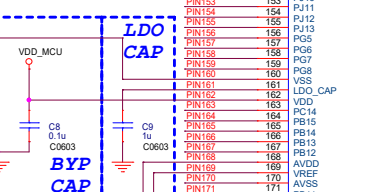
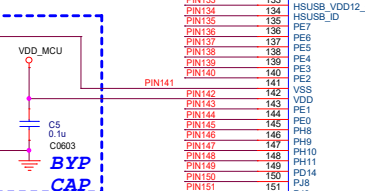
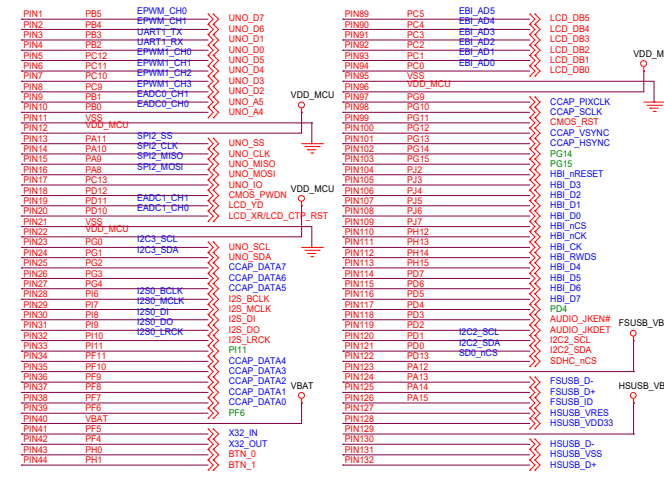
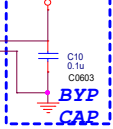
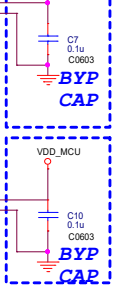
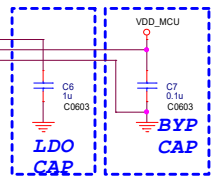
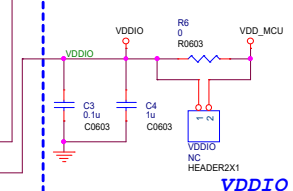
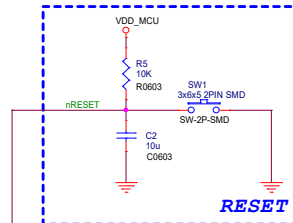
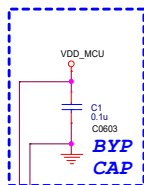
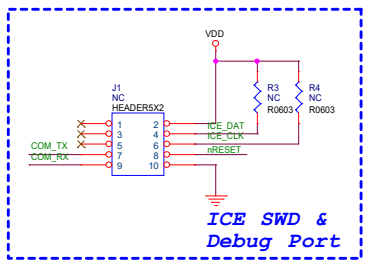


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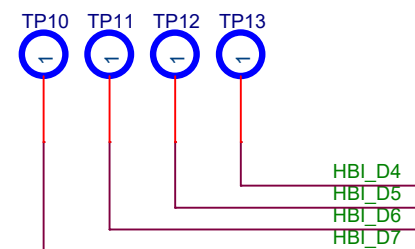
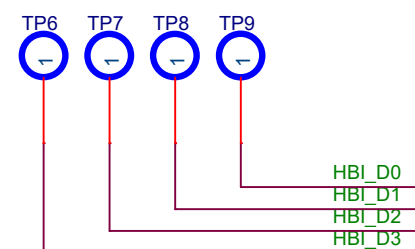
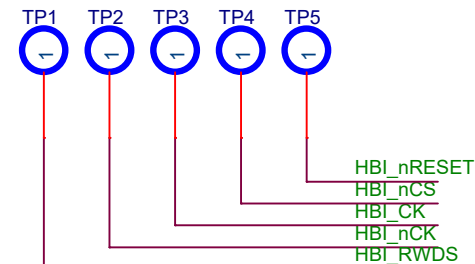
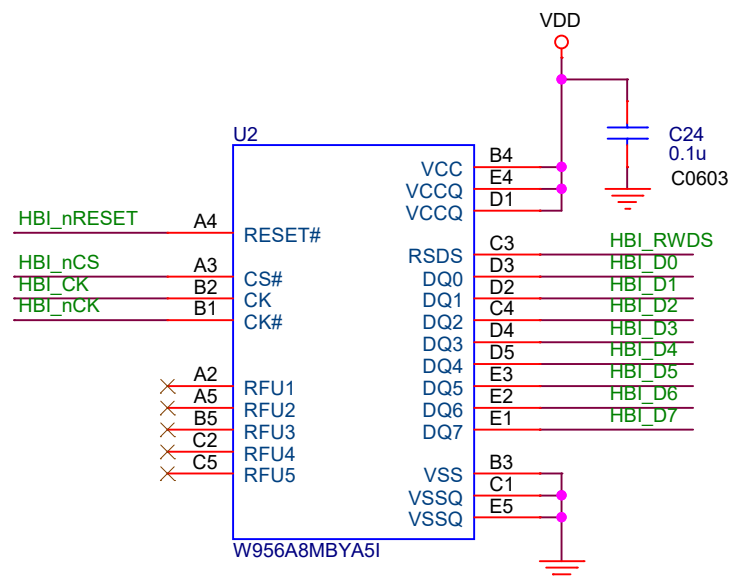






PIN104	PJ2	HBI_nRESET
PIN105	PJ3	HBI_D3
PIN106	PJ4	HBI_D2
PIN107	PJ5	HBI_D1
PIN108	PJ6	HBI_D0
PIN109	PJ7	HBI_nCS
PIN110	PH12	HBI_nCK
PIN111	PH13	HBI_CK
PIN112	PH14	HBI_RWDS
PIN113	PH15	HBI_D4
PIN114	PD7	HBI_D5
PIN115	PD6	HBI_D6
PIN116	PD5	HBI_D7

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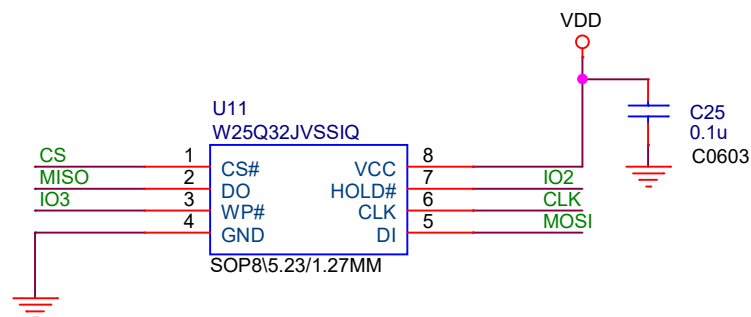
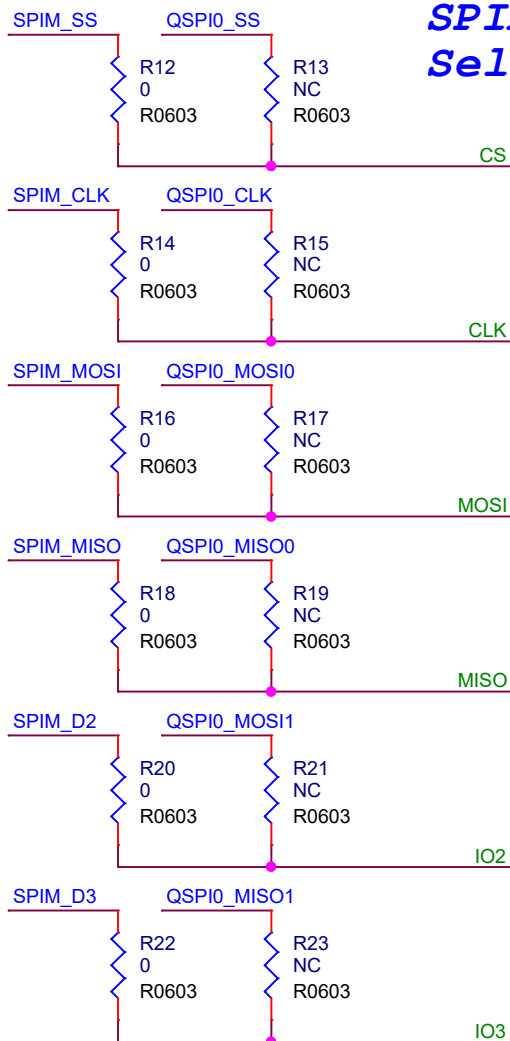
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PIN66	PI12	SPIM_SS	QSPI0_MISO1	}} FLASH_nCS FLASH_MISO FLASH_IO2 FLASH_IO3 FLASH_CLK FLASH_MOSI
PIN67	PI13	SPIM_MISO	QSPI0_MOSI1	
PIN68	PI14	SPIM_D2	QSPI0_SS	
PIN69	PI15	SPIM_D3	QSPI0_CLK	
PIN70	PJ0	SPIM_CLK	QSPI0_MISO0	
PIN71	PJ1	SPIM_MOSI	QSPI0_MOSI0	

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SPIM / QSPI Selection

These resistors are used to determine the SPI Flash is controlled by which i/f. By default, it's controlled by SPIM.



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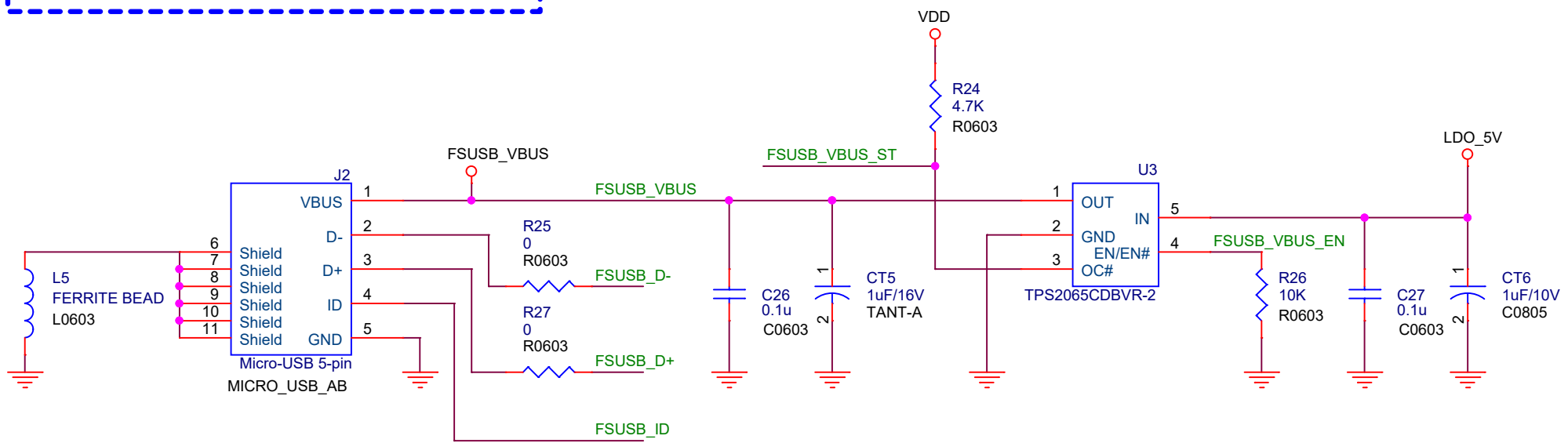
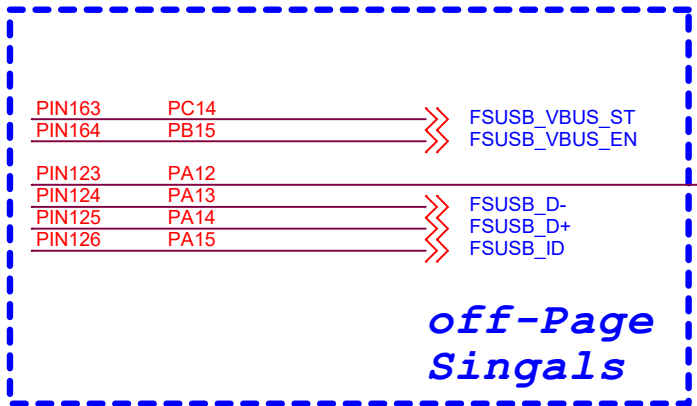
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SPI Flash

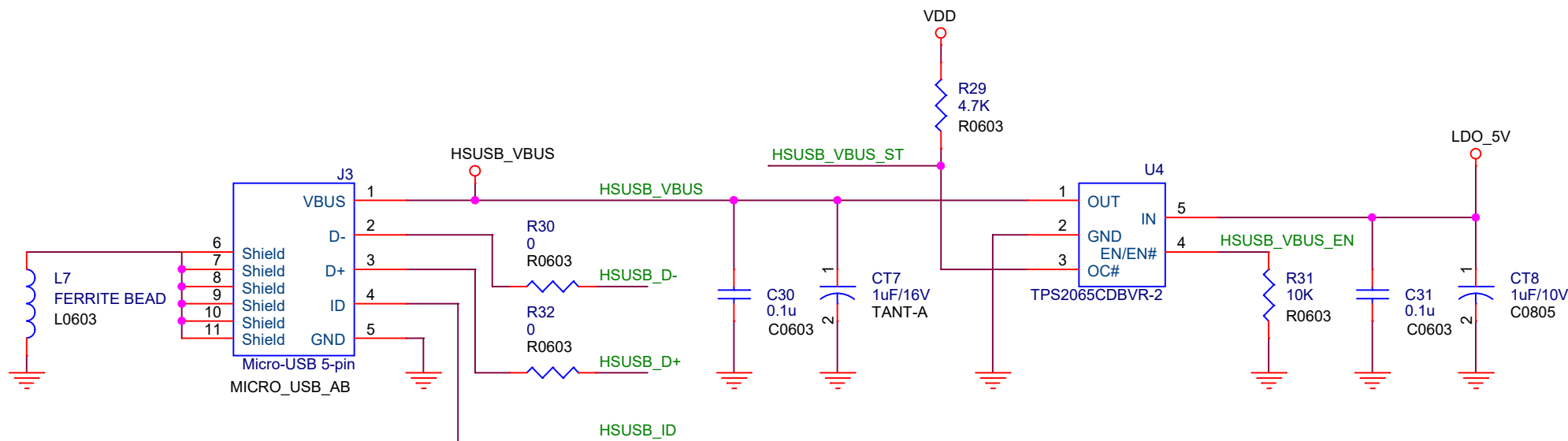
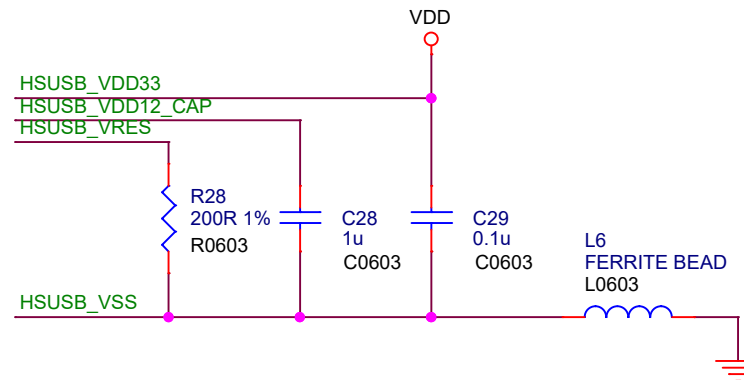
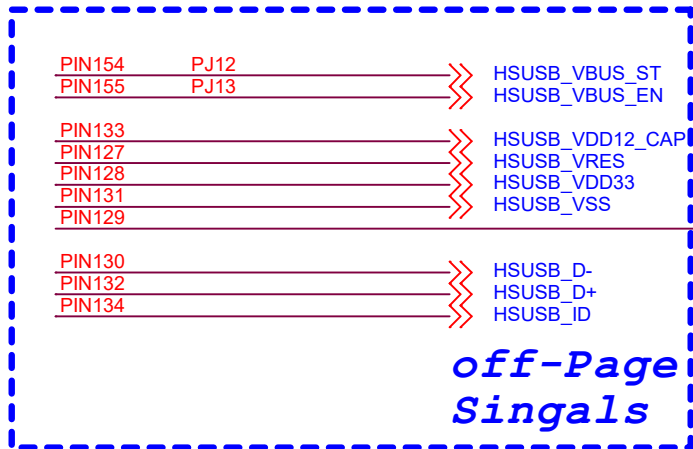
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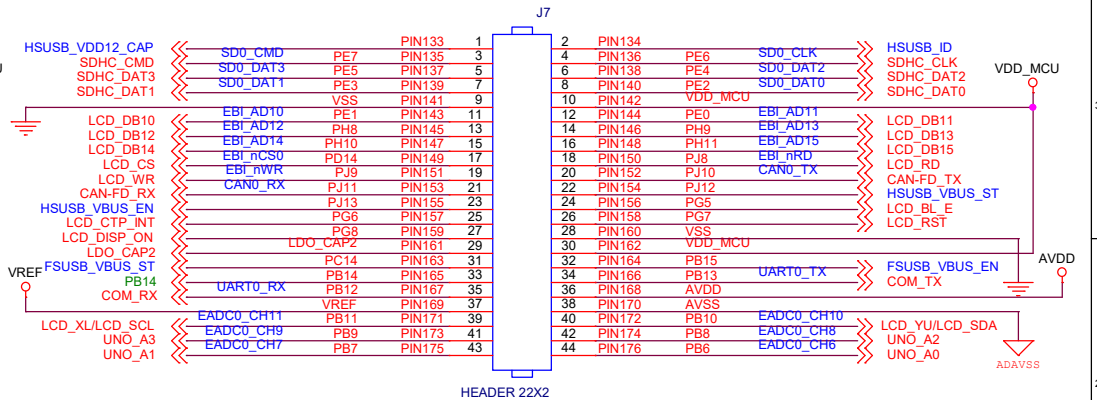
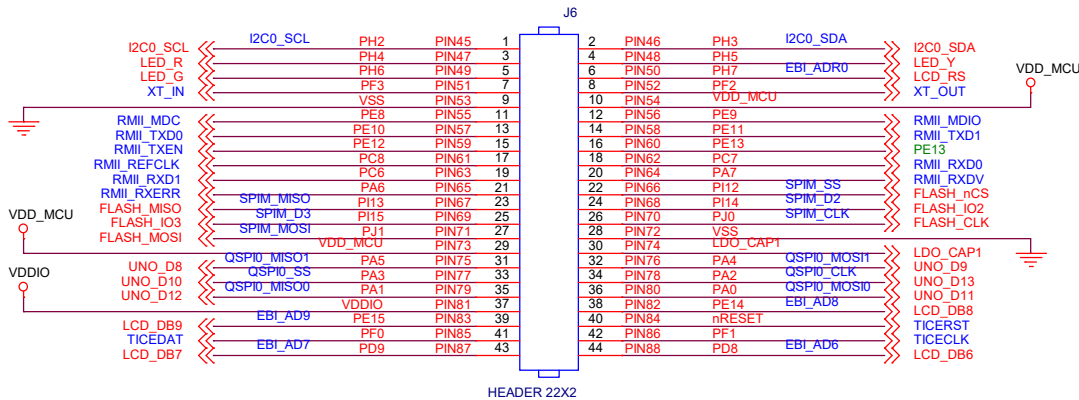
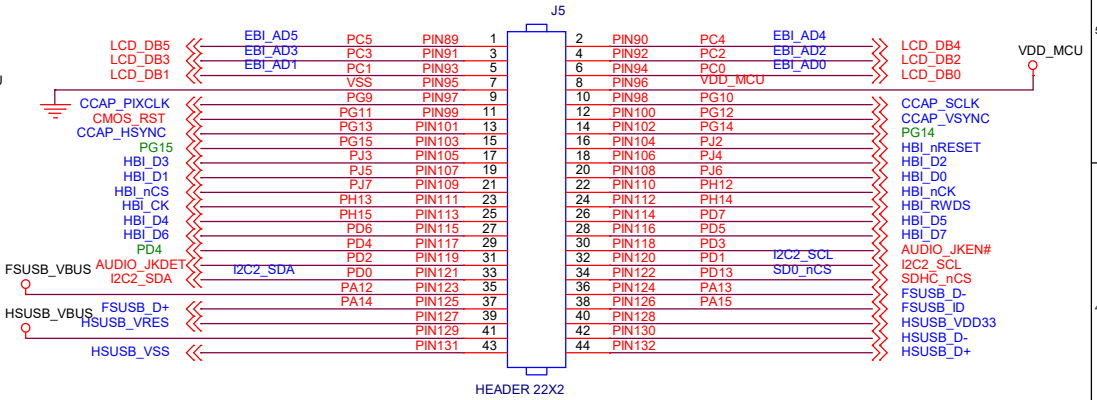
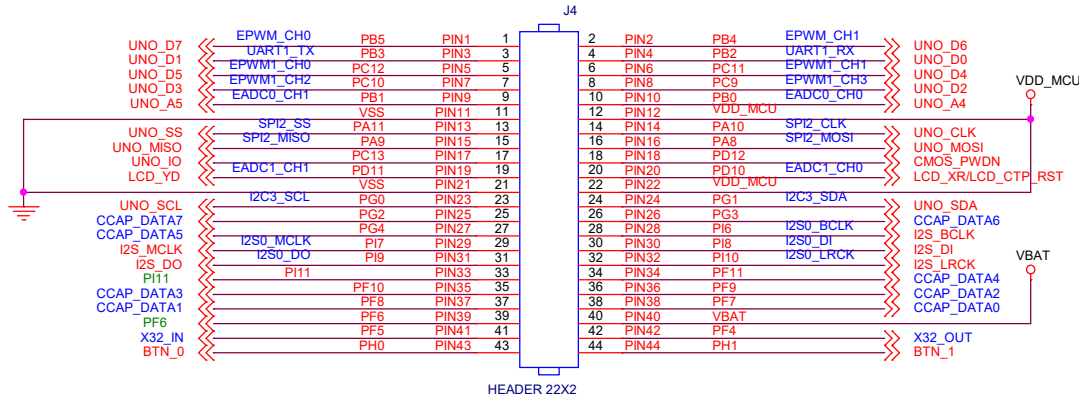
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PIN176	PB6	EADC0_CH6	UNO_A0
PIN175	PB7	EADC0_CH7	UNO_A1
PIN174	PB8	EADC0_CH8	UNO_A2
PIN173	PB9	EADC0_CH9	UNO_A3
PIN10	PB0	EADC0_CH0	UNO_A4
PIN9	PB1	EADC0_CH1	UNO_A5

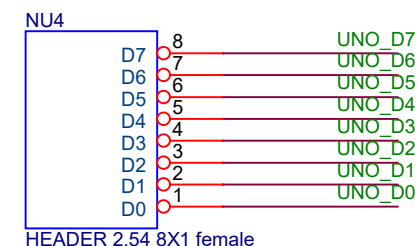
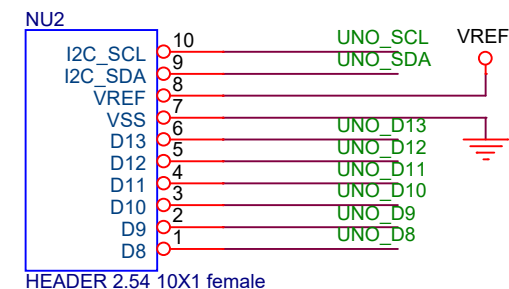
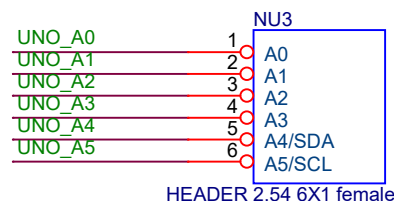
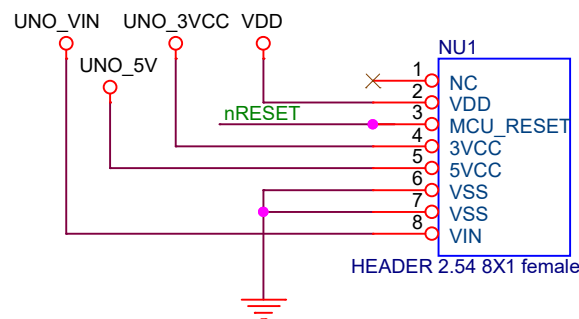
PIN4	PB2	UART1_RX	UNO_D0
PIN3	PB3	UART1_TX	UNO_D1
PIN8	PC9	EPWM1_CH3	UNO_D2
PIN7	PC10	EPWM1_CH2	UNO_D3
PIN6	PC11	EPWM1_CH1	UNO_D4
PIN5	PC12	EPWM1_CH0	UNO_D5
PIN2	PB4	EPWM_CH1	UNO_D6
PIN1	PB5	EPWM_CH0	UNO_D7
PIN75	PA5	QSPI0_MISO1	UNO_D8
PIN76	PA4	QSPI0_MOSI1	UNO_D9
PIN77	PA3	QSPI0_SS	UNO_D10
PIN80	PA0	QSPI0_MOSI0	UNO_D11
PIN79	PA1	QSPI0_MISO0	UNO_D12
PIN78	PA2	QSPI0_CLK	UNO_D13

PIN23	PG0	I2C3_SCL	UNO_SCL
PIN24	PG1	I2C3_SDA	UNO_SDA

PIN13	PA11	SPI2_SS	UNO_SS
PIN14	PA10	SPI2_CLK	UNO_CLK
PIN15	PA9	SPI2_MISO	UNO_MISO
PIN16	PA8	SPI2_MOSI	UNO_MOSI
PIN17	PC13		UNO_IO

PIN84	nRESET	TICERST
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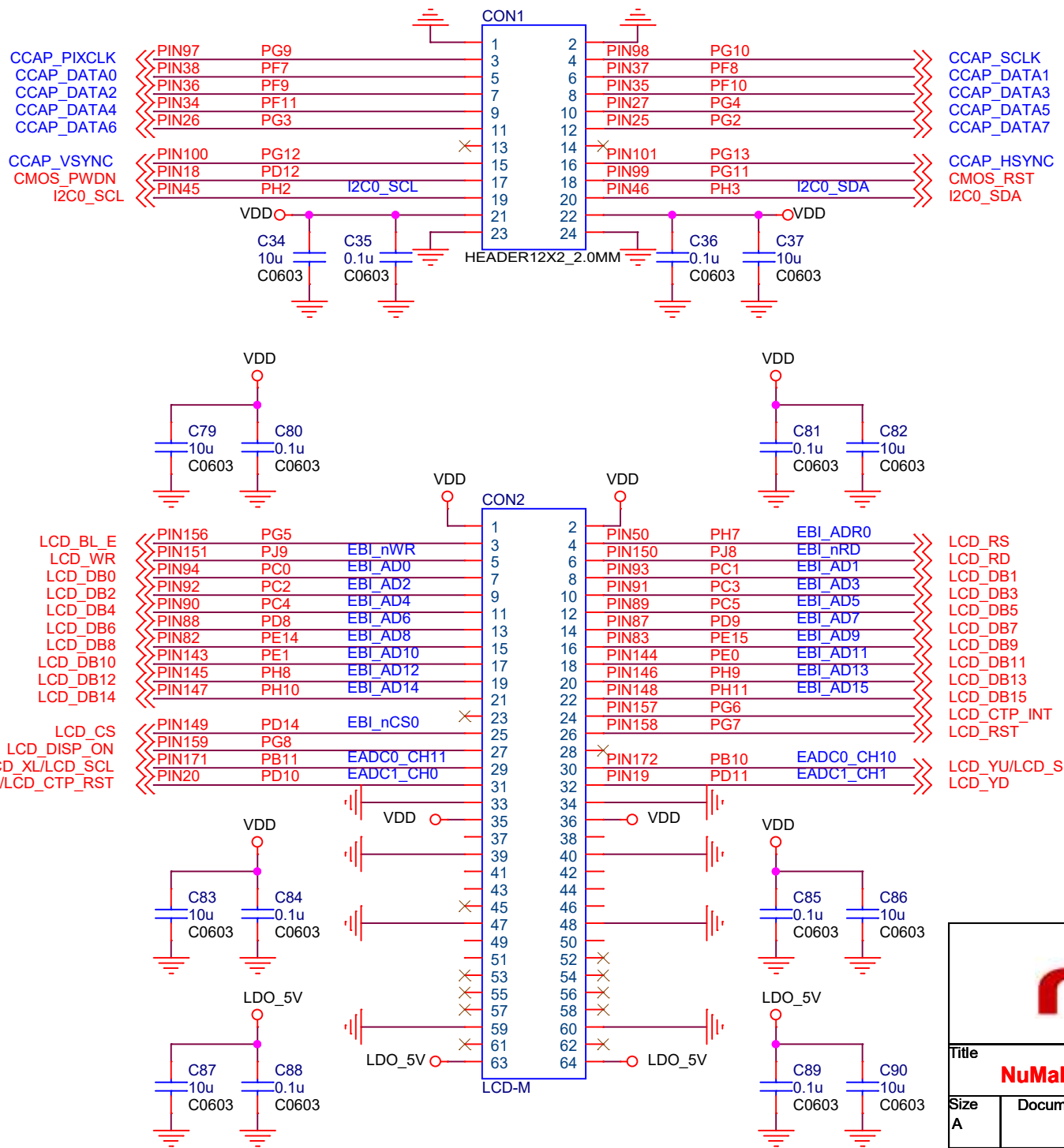
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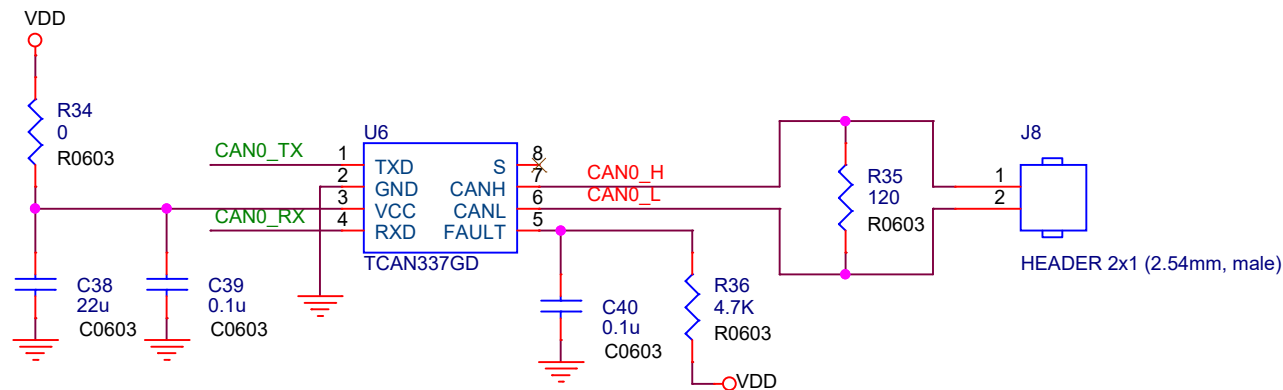
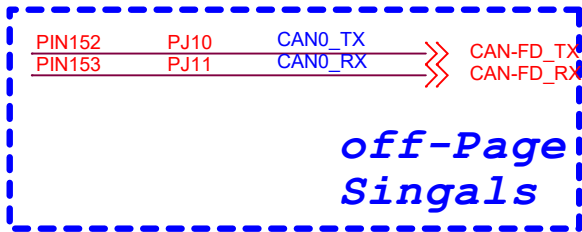
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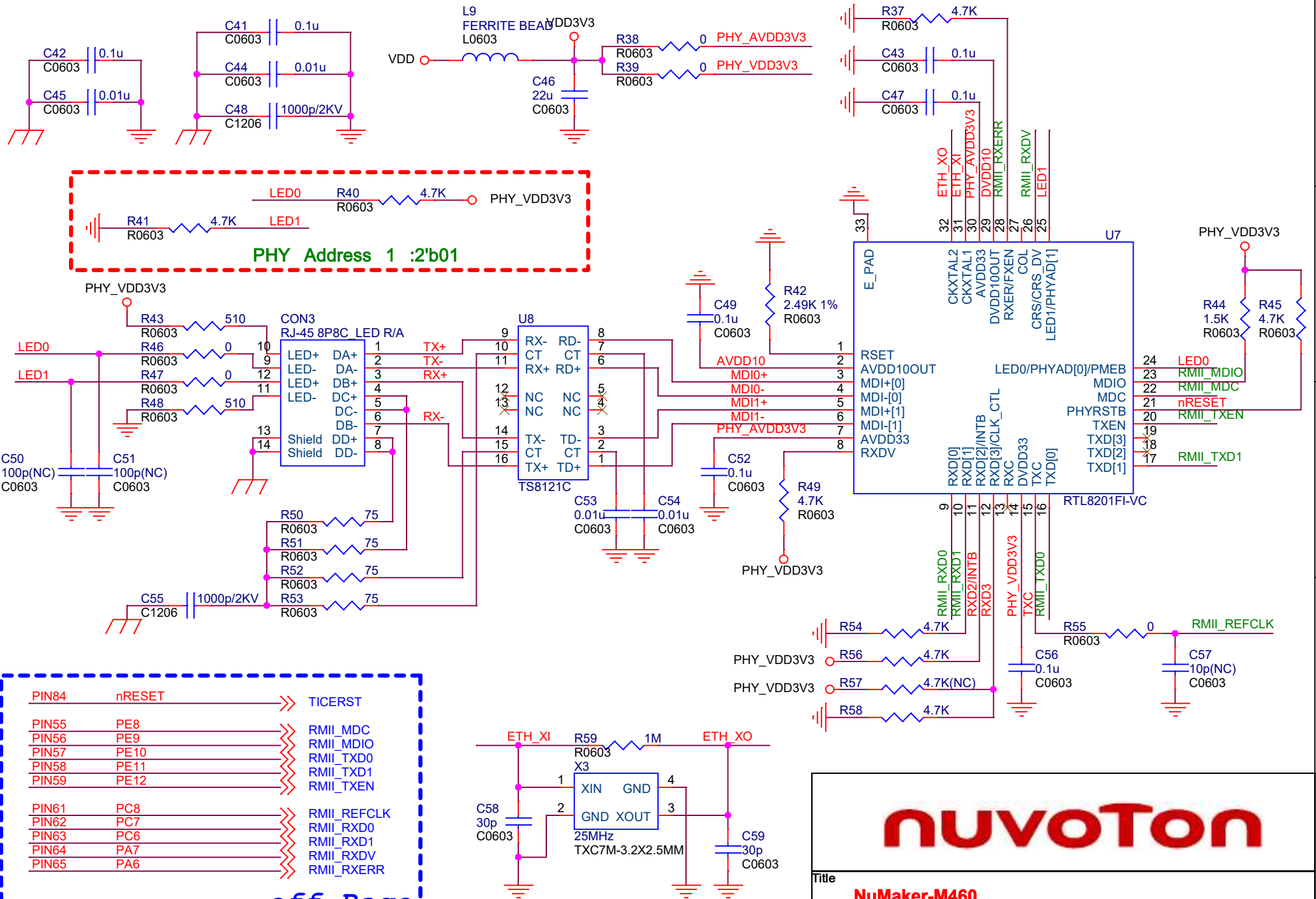
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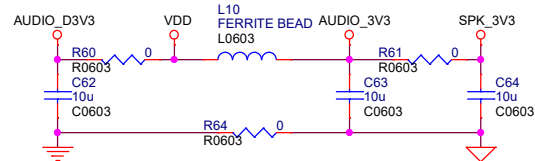
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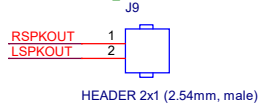
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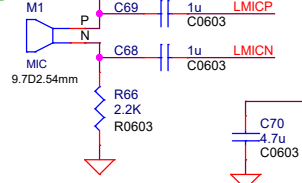




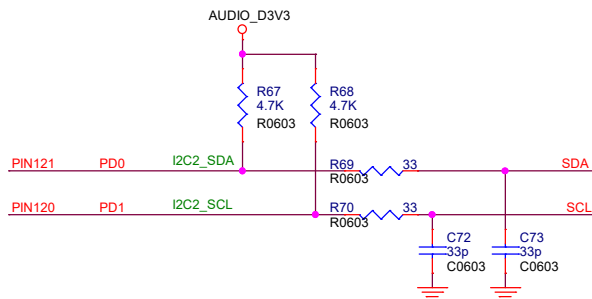
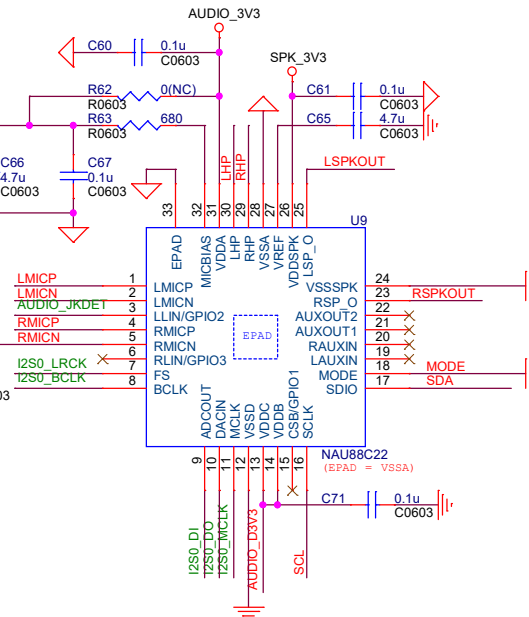
Speaker



Microphone

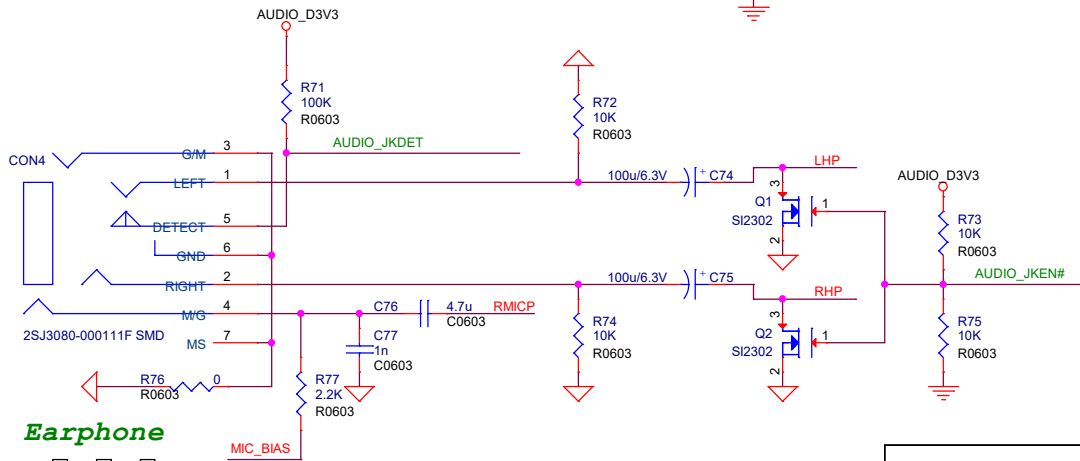


Codec

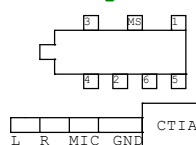


PIN28	PI6	I2S0_BCLK	I2S_BCLK
PIN29	PI7	I2S0_MCLK	I2S_MCLK
PIN30	PI8	I2S0_DI	I2S_DI
PIN31	PI9	I2S0_DO	I2S_DO
PIN32	PI10	I2S0_LRCK	I2S_LRCK
PIN121	PD0	I2C2_SDA	I2C2_SDA
PIN120	PD1	I2C2_SCL	I2C2_SCL
PIN119	PD2	AUDIO_JKDET	AUDIO_JKDET
PIN118	PD3	AUDIO_JKEN#	AUDIO_JKEN#

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Earphone

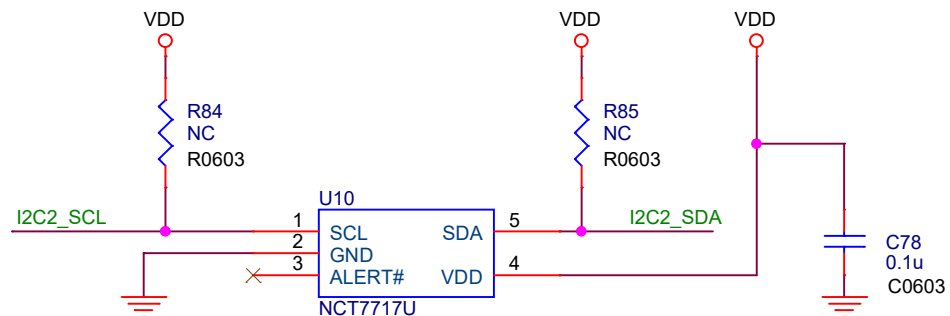


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PIN121 PD0 I2C2_SDA
 PIN120 PD1 I2C2_SCL
 The thermal sensor (NCT7717U) and
 audio codec (NAU88C22) are shared
 with the same I2C bus (I2C2)

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