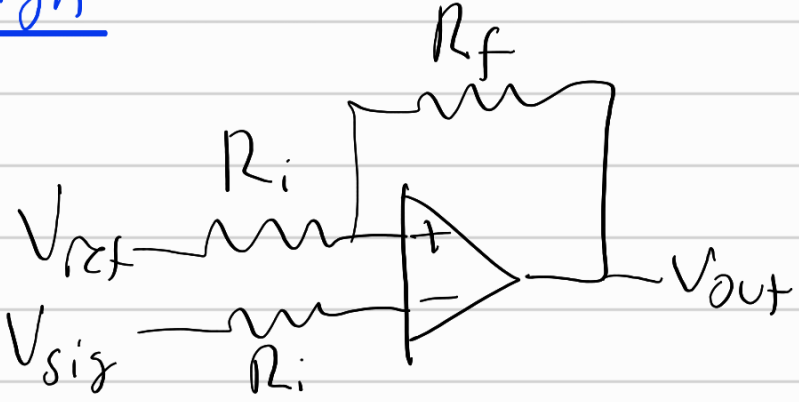
Design



$$V_{Hyst} = \frac{R_i}{R_i + R_f}(V_{OH} - V_{OL})$$

$$= \frac{100}{100 + 1M}(6.76 + 7.68)$$

$$= 0.00144$$

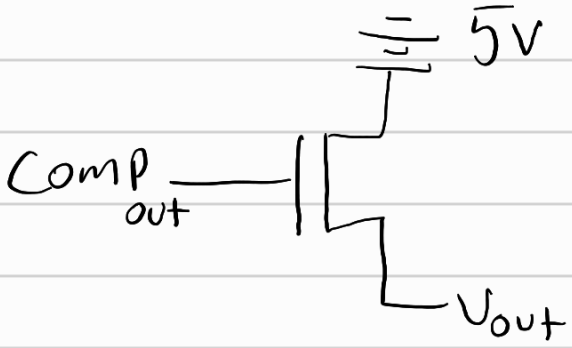
$S/H > DAC = 6.76v$

$S/H < DAC = -7.68v$

Design pt. 2

Outputs are not close enough to 5v or 0v for S/H

Solution: Use comparator output as gate voltage for NMOS with 5 volt supply.



Testing

Target Value	Measured value
100Ω	98.3Ω
1MΩ	.989MΩ
5V	4.58V
0V	0.64mV