DCO Final project - Documentation

All the files are in Intel's environment on micron under two paths:

1. /project/intel16/users/mayashami/ws/dco_project_maya
2. /project/intel16/users/gumerova/ws/dco_project

Here are the relevant files of the project:

The DCO:

Initial simulation's file name: cross_coupled - **path 2**

Views:

- Schematic
- Maestro

Component's file name: DCO – **path 1**

Views:

- Schematic
- Layout

Final simulation's file name: cap_bank_test - **path 1**

Views:

- Schematic
- Maestro
- config

The LC Tank: path 2

Component's file name: lc_tank

Views:

- Schematic
- Layout
- Symbol
- physconfig

The Inductor: path 1

Component's file name: inductor8

Views:

- Schematic
- Layout
- Symbol
- Schematic_nport

Simulation's file name: Test_bench_for_inductor

Views:

- Schematic
- Maestro

Capacitor Bank: path 2

Fine tuning:
Component's file name: diff_switched_cell_v3

Views:

- Schematic
- Layout
- Symbol
- sv_RCC_tttt_25.0

Component's file name: cap_bank_fine

Views:

- Schematic
- Layout
- Symbol

Component's file name: cap_bank_fine_w_logic

Views:

- Schematic
- Layout
- Symbol

Coarse tuning:

Component's file name: diff_switched_cell_coarse

Views:

- Schematic
- Layout
- Symbol

Component's file name: coarse_cell_w_logic

Views:

- Schematic
- Layout
- Symbol
- sv_RCC_tttt_25.0

Component's file name: cap_bank_coarse_w_logic

Views:

- Schematic
- Layout
- Symbol

Simulation's file name: diff_switched_cell_sim

Views:

- Schematic
- Maestro

The capacitor bank

Component's file name: cap_bank

Views:

- Schematic
- Layout
- Symbol
- sv_RCC_tttt_25.0

Capacitor Matrix: path 1

Single capacitor:
Component's file name: cap_matrix_for_symbol

Views:

- Schematic
- Layout
- Symbol
- Schematic_nport

Simulation's file name: test_cap_matrix_for_symbol

Views:

- Schematic
- Maestro
- config

Single cell:
Component's file name: schematic_of_a_single_cell_matrix

Views:

- Schematic
- Layout
- Symbol

Simulation's file name: se_switch_cap10

Views:

- Schematic
- Maestro

Single cell with logic:
Component's file name: switch_with_logic

Views:

- Schematic
- Layout
- Symbol

4-cells with logic:

Component's file name: 8_cells_of_matrix

Views:

- Schematic
- Layout
- Symbol
- sv_RCC_tttt_25.0

The Matrix

Component's file name: 256_cell_cap_matrix

Views:

- Schematic
- Layout
- Symbol
- sv_RCC_tttt_25.0

Simulation's file name: Test_bench_for_cap_matrix

Views:

- Schematic
- Maestro
- config

The Amplifier: path 2

Component's file name: GM_Amplifier

Views:

- Schematic
- Layout
- Symbol
- sv_RCC_tttt_25.0

Simulation's file name: amp_test

Views:

- Schematic
- Maestro