

ECE 243S - Computer Organization
February 2026
Lab 6

Coding in C and Audio Input and Output

Due date/time: During your scheduled lab period in the week of February 23, 2026. [Due date is not when Quercus indicates, as usual.]

Learning Objectives

The goal of this lab is to become comfortable with using the C programming language to do “embedded system” input/output, which you have been doing up to now using the Nios V Assembly Language. We will begin by doing something very similar to previous labs, except in the C language. Then we’ll move on to a more complex input/output device: sound input and output. You will see a relationship between concepts needed here and those in two other courses in second year ECE: ECE 216 Signals and Systems, and ECE 231 Introductory Electronics. You’ll be exposed to hardware-level detail required to link audio input and output in software - in lectures and in this lab.

What To Submit

You should hand in the following files prior to the end of your lab period. However, note that the grading takes place in-person, with your TA:

- The C code for Parts I,II, III and IV in the files `part1.c`, `part2.c`, `part3.c` and `part4.c`.

Part I

Recent lectures have described how to access memory-mapped input/output devices in the C language, using C pointers. The other thing that can be helpful here are the the C language bit-level logical operators, which relate to the use of the assembly `and`, `or`, `xor`, `srl`, `sll` instructions:

- logical bit-wise AND is done using the `&` operator
- logical bit-wise OR is done using the `|` operator
- logical bit-wise XOR is done using the `^` operator
- logical bit-wise NOT is done using the `~` operator
- shift left is done using the `«` operator
- shift left is done using the `»` operator

You can see examples of how these are used here: <https://www.programiz.com/c-programming/bitwise-operators>

Also, one other thing: recall that pointer arithmetic in C takes into account the byte-addressability of the item being pointed to, and that item’s size in bytes. If you want to de-reference a pointer to an integer with an offset, the offset must be in words, not bytes. For example, in the following code we add 3 to the base pointer of `KEYs` to access the memory mapped edge capture register at address `0xFF20005C`, rather than `1210` as we did in the related assembly code.

```
volatile int *KEY_BASE = 0xff200050
int edge_cap;

// getting the KEYs edge capture register into the variable edge_cap:

edge_cap = *(KEY_BASE + 3);
```

The task of Part I: Write a C-language program that turns on all ten Red LEDs when button KEY_0 is pressed and released, and turns them off when KEY_1 is pressed and released. The program should run continuously, responding to either one of those Keys being pressed and released. The program should use the edge capture register of the Key pushbuttons parallel port to determine when one of those Keys is activated. You should use your knowledge from the previous labs to make sure this works. **Do not** use interrupts, just use polling on the edge capture register.

Create a new folder to hold your solution for this part and put your code into a file called `part1.c`. In the lab itself, you must use a slightly different Makefile than the one you've use up to now, as we are now compiling a C program. The `files` folder of the release of this lab has the modified Makefile for you to use as well as the file `address_map.h`, which you should use to compile and load your C code in the DE1-SoC processor during the lab. (You should use this Make for all of the parts of this and the next lab, just modifying the source file as usual). Show it working to your TA for grading in the lab. Submit `part1.c` to Quercus before the end of your lab period.

Part II

Write a C-Language program that takes the input from the microphone jack on the DE1-SoC board, and connects it to the output speaker jack on the board, using the audio input and output system described in class. From class, you will know that this involves collecting samples of the input from the input FIFO of the audio interface and sending them to the output FIFO.

You will want to test this program using CPULATOR, no doubt! However, this is one of those times where CPULATOR is not a perfect replication of the hardware. In particular, it isn't capable of using the microphone on your computer to act as the input microphone. So, instead, when you access the samples from the CPULATOR-based input FIFOs, you will get a simple sawtooth wave, as described here: <https://cpulator.01xz.net/doc/>, which says "Audio recording is functional, but will record a sawtooth wave with period 32 samples (250 Hz at 8 kHz sampling rate)." CPULATOR for Nios V uses an 8khz sampling rate, which is the default for the DE1-SoC hardware.

Create a new folder to hold your solution for this part and put your code into a file called `part2.c`. Show it working to your TA for grading in the lab. Submit `part2.c` to Quercus before the end of your lab period.

Part III

Write a C-language program that will generate a square wave to go to the audio output device (no input). Make it so that the frequency of the audio output is changeable (across the audible range from about 100Hz to 2KHz) using the 10 switches on the DE1-Soc to create a fine-grained selection of the frequency.

Create a new folder to hold your solution for this part and put your code into a file called `part3.c`. Show it working to your TA for grading in the lab. Submit `part3.c` to Quercus before the end of your lab period.

Part IV

Write a C-language program, similar to the one in part II, except that it acts as an echo - as if you were speaking/yelling into the a large canyon, as shown in this video: <https://www.youtube.com/watch?v=SCUUu96PYjo> which shows the distinction between "reverb" and "echo." (We want echo in this case). See the lecture notes that

describe, in general, how this might be done. We suggest a delay of roughly 0.4 seconds. You should experiment to find a good damping amount.

Create a new folder to hold your solution for this part and put your code into a file called `part4.c`. Show it working to your TA for grading in the lab. Submit `part4.c` to Quercus before the end of your lab period. Show your TA that you have this program working.