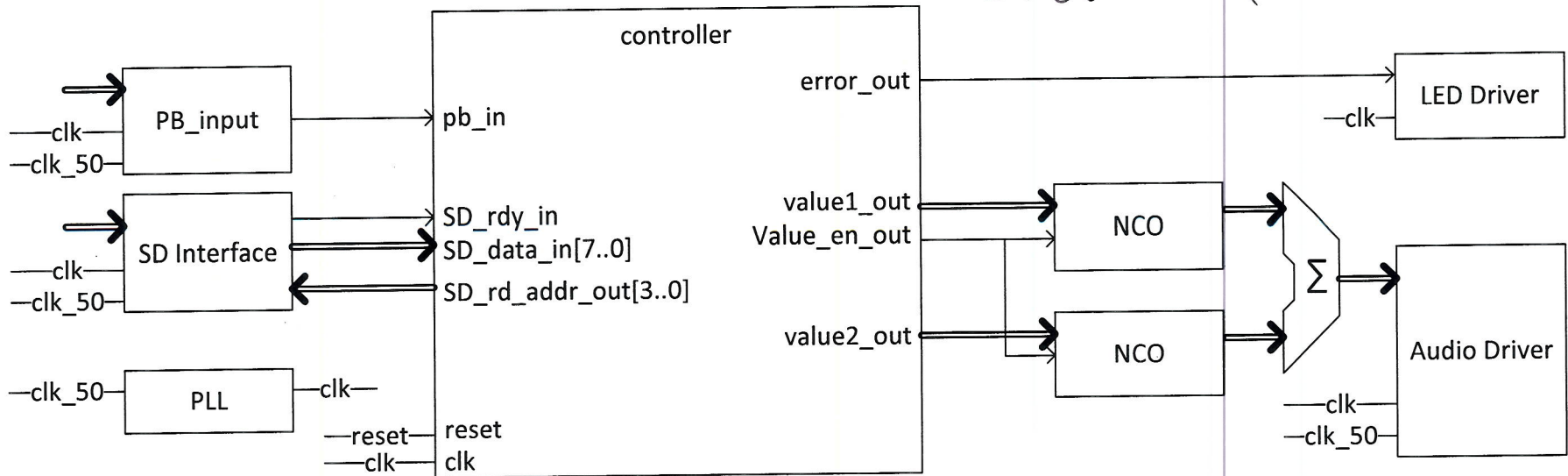


Quiz 12



SD Interface is presented as a ROM:

SD_rd_addr_out	SD_data_in
4'b0000	1
4'b0001	8
4'b0010	0
4'b0011	0
4'b0100	5
4'b0101	5
4'b0110	6
4'b0111	1
4'b1000	7
4'b1001	4
4'b1010	3

Must hold value1_out and value2_out for 250 clocks.

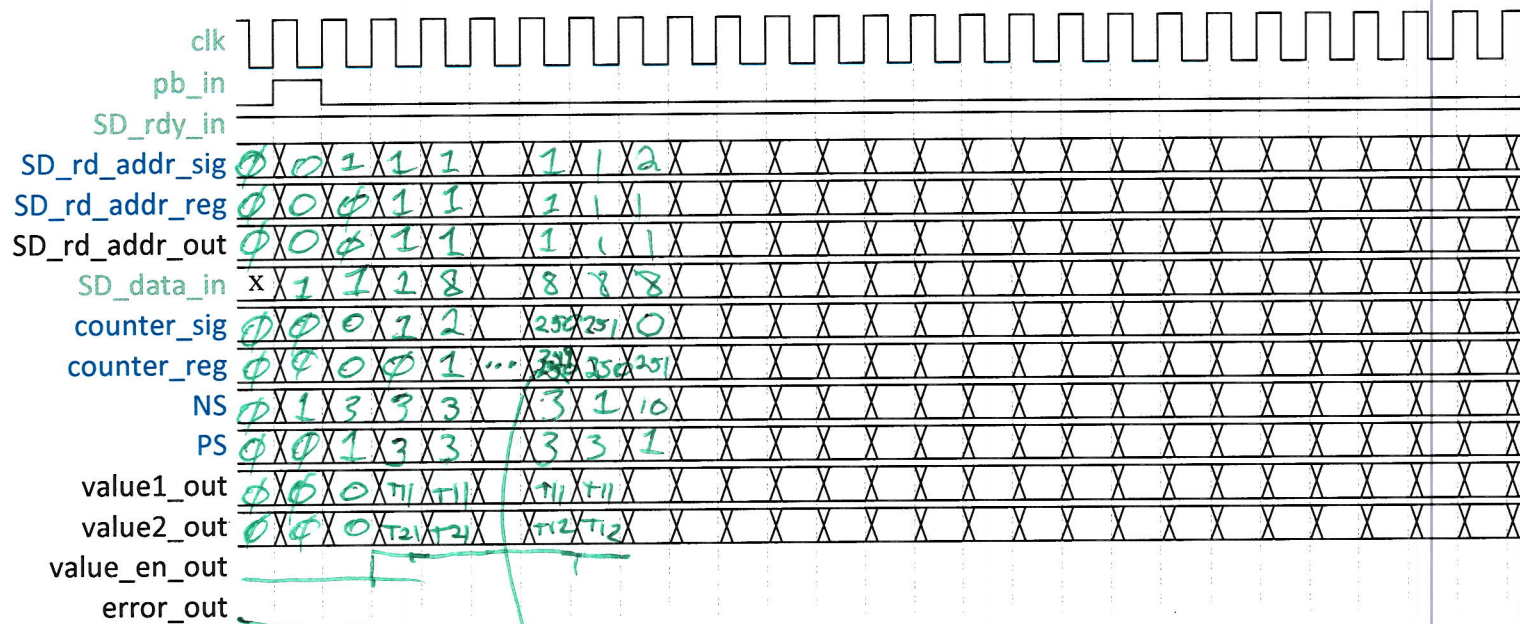
GIVEN

TONE1_0 THROUGH TONE1_9
TONE2_0 THROUGH TONE2_9

FIND

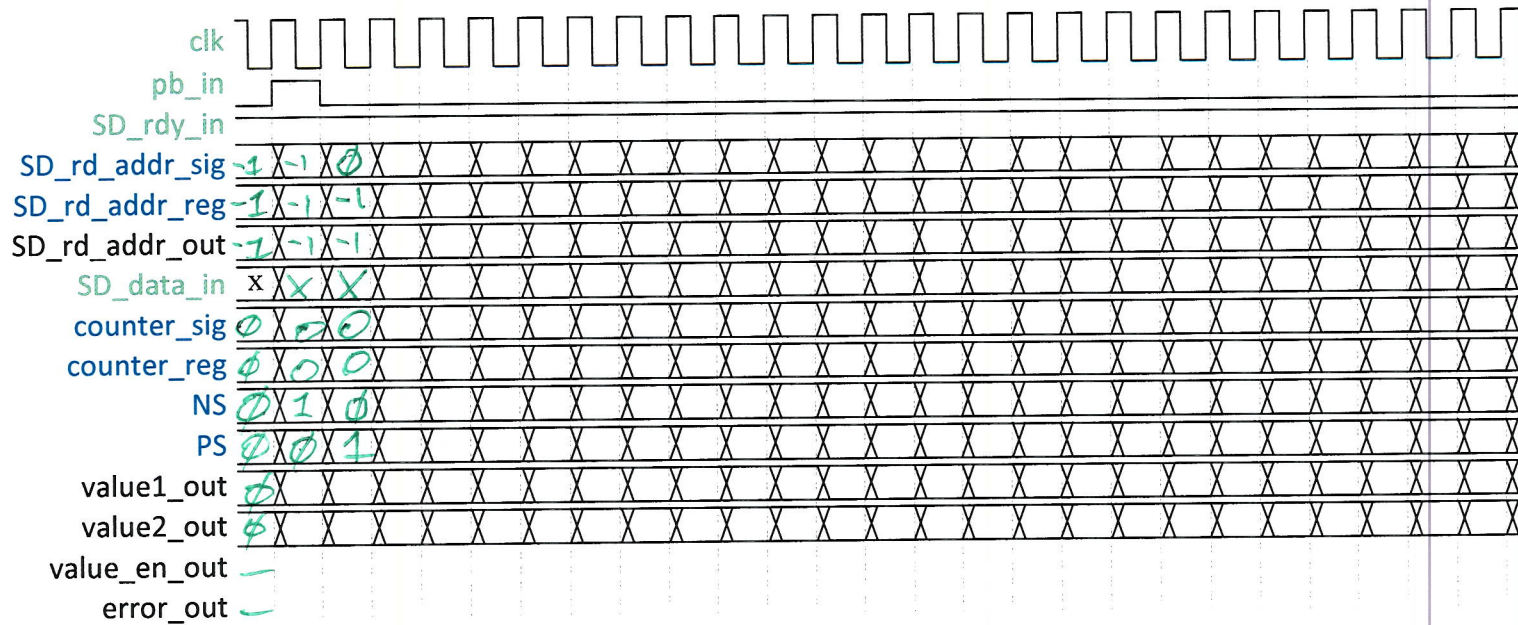
→ ASM AND COMPLETE THE ATTACHED TIMING DIAGRAM.

→ DOES YOUR ASM WORK? WHY?



THIS DID NOT WORK
BECAUSE IT HELD
THE OUTPUT HIGH FOR
251 CLOCKS INSTEAD
OF 250.

Quiz 14



$SD_RD_ADDR_OUT = REG.$

THIS DID NOT
WORK BECAUSE
OF THE STARTING
VALUE OF RD-ADDR-REG
THE SD-