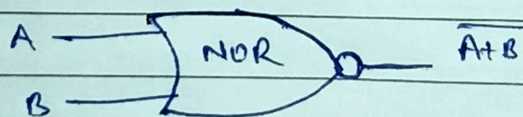# NOR GATE

The NOR Gate is a digital logic gate that implements logical NOR. It behaves according to the truth table. A High output

(1) results if both the inputs to the gate are low (0); if one or both input is High (1), a low output (0) results. NOR is the result of the negation of the OR operator. It can also in some senses be seen as the inverse of an AND gate. NOR is functionally complete operation — NOR gate can be combined to generate any other logical function. It shares this property with the NAND gate. OR operator is monotonic as it can only change Low to HIGH but not vice versa.

A —
B —
NOR
$\overline{A+B}$

Truth Table :-

Input		Output
A	B	A NOR B
0	0	1
0	1	0
1	0	0
1	1	0