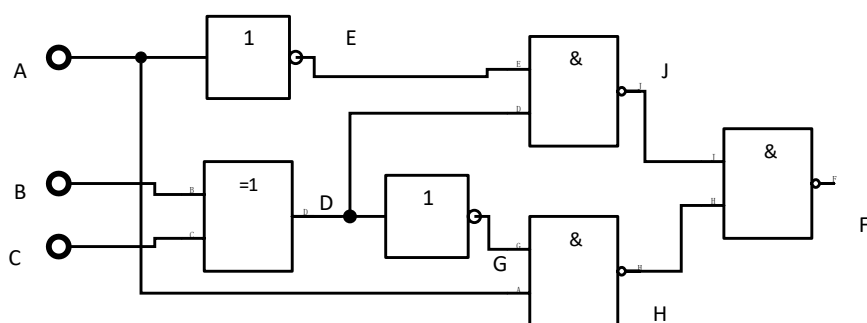


模拟电路与数字电路

第九章作业

9.2 分析如图所示组合电路的逻辑功能。



$$F = \overline{JH} = \overline{J} + \overline{H} = ED + AG = \overline{A}(B \oplus C) + A\overline{D} = \overline{A}(B \oplus C) + \overline{AB} \oplus \overline{C} = A \oplus B \oplus C$$

功能为 3 输入异或门。

9.3 试用与非门设计一个 8421BCD 码 N 的监视器。如果 $N \geq 4$ ，监视器输出为 1，否则为 0。

解：真值表

N=ABCD				Y
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
-	-	-	-	-

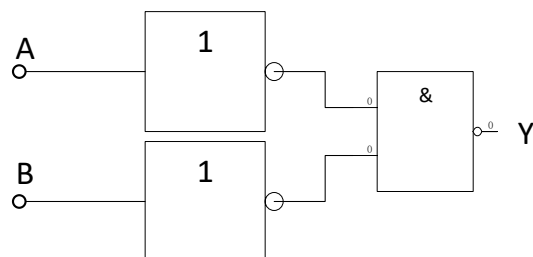
卡诺图：

ABCD	00	01	11	10
00	0	0	0	0
01	1	1	1	1
11	-	-	-	-
10	1	1	-	-

逻辑表达式

$$Y = A + B = \overline{\overline{A}\overline{B}}$$

逻辑图：

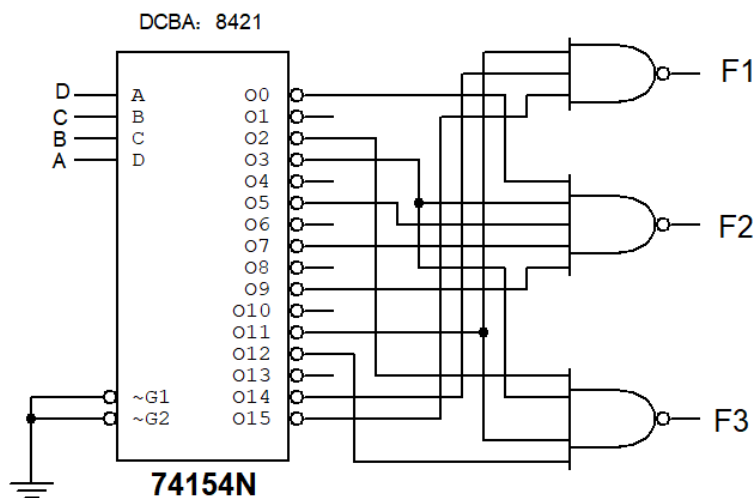


9.8 试用一片 4-16 线译码器 74154 和适当的门电路实现一下多输出逻辑函数。

$$(1) F_1 = \overline{A}\overline{B}C + A\overline{C}D = \overline{A}\overline{B}C\overline{D} + \overline{A}\overline{B}CD + A\overline{B}\overline{C}D + A\overline{B}CD = \Sigma m(2,3,9,13)$$

$$F_2 = \Sigma m(1,3,5,7,9)$$

$$F_3 = \Pi M(0,1,4 \sim 10,13 \sim 15) = \Sigma m(2,3,11,12)$$



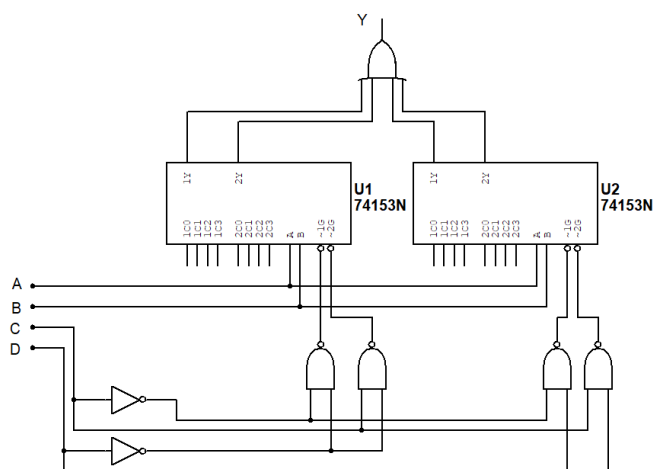
$$(2) F_1 = \Pi M(1,3,4,5,7,8,9,10,12,14) = \Sigma m(0,2,6,11,13,15)$$

$$F_2 = \Sigma m(2,7,10,13)$$

$$F_3 = B\bar{C}\bar{D} + A\bar{B}D = ABC\bar{D} + \bar{A}BC\bar{D} + A\bar{B}CD + A\bar{B}CD = \Sigma m(6,9,11,14)$$

图略

9.11 试用两片双 4 选 1 数据选择器 74153 和少许门电路，接成一个 16 选 1 数据选择器。



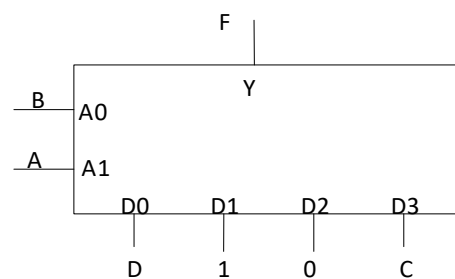
9.13 试用 4 选 1 数据选择器实现下面各函数

$$(3) F(A, B, C, D) = \Sigma m(1,3,4,5,14,15) + \Sigma d(6,7,10,11,12)$$

解：卡诺图

ABCD	00	01	11	10
00	0	1	1	0
01	1	1	X	X
11	X	0	1	1
10	0	0	X	X

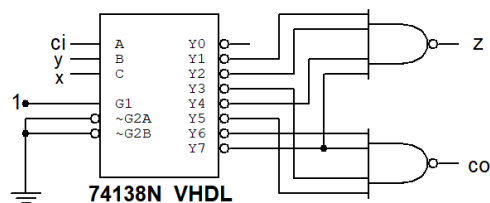
$$F = \bar{A}\bar{B}D + \bar{A}B + ABC$$



9.14 试用 3 线-8 线译码器 74138 实现一位全加器。

解：全加器真值表。输入 x, y, ci 输出本位和 $z = m_1 + m_2 + m_4 + m_7$, $co = m_3 + m_5 + m_6 + m_7$ z, co 。

X	Y	Ci	Z	co
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1



9.16 试用 1 片双 4 选 1 数据选择器 74153 和少量门电路实现一位全减器。

解：全减器真值表。被减数 x , 减数 y , 借位 $z = m_1 + m_2 + m_4 + m_7 =$

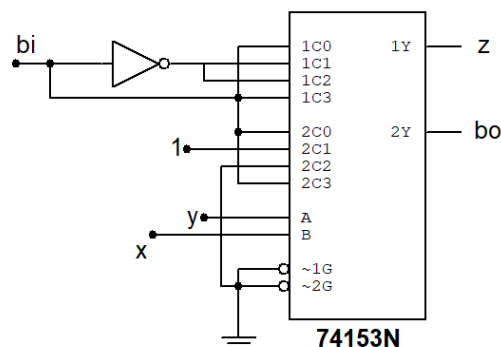
输入 bi , 本位差, 借位输出 bo

X	Y	bi	Z	bo
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

$$z = m_1 + m_2 + m_4 + m_7 = \bar{x}\bar{y}b_i + \bar{x}y\bar{b}_i + x\bar{y}\bar{b}_i + xyb_i$$

$$bo = m_1 + m_2 + m_3 + m_7 = \bar{x}\bar{y}b_i + \bar{x}y\bar{b}_i + \bar{x}yb_i + xyb_i$$

$$= \bar{x}\bar{y}b_i + \bar{x}y + xyb_i$$



9.17 试写出题图 9-17 所示电路的输出 F 的最小项之和表达式。

$$s = a \oplus b \oplus 1 = a \oplus (\bar{b} \cdot 1 + b \cdot \bar{1}) = a \oplus \bar{b} = \bar{a}\bar{b} + ab$$

$$c0 = (a \oplus b)CI + ab = \bar{a}b + a\bar{b} + ab = a + b$$

$$S \oplus c0 = (\bar{a}\bar{b} + ab) \oplus (a + b) = (\bar{a}\bar{b} + ab) \oplus \overline{\bar{a}\bar{b}} = (\bar{a}\bar{b} + ab)(\bar{a}\bar{b}) + \overline{\bar{a}\bar{b} + ab} \cdot \bar{a}\bar{b}$$

$$= \bar{a}\bar{b} + \overline{\bar{a}\bar{b} + ab} \cdot \bar{a}\bar{b} = \bar{a}\bar{b} + \overline{\bar{a}\bar{b} + ab} = \bar{a}\bar{b} + \overline{\bar{a}\bar{b}} \cdot \overline{ab} = \bar{a}\bar{b} + \overline{\bar{a}\bar{b}} = \bar{a}\bar{b} + a + b = \bar{a} + \bar{b} = \overline{ab}$$

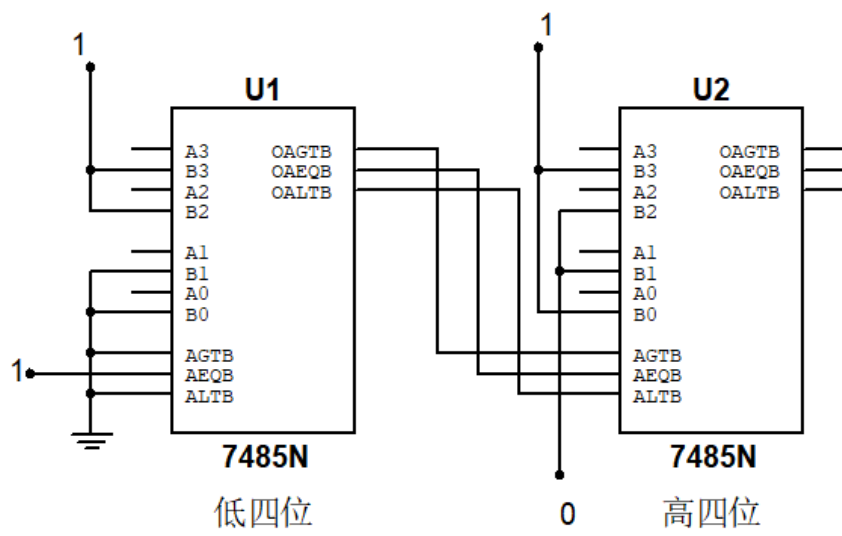
$$D_0 = ab, D_1 = \bar{a}\bar{b}, D_2 = a + b, D_3 = \overline{a + b} = \bar{a}\bar{b}$$

$$F = ab\bar{c}\bar{d} + (\bar{a} + \bar{b})\bar{c}\bar{d} + (a + b)c\bar{d} + \bar{a}\bar{b}cd = ab\bar{c}\bar{d} + \bar{a}\bar{c}\bar{d} + \bar{b}\bar{c}\bar{d} + ac\bar{d} + bc\bar{d} + \bar{a}\bar{b}cd$$

$$F = \Sigma m(12, 1, 5, 1, 9, 10, 14, 6, 14, 3) = \Sigma m(1, 3, 5, 6, 9, 10, 12, 14)$$

9.20 试用 4 位二进制数值比较器 7485 实现一个判断 8 位二进制数大于、等于或小于 156 的逻辑电路。

解：(156) D=(1001 1100)B



B=1001 1100