

## 1. Description

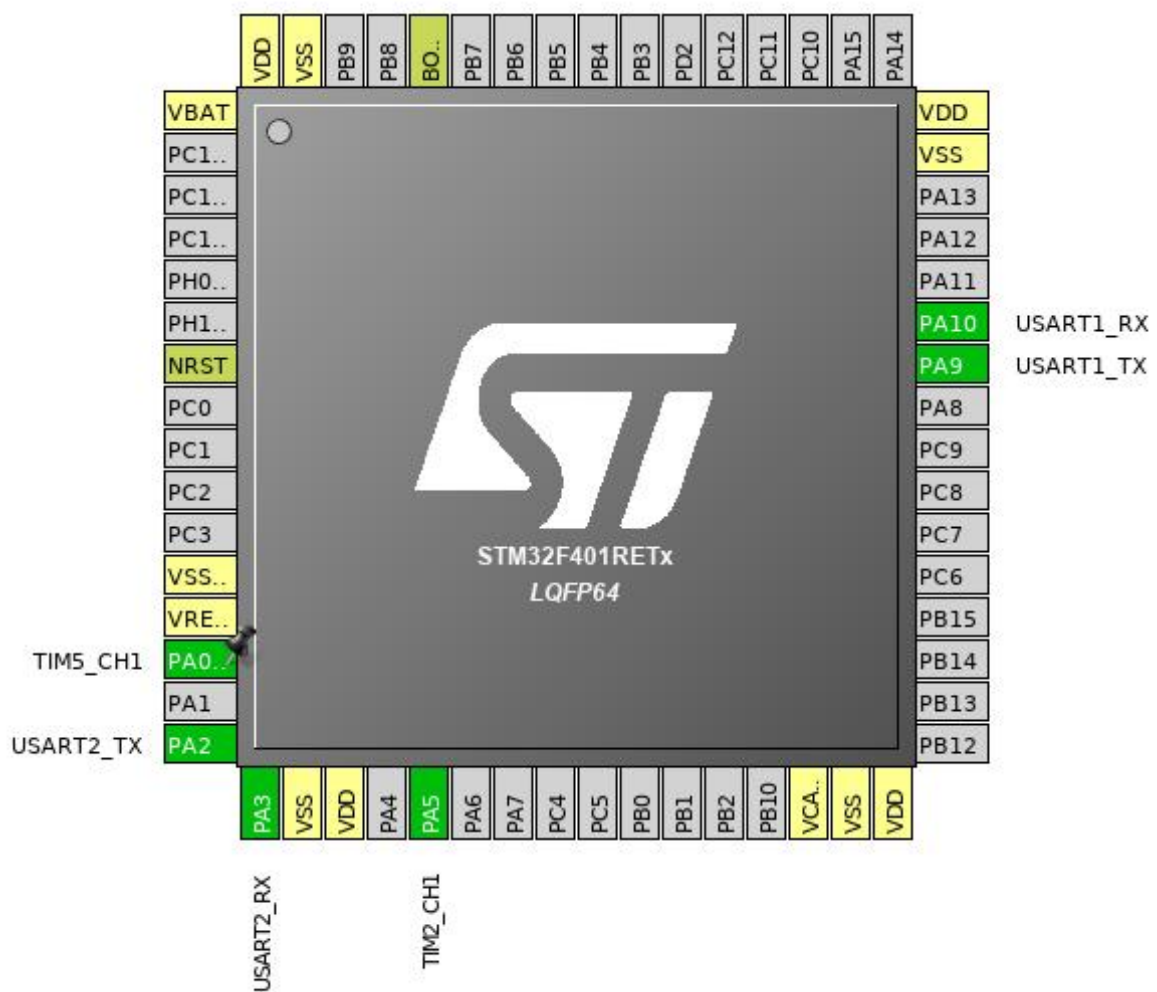
### 1.1. Project

Project Name	TP1 stm
Board Name	TP1 stm
Generated with:	STM32CubeMX 4.23.0
Date	01/05/2018

### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F401
MCU name	STM32F401RETx
MCU Package	LQFP64
MCU Pin number	64

## 2. Pinout Configuration



### 3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
12	VSSA/VREF-	Power		
13	VREF+	Power		
14	PA0-WKUP	I/O	TIM5_CH1	
16	PA2	I/O	USART2_TX	
17	PA3	I/O	USART2_RX	
18	VSS	Power		
19	VDD	Power		
21	PA5	I/O	TIM2_CH1	
30	VCAP1	Power		
31	VSS	Power		
32	VDD	Power		
42	PA9	I/O	USART1_TX	
43	PA10	I/O	USART1_RX	
47	VSS	Power		
48	VDD	Power		
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		



## 5. IPs and Middleware Configuration

### 5.1. SYS

Timebase Source: SysTick

### 5.2. TIM2

Clock Source : Internal Clock

Channel1: PWM Generation CH1

#### 5.2.1. Parameter Settings:

##### Counter Settings:

Prescaler (PSC - 16 bits value)	1080-1 *
Counter Mode	Up
Counter Period (AutoReload Register - 32 bits value )	2000-1 *
Internal Clock Division (CKD)	No Division

##### Trigger Output (TRGO) Parameters:

Master/Slave Mode	Disable (no sync between this TIM (Master) and its Slaves
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

##### PWM Generation Channel 1:

Mode	PWM mode 1
Pulse (32 bits value)	0
Fast Mode	Disable
CH Polarity	High

### 5.3. TIM5

Channel1: Input Capture direct mode

#### 5.3.1. Parameter Settings:

##### Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 32 bits value )	4294967295 *
Internal Clock Division (CKD)	No Division

#### Trigger Output (TRGO) Parameters:

Master/Slave Mode	Disable (no sync between this TIM (Master) and its Slaves)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

#### Input Capture Channel 1:

Polarity Selection	<b>Both Edges *</b>
IC Selection	Direct
Prescaler Division Ratio	No division
Input Filter (4 bits value)	0

## 5.4. USART1

### Mode: Asynchronous

#### 5.4.1. Parameter Settings:

##### Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

##### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

## 5.5. USART2

### Mode: Asynchronous

#### 5.5.1. Parameter Settings:

##### Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

##### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

\* User modified value

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
TIM2	PA5	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
TIM5	PA0-WKUP	TIM5_CH1	Alternate Function Push Pull	<b>Pull-up *</b>	Low	
USART1	PA9	USART1_TX	Alternate Function Push Pull	Pull-up	<b>Very High *</b>	
	PA10	USART1_RX	Alternate Function Push Pull	Pull-up	<b>Very High *</b>	
USART2	PA2	USART2_TX	Alternate Function Push Pull	Pull-up	<b>Very High *</b>	
	PA3	USART2_RX	Alternate Function Push Pull	Pull-up	<b>Very High *</b>	

### 6.2. DMA configuration

nothing configured in DMA service



### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
TIM5 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM2 global interrupt	unused		
USART1 global interrupt	unused		
USART2 global interrupt	unused		
FPU global interrupt	unused		

\* User modified value

## ***7. Power Consumption Calculator report***

### 7.1. Microcontroller Selection

Series	STM32F4
Line	STM32F401
MCU	STM32F401RETx
Datasheet	025644_Rev3

### 7.2. Parameter Selection

Temperature	25
Vdd	null

## 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	TP1 stm
Project Folder	/home/mayeul/Documents/5ETI/stm32/TP/TP1 stm
Toolchain / IDE	Makefile
Firmware Package Name and Version	STM32Cube FW_F4 V1.18.0

### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No