Short Quiz on Timers: Group 1 CPE 3201 - EMBEDDED SYSTEMS

Short Quiz on Timers

Due No due date Points 20 Available until Mar 8 at 3:30pm

Questions 20 Time Limit 30 Minutes

Instructions

Choose the best answer from the choices

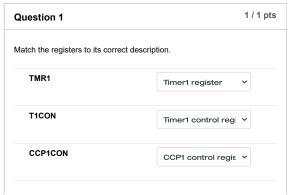
This quiz was locked Mar 8 at 3:30pm.

Attempt History

	Attempt	rime	Score	
LATEST	Attempt 1	30 minutes	15.75 out of 20	

(!) Correct answers are hidden.

Score for this quiz: 15.75 out of 20 Submitted Mar 8 at 3:23pm This attempt took 30 minutes



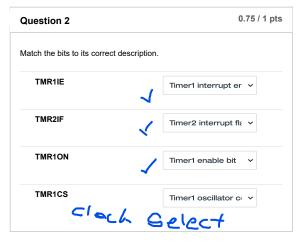
https://usc.instructure.com/courses/17083/quizzes/120268?module_item_id=762746

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Short Quiz on Timers: Group 1 CPE 3201 - EMBEDDED SYSTEMS GIF Global interrupt er 🗸

1 / 1 pts Question 4 If the duty cycle of the output signal generated by the CCP module is 60%at 1000Hz, what should be the value for the CCP1CON<5:4>? Assume F_{OSC} = 4MHz and Timer2 prescaler value of 1:16. $(0.6/1000) (4*10^6)(1/16) = 150 = 100101 10$ 11₂ 012

0 / 1 pts Question 5 What is the event when the timer transitions from 0000h to FFFFh? Match



Question 3	1 / 1 pts				
Match the registers to its correct description.					
TMR2ON	Timer2 enable bit 💙				
PEIE	Peripheral interrup 🗸				
TMR2IE	Timer2 interrupt eı 🗸				

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1 / 1 pts Question 6 Which of the following timers does not generate an interrupt upon overflow? CCP Module Timer1 Timer2 Timer0

Question 7	1 / 1 pts
What is a prescaler?	
A ration of the source clock to the instruction clock cycle.	
A ratio of the source clock to the actual clock of the timer.	
None of the choices.	
A ratio between oscillator frequency to the actual clock of the ti	mer.

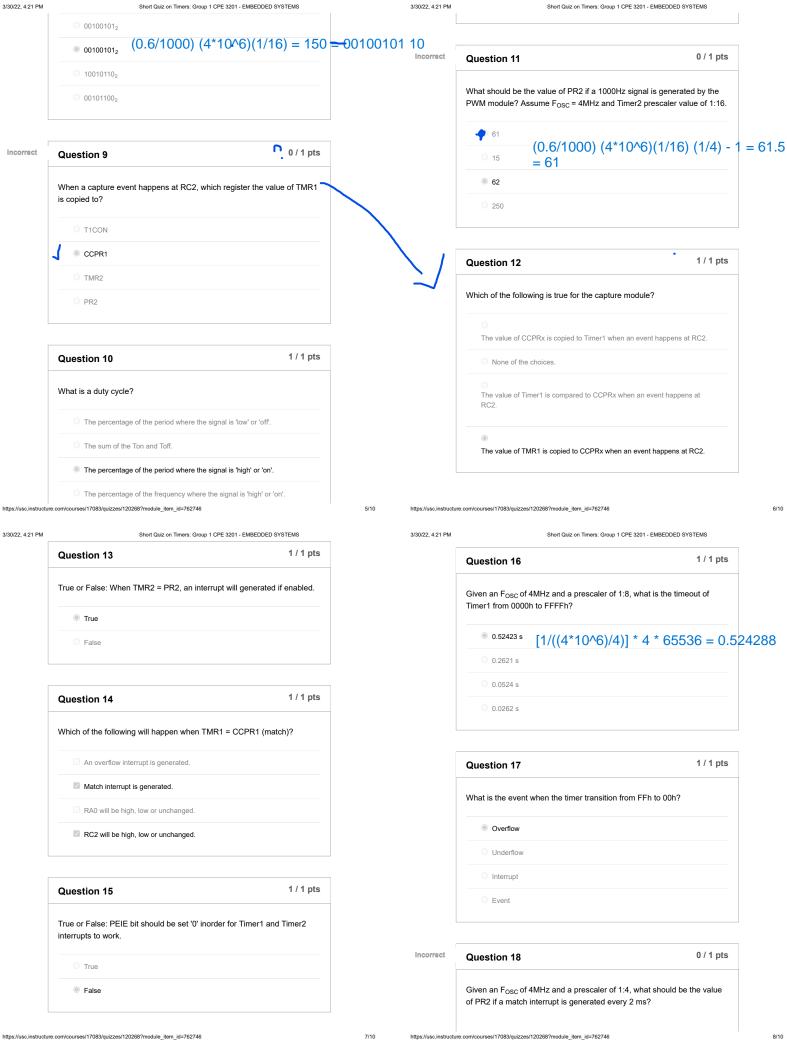
1 / 1 pts **Question 8**

If the duty cycle of the output signal generated by the CCP module is 60% at 1000Hz, what should be the value for the CCPR1L? Assume F_{OSC} = 4MHz and Timer2 prescaler value of 1:16.

Interrupt

Underflow

Overflow





Question 19	1 / 1 pts
Which of the following timers are count up?	
□ PR2	
☑ Timer1	
☑ Timer2	
☑ Timer0	

	Question 20	1 / 1 pts			
	Which of the following registers are used for the PWM resolution?				
	CCPR2L				
	CCP2CON<5:4>				
	CCP1CON<5:4>				
	☑ CCPR1L				
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ttps://usc.instructure.com/courses/17063/quizzes/120266?moutie_item_iu=702740