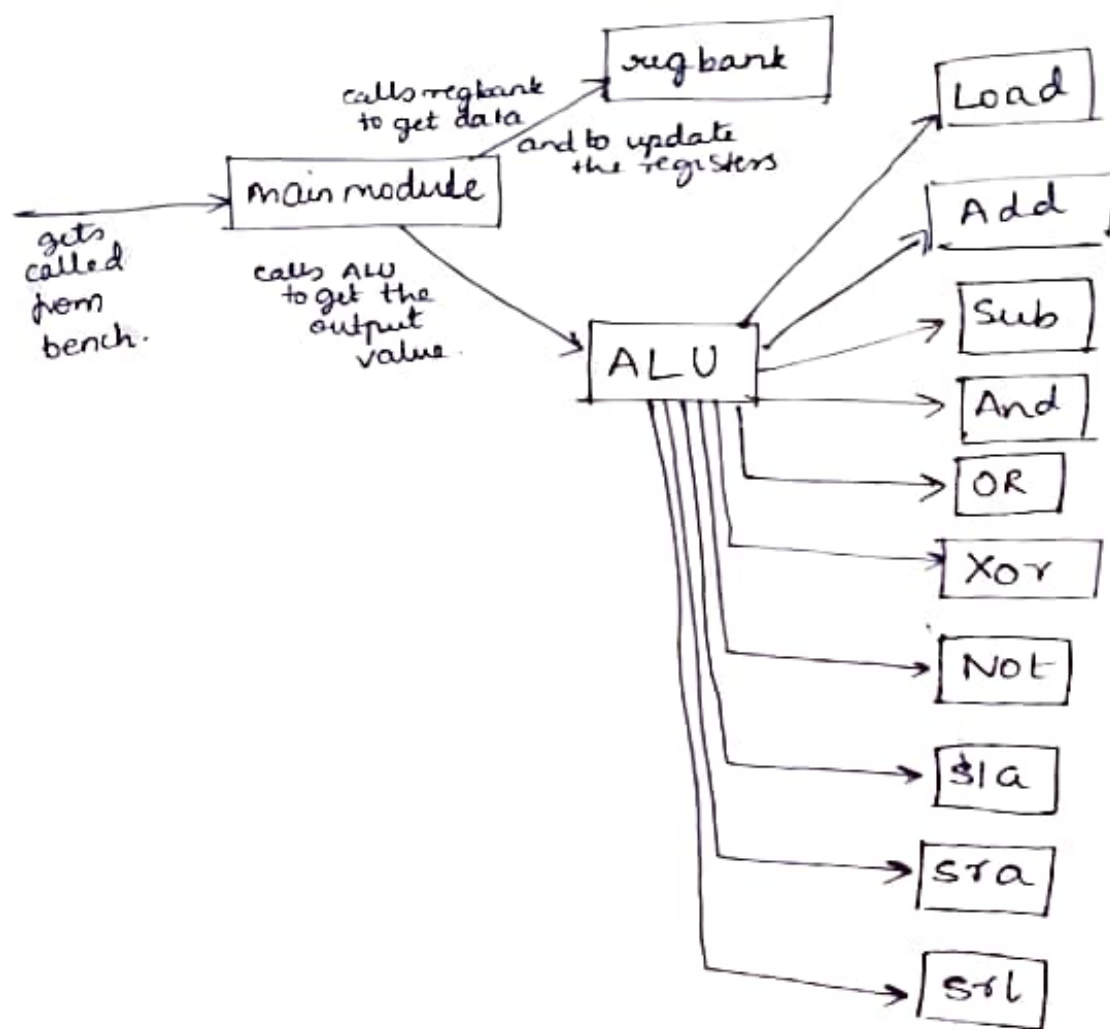
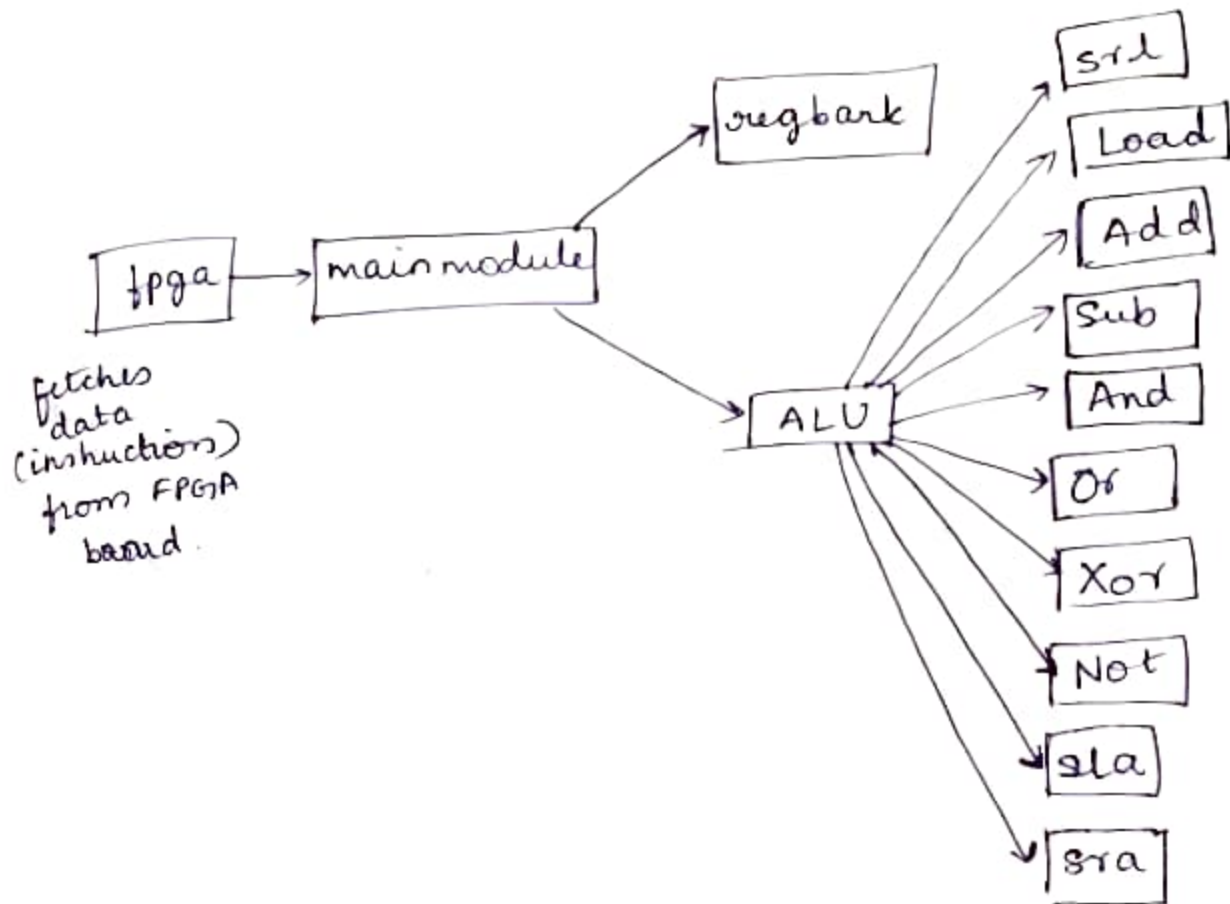


# Simulation



ALU  
calls multiple  
modules  
to get the  
output and  
returns this  
to main module.

# Synthesis



calls multiple modules and returns the output corresponding to the opcode