

# MAYUR S GOWDA

Email: mayur.s.gowda2001@gmail.com | Contact: (412) 287-0338 | LinkedIn: linkedin.com/in/mayur-s-gowda-19236421a

## EDUCATION

<b>Carnegie Mellon University</b> Master of Science in Electrical and Computer Engineering (GPA: 3.80/4.00)	<b>Pittsburgh, PA</b> <b>May 2025</b>
<b>Ramaiah Institute of Technology (RIT)</b> Bachelor of Engineering in Telecommunication Engineering (GPA: 9.74/10)	<b>Bangalore, India</b> <b>August 2022</b>

## RESEARCH

<b>Carnegie Mellon University</b> <b>Graduate Research Assistant</b>	<b>Pittsburgh, PA</b> <b>May 2024 - Present</b>
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### Project: Recovery of Underlying Graph Networks and its Application in Behavioural Classification

- Leading research on behavioral classification problems and the recovery of graph network structures for identified behaviors, involving advanced network analysis and modeling to enhance behavioral pattern recognition.

### Project: Gesture Recognition in Amyotrophic Lateral Sclerosis (ALS) Patients

- Developing and refining gesture recognition systems specifically for ALS patients. Focuses on improving the accuracy and usability of gesture-based communication aids for individuals with motor impairments.

<b>Indian Institute of Science (IISc)</b> <b>Research Intern</b>	<b>Bangalore, India</b> <b>November 2021 - December 2023</b>
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### Project: Sports Analysis using Computer Vision.

- Basketball Analysis: Implemented an object detection system for basketball and hoop identification. Devised algorithms to estimate the release angle using pose estimation, with shot prediction based on ball trajectory. The system was able to achieve an overall accuracy of 86.2% at predicting the shots. **Published in CVIP 2023.**
- Built an algorithm to perform Foot-Ground Contact detection-based Segmentation framework and Computer Vision techniques. The algorithm was able to predict the foot-ground contact instances with an accuracy of 85.17% over the test dataset.

<b>Center for Imaging Technologies, Ramaiah Institute of Technology</b> <b>Research Associate</b>	<b>Bangalore, India</b> <b>July 2022 - December 2023</b>
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- Led a project named Novel in-cabin sensing and analytical systems for enhancing the safety, health, and comfort of drivers and occupants (**Sponsored by STELLANTIS**).
- Built a novel method for the detection of different cognitive states of the driver by combining Feature detection (Yolov5 Algorithm), Head-pose estimation (HPE), and Eye-Gaze Tracking (EGT). The HPE and EGT algorithms were developed using the existing mediapipe framework. The work was conducted using both RGB and IR sensors. An accuracy of around 90% was obtained for the RGB sensors for the proposed method.

<b>Indian Space Research Organization (ISRO, LEOS Lab)</b> <b>Project Trainee</b>	<b>Bangalore, India</b> <b>December 2021 - July 2022</b>
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### Worked on a project entitled, Design and Development of a Controller for Piezo-based Fast Steering Mirror.

- Developed a Verilog code for controlling the Fast-Steering Mirror present in space equipment to establish the line of Sight between two devices communicating with one another. The system was tested and executed in real-time on FPGA.

<b>Ramaiah Institute of Technology</b>	<b>Bangalore, India</b> <b>February 2021 - June 2021</b>
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- Worked on a project entitled Smart Detection and Mitigation of DDoS Attacks. Distributed Denial-of-Service attack is a cybercrime in which the attacker floods a server with internet traffic to prevent users from accessing online services.
- Constructed a system for detecting the DDoS attacks and mitigating those DDoS attacks in real-time attack scenarios. The system was tested in a LAN network and resulted in an attack prediction accuracy of 84%.

## WORK EXPERIENCE

<b>Delta Electronics, Inc.</b> <b>R&amp;D Engineer (Intern)</b>	<b>Bangalore, India</b> <b>March 2022 - July 2022</b>
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- Enhanced DeIREMO (Delta Remote Monitoring) by developing and implementing features for data collection from solar site sensors and streamlining data transmission to Delta's Remote Monitoring Server.
- Resolved critical timing and location-related bugs across multiple software versions, improving system reliability and performance in real-time monitoring applications.

## SKILLS

**Programming Languages:** Advanced: C, C++, Python; Basic: Java., **Hardware Description language:** Verilog; VHDL.

**Frameworks & Libraries:** PyTorch, TensorFlow; CUDA (basic), OpenCV, MediaPipe.

**Tools:** Xilinx ISE, MATLAB, Arduino, Intel Quartus Prime, LabVIEW, WireShark, VoTT, LOIC Tool, AWS, AWS Trainium.

## HONORS

- Best Outgoing Student** in Electronics and Telecommunication Engineering, RIT. Awarded this honor for excellence in academics, co-curricular and extracurricular activities, and community service.
- Gold Medallist** in Undergraduate Studies from the Department of Electronics and Telecommunication Engineering, RIT. Awarded the gold medal for achieving the highest GPA of 9.74, securing 1st rank in the department.