

UNIT-4

MEMORY MANAGEMENT

MEMORY MANAGEMENT: Memory management is a form of resource management applied to computer memory. The essential requirement of memory management is to provide the ways to dynamically allocate memory to programs at their request, and free it for reuse when no longer needed.

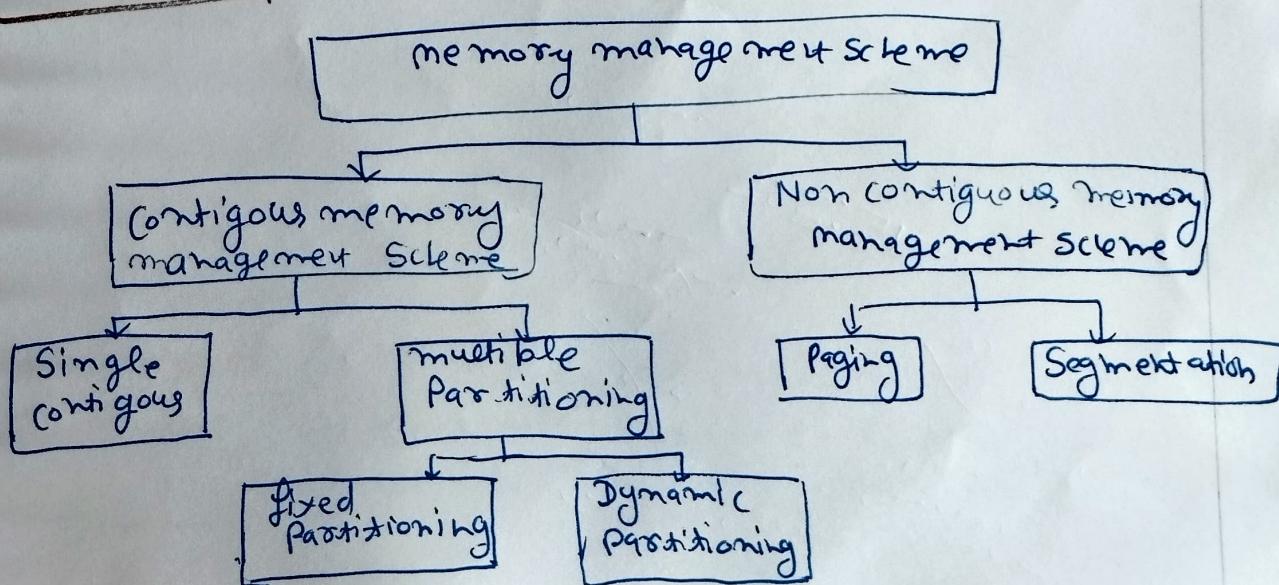
Why memory is Required:

Allocate and deallocate memory before and after process execution.

To keep track of used memory space by processes.

To minimize fragmentation issues. To proper utilization of main memory. To maintain data integrity while executing of process.

Memory management Techniques:



CONTIGUOUS MEMORY ALLOCATION:-

In a contiguous memory management scheme, each program occupies a single block of storage location such as a set of memory locations with consecutive addresses.

Ex

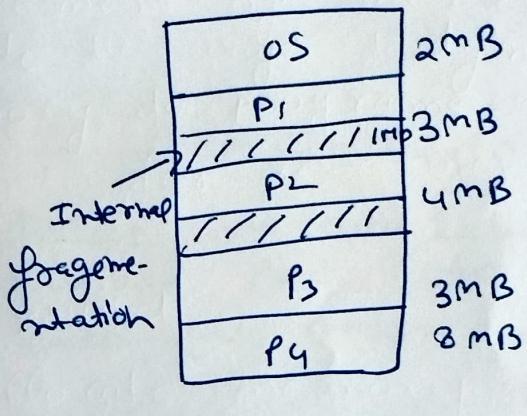
$$P_1 \Rightarrow 1 \text{ MB}$$

$$P_2 \Rightarrow 2 \text{ MB}$$

$$P_3 \Rightarrow 3 \text{ MB}$$

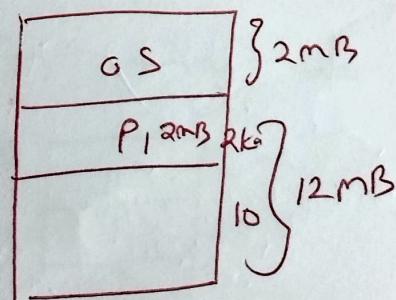
$$P_4 \Rightarrow 10 \text{ MB}$$

$$\begin{matrix} 8 \text{ MB} \\ \diagdown \\ 2 \text{ MB} \end{matrix}$$



Single Contiguous memory Allocation:-

In this scheme the main memory is divided into two contiguous areas or partitions. The operating system (OS) reside permanently in one partition, generally at the low memory and the user process is loaded into the other partition.



Advantages:

- (1) Simple to implement
- (2) Does not require expertise to understand and use such as a system.

Disadvantage of single contiguous memory

- (1) wastage of memory space.
- (2) The CPU remain Idle.
- (3) It can not be executed if the program is too large.
- (4) It does not support multiprogramming.

MULTIPLE PARTITIONING

In this operating system needs to divide the available main memory into multiple parts to load multiple processes into the main memory. It has two types:-

- (1) fixed Partitioning
- (2) variable Partitioning.

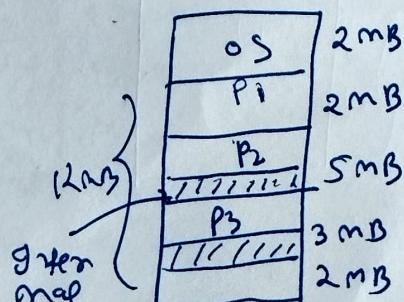
(1) fixed Partitioning:- In this technique is also known as Static Partitioning. In this scheme the system divides the memory into fixed size partitions. The partitions may or may not be the same size.

$$P_1 \Rightarrow 2 \text{ MB}$$

$$P_2 \Rightarrow 2 \text{ MB}$$

$$P_3 \Rightarrow 1 \text{ MB}$$

$$P_4 \Rightarrow 3 \text{ MB}$$



Given
map
fragmentation

→ External fragmentation

Fragmentation:- memory fragmentation can be of two types.

- (1) Internal fragmentation.
- (2) External fragmentation.

Internal fragmentation:- In Internal fragmentation there is wasted space internal to a Partition due to fact the block of data loaded is smaller than the partition.

External fragmentation:- The external fragmentation here is a hole of in multiple partition allocation scheme. Next process request of memory, actually of memory is free which satisfy the request but hole is not contiguous.

Difference Between Internal and External fragmentation.

internal

(1) memory allocated to a process may be slightly larger than the requested memory the difference between these two numbers is internal fragmentation.

external

(1) external fragmentation exists when there is enough total memory space to satisfy a request but available spaces are not contiguous.

(2) first-fit and best-fit memory allocation does not suffer from internal fragmentation

(3) Systems with fixed size allocation units such as the single partitions scheme and paging suffer from internal fragmentation

(2) first-fit and best-fit memory allocation suffers from external fragmentation.

(3) Systems with variable sized allocation units such as 4K multiple partitions scheme and segmentation suffer from external fragmentation

UNIT-2

READERS - WRITERS PROBLEM:-

A Database is to be shared among several concurrent processes. Some of the processes may want to update i.e. read/write the database.

Hardcase
if a writer and some other thread access the database simultaneously choose may早晚 to ensure these difficulties do not arise all require that writers have exclusive access to Shared database. This is known as Reader's writer Problem.

Solution:-

We will use 2 semaphores and one integer variables

(1) mutex (m) \rightarrow initialised to 1

(2) wrt \rightarrow initialised to 0

(3) readcount \rightarrow initialised to 0

Writer:

```
do
{
    wait (wst);
    // performs write operation
    signal (wst);
}
while (TRUE);
```

Reader's Process:-

```
do
{
    wait (mutex)
    read count++;
    if (read count == 1)
        wait (wst);
    signal (mutex);
    // perform reading
    wait (mutex);
    read count--;
    if (read count == 0)
        signal (wst),
        signal (mutex);
```

3
while (TRUE);

BARE MACHINE:-

Bare machine is a simplest form of memory management. Bare machine is logical hardware which is used to execute the program in the processor without using the operating system.

The execution of an instruction is done by directly on hardware without using any interfering hardware.

Bare machine accepting the instruction in only machine language due to this those person who has sufficient knowledge about computer field are able to operate a computer.

Resident monitor:- The Resident monitor

is a code that runs on Bare machine, it also work like an operating system that controls the instructions and performing all necessary functions.

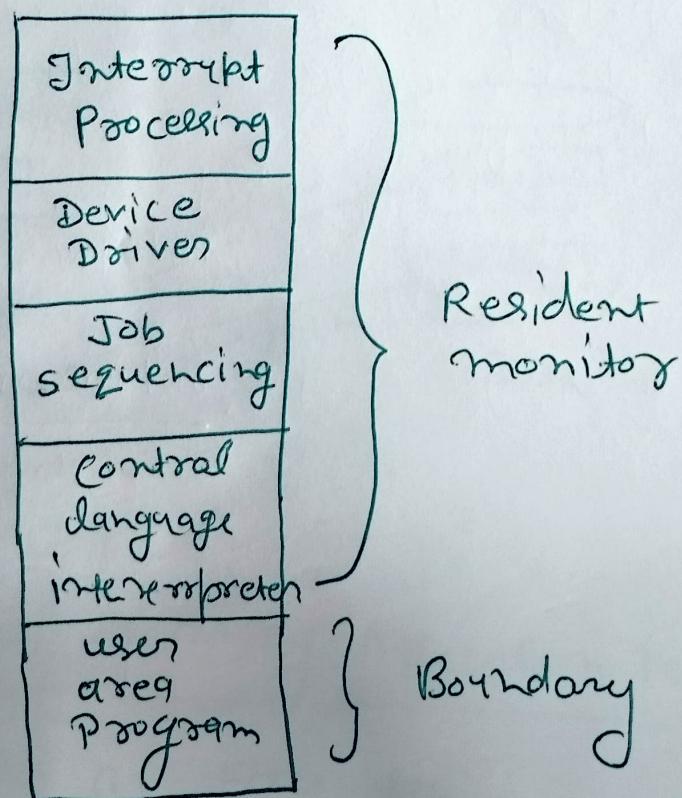
It also work like job sequencer because it also sequences the job and send them to the processor.

After scheduling the job Resident monitor loads the program one by one into the main memory according to their sequence.

When the program executing occurred there is no gap between the program execution and the processing is going to be faster.

It is divided into 4 parts as:

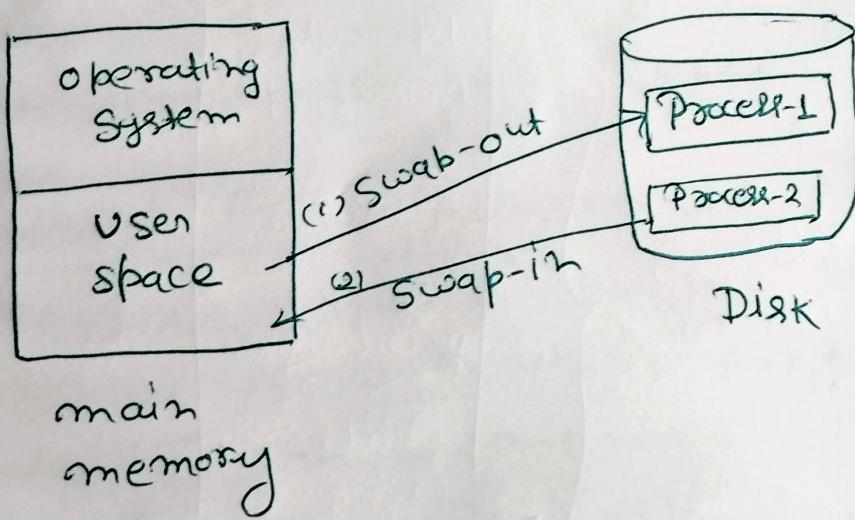
- (1) Control Language Interpreter
- (2) Loader
- (3) Device Drivers
- (4) Interrupt Processing



SWAPPING: Swapping is a mechanism in which a process can be swapped temporarily out of main memory to secondary storage (disk) and make that memory available to other processes.

At some later time the system swaps back the process from the second storage (disk) to main memory.

- (1) Swap-out \Rightarrow Take out the current data from main memory
- (2) Swap-in \Rightarrow Bring the data of process of new user in to main memory.



Contiguous Memory Allocation Techniques,

Algorithms

- (1) First fit
- (2) Best-fit
- (3) Worst-fit

(1) FIRST FIT: In the first fit partition is allocated which is first sufficient from the top of main memory.

A \rightarrow search time is small

D \rightarrow memory loose on account of fragmentation is likely to be high

(2) BEST-FIT: Allocate the process to the partition which is first smallest sufficient partition among the free available partition.

memory loss on account of fragmentation will be large in case of first fit.

search time will be large.

(3) WORST-FIT: Allocate the process to the partition which is largest sufficient among the freely available partition available in the main memory.

Ex Given five memory partition of 100 kb, 500 kb, 200 kb, 300 kb, and 600 kb in same order. How would each of 44 first-fit best fit and worst fit algorithms place processes of 212 kb, 417 kb, 112 kb, and 426 kb. Calculate which algorithm makes the most efficient use of memory.

Sol

memory Partition
100 kb, 500 kb, 200 kb, 300 kb, and 600 kb
in same order. How would each of 4
processes fit?

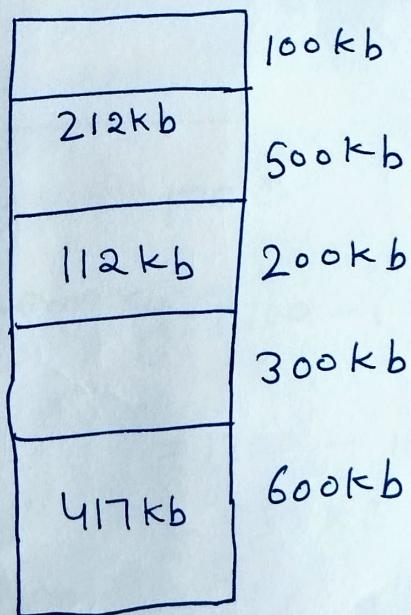
Process $P_1 \Rightarrow 212 \text{ kB}$

$P_2 \Rightarrow 417 \text{ kB}$

$P_3 \Rightarrow 112 \text{ kB}$

$P_4 \Rightarrow 426 \text{ kB}$

first-fit Algorithm,

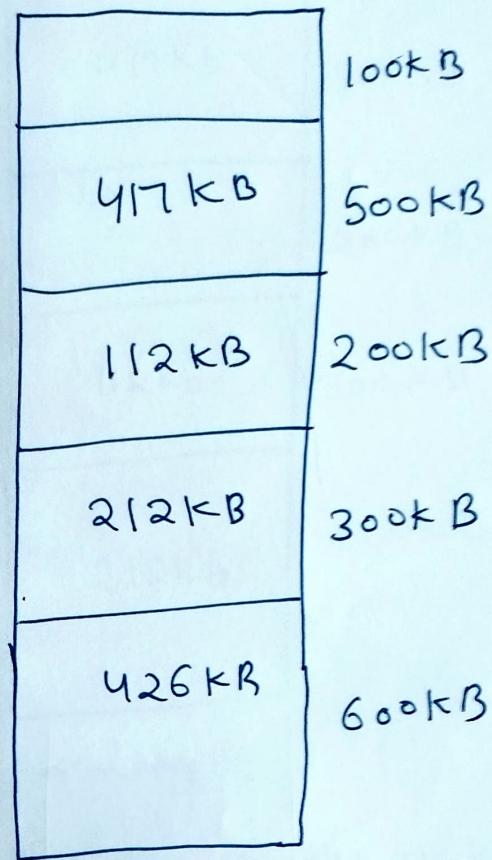


426 kb can not get memory

$$\text{total memory} = 1700 \text{ kB}$$

$$\begin{aligned}\text{memory waste} &= 1700 - (212 + 112 + 417) \\ &= 1700 - 741 \\ &= 959 \text{ kB}\end{aligned}$$

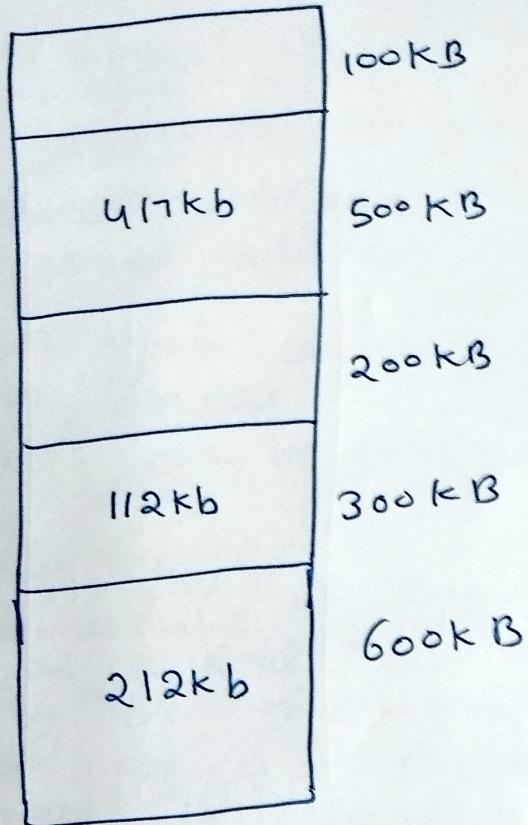
Best-fit Algorithm:-



$$\text{total memory} = 1700$$

$$\begin{aligned}\text{wastage of memory} &= 1700 - (417 + 112 + \\ &\quad 212 + 426) \\ &= 1700 - 1167 \\ &= 533 \text{ kB}\end{aligned}$$

worst-fit Algorithm:-



426 kb met is waiting

Waetage of memory in this algorithm

$$= 1700 - (417 + 112 + 212)$$

$$= 1700 - (529 + 212)$$

$$= 1700 - (741)$$

$$= 959$$

Best-fit algorithm makes the most efficient use of memory.

Logical Address Space:-

An address generated by the CPU is known as Logical Address. It is also known as virtual address.

Logical Address Space can be defined as the size of the Poo cell.

A Logical address can be changed.

Physical Address Space:-

A Physical Address is also known as a Real address. An address seen by the memory unit is commonly known as a Physical Address. A Physical Address is computed by (MMU) main memory unit.

STATIC AND DYNAMIC LOADING:-

To load a Poo cell into the main memory is done by a loader there are two types of loading.

Static Loading:-

In Static Loading into a fixed address. The entire program required more memory space.

Dynamic Loading:-

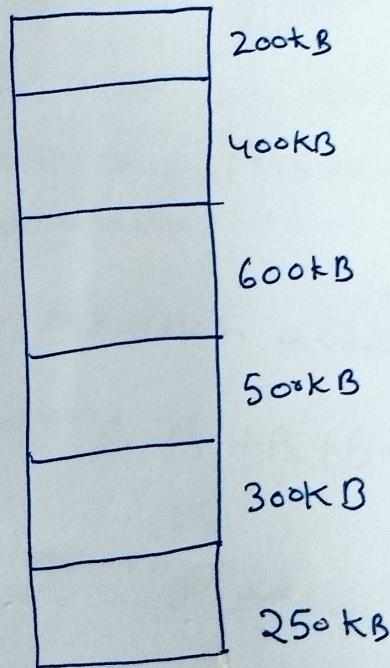
All the programs are loaded in the main memory for execution. Sometimes complex program is loaded into the memory by some

Practice Problem for memory allocation:-

Ex Given memory partitions of 200 kB, 400 kB, 600 kB, 500 kB, 300 kB, and 250 kB. These partitions need to be allocated to four processes of size 357 kB, 210 kB, 465 kB and 491 kB in that order using first-fit, Best fit and worst-fit algorithm.

Sol

The main memory has been divided into fixed partitions as:



Given Process are

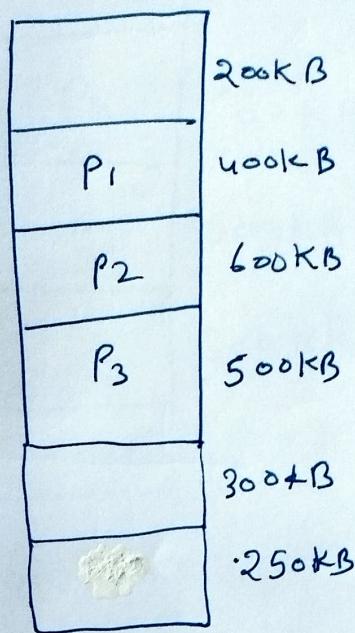
Process $P_1 = 357 \text{ kB}$

Process $P_2 = 210 \text{ kB}$

Process $P_3 = 468 \text{ kB}$

Process $P_4 = 491 \text{ kB}$

Allocation using first fit Algorithm:-



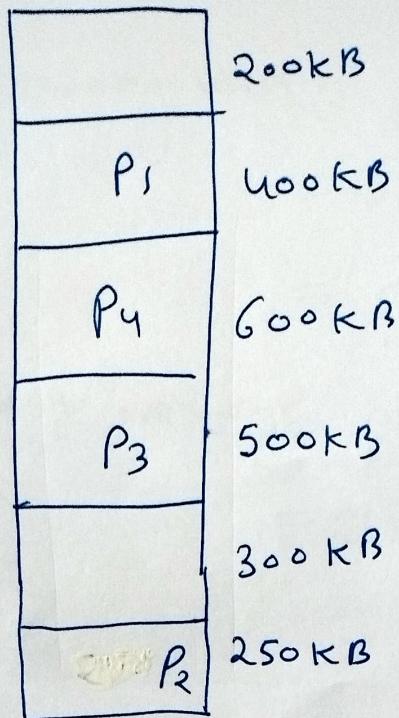
Process P₄ can not be allocated the memory because no partition of size of process P₄ is available.

Total memory Partition available = 2250 kB

Total used memory P₁ + P₂ + P₃ ⇒ 325 + 210 + 468

$$\begin{aligned} \text{total used memory} &= 1035 \text{ kB} \\ \text{free} &= 2250 \text{ kB} - 1035 \text{ kB} \\ &= 1215 \end{aligned}$$

Ex- Best-fit - Algorithm:-



Total available memory Partition = 2250 KB

$$\begin{aligned}
 \text{Total used memory} &= P_1 + P_2 + P_3 + P_4 \\
 &= 357 + 210 + 468 + 491 \\
 &= 1526 \text{ KB}
 \end{aligned}$$

$$\begin{aligned}
 \text{Total memory loss} &= 2250 - 1526 \\
 &= 724 \text{ KB}
 \end{aligned}$$

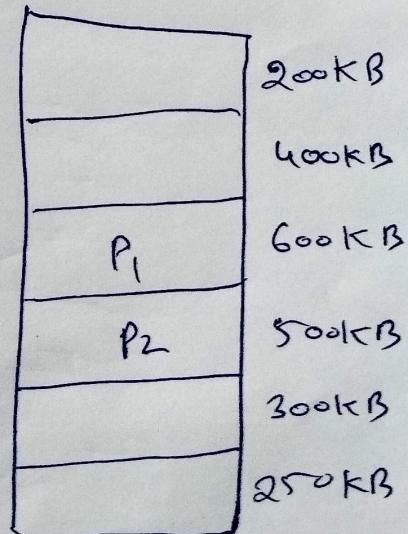
worst-fit - Algorithm

$$P_1 = 357 \text{ KB}$$

$$P_2 = 210 \text{ KB}$$

$$P_3 = 468 \text{ KB}$$

$$P_4 = 491 \text{ KB}$$



$$\begin{aligned}\text{total memory used} &= P_1 + P_2 \\ &= 357 \text{ kB} + 210 \text{ kB} \\ &= 567 \text{ kB}\end{aligned}$$

$$\begin{aligned}\text{total working memory} &= 2250 - 567 \\ &= 2183 \text{ kB}\end{aligned}$$