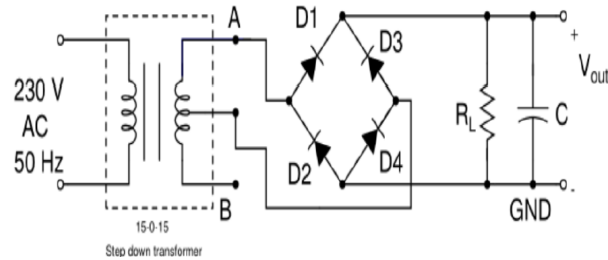


EE230 : Analog Circuits Lab

Mayur Ware | 19D070070, **Section 6**
Experiment 2: DC Power Supply

August 7, 2021

Unregulated Supply – without and with a Capacitive Filter



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*Unregulated Supply without a Capacitive Filter

*Importing the Diode Model

.include Diode_1N914.txt

D1 3 Out 1N914

D2 GND 1 1N914

D3 4 Out 1N914

D4 GND 2 1N914

RL Out GND 1k

*Reference Voltage Sources

V1 1 3 dc 0V

V2 2 4 dc 0V

*Input AC Source

*sin(offset amplitude frequency delay damping-factor)

V 1 2 sin(0 21.2132 50 0 0)

*Analysis

.tran 0.1m 100m

*Control Commands

.control

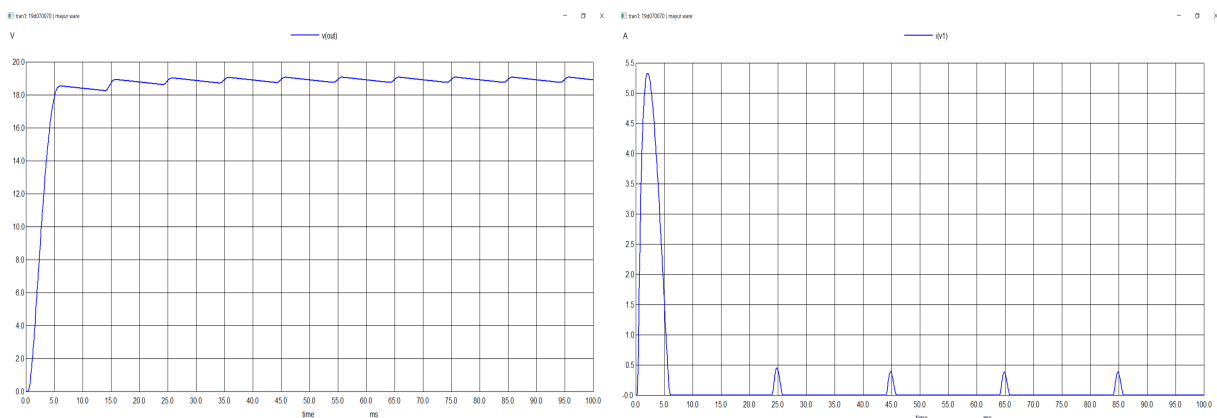
run

plot V(Out) i(V1) i(V2)

.endc

.end

Plots :



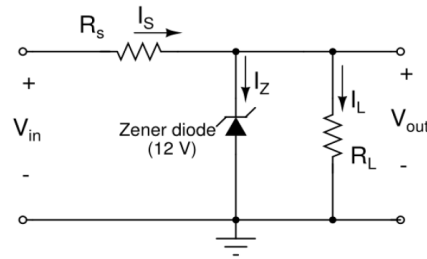
Output Voltage

Current through Diode

Learnings :

High capacitance may result in lower ripple in Output voltage but it increases the first peak of the diode current which may damage the diode. Using high load resistance also helps to reduce ripple in output voltage.

DC Power Supply with Zener Diode Regulator



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*Zener Regulator – Analysis

*Importing the Zener Model

```
.SUBCKT ZENER_12 1 2
```

```
D1 1 2 DF
```

```
DZ 3 1 DR
```

```
VZ 2 3 10.8
```

```
.MODEL DF D ( IS=27.5p RS=0.620 N=1.10 CJO=78.3p VJ=1.00 M=0.330 TT=50.1n )
```

```
.MODEL DR D ( IS=5.49f RS=50 N=1.77 )
```

```
.ENDS
```

*Netlist

```
Rs In 1 470
```

```
RI 4 GND 705
```

```
X1 GND 3 ZENER_12
```

*Voltage Sources

```
Vin In GND dc 20V
```

```
V1 1 Out dc 0V
```

```
V2 Out 3 dc 0V
```

```
V3 Out 4 dc 0V
```

*Control Commands

```
.dc Vin 15 25 0.5
```

```
.control
```

```
run
```

```
set color0 = white
```

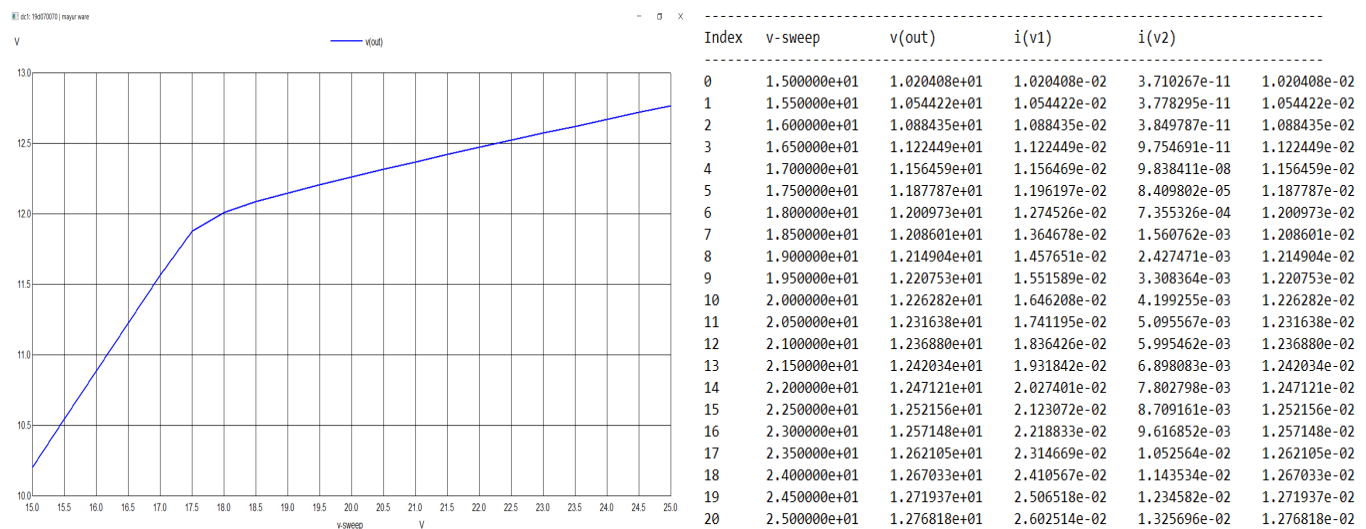
```
set xbrushwidth = 2
```

```
print V(Out) i(V1) i(V2) i(V3)
```

```
.endc
```

```
.end
```

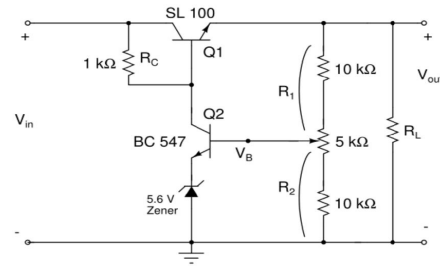
Plots :



Learnings :

Output voltage in this case depends on input voltage as well as the load resistance. Load resistance value must be greater than a calculated minimum value to get the correct outputs.

DC Power Supply with a BJT Series Regulator



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*BJT Series Regulator – Analysis

*Importing the SL100 and BC547

```
.model bc547a NPN IS=10f BF=200 ISE=10.3f IKF=50m NE=1.3
+ BR=9.5 VAF=80 IKR=12m ISC=47p NC=2 VAR=10 RB=280 RE=1 RC=40
+ tr=0.3u tf=0.5n cje=12p vje=0.48 mje=0.5 cjc=6p vjc=0.7 mjc=0.33 kf=2f
.model SL100 NPN IS=100f BF=80 ISE=10.3f IKF=50m NE=1.3
+ BR=9.5 VAF=80 IKR=12m ISC=47p NC=2 VAR=10 RB=100 RE=1 RC=10
+ tr=0.3u tf=0.5n cje=12p vje=0.48 mje=0.5 cjc=6p vjc=0.7 mjc=0.33 kf=2f
```

*Netlist

```
Rc In 1 1k
Rl Out GND 1k
X1 GND 3 ZENER_5.6
R1 Out 2 11k
R2 2 GND 14k
```

*Voltage Sources

```
Vin In GND dc 20V
```

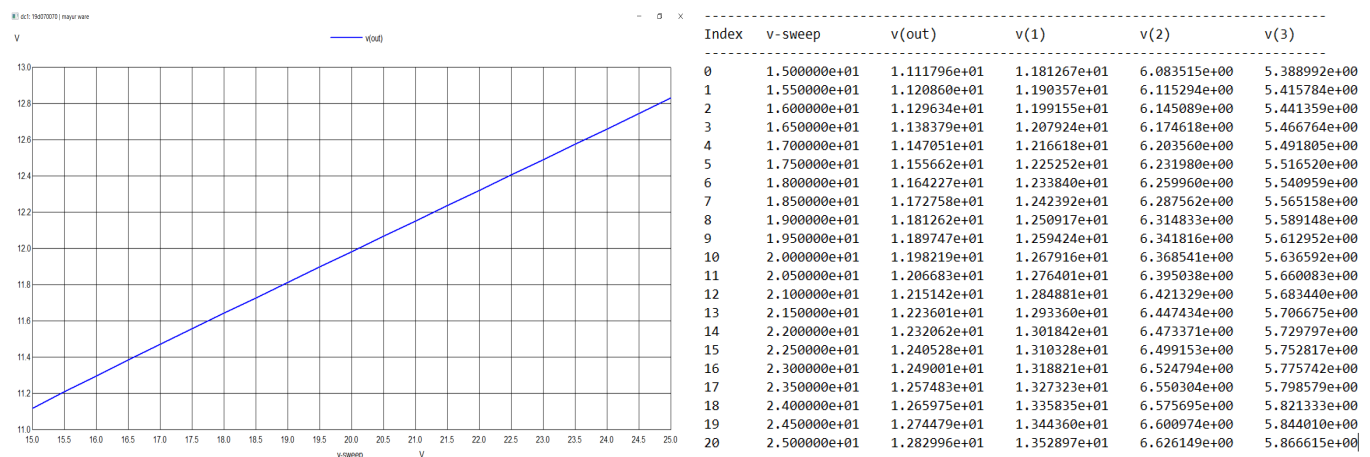
*BJTs

```
Q1 In 1 Out SL100
Q2 1 2 3 bc547a
```

*Control Commands

```
.dc Vin 15 25 0.5
.control
run
set xbrushwidth = 2
plot V(Out) V(1) V(2) V(3)
.endc
.end
```

Plots :



Learning :

DC Power Supply with a BJT Series Regulator is a negative feedback circuit which is more stable than the Zener Regulator. This Regulator gives the most stable output.