1) LDA BETA

MUL GAMMA

STA ALPHA

ALPHA RESW 1

BETA RESW 1

GAMMA RESW 1

2) LDA BETA

DIV GAMMA

STA ALPHA

ALPHA RESW 1

BETA RESW 1

GAMMA RESW 1

3) LDA ZERO

STA INDEX

LOOP LDX INDEX

STA ALPHA, X

LDA INDEX

ADD THREE

STA INDEX

COMP K300

TIX TWENTY

JLT LOOP

ALPHA RESW 100

K300 WORD 300

INDEX RESW 1

TWENTY WORD 20

THREE WORD 3

ZERO WORD 0

4) WRREC LDX ZERO

WLOOP TD OUTPUT

JEQ WLOOP

LDCH RECORD, X

WD OUTPUT

TIX AMOUNT

JLT WLOOP

RSUB

RECORD RESB 100

ZERO WORD 0

AMOUNT WORD 1

OUTPUT BYTE ‘05’

5) WLOOP TD OUTPUT

JEQ WLOOP

LDA ZERO

STA INDEX

LOOP LDX INDEX

WD INDEX

TIX TEN

JLT WLOOP

RSUB

OUTPUT BYTE X ’05’

ZERO WORD 0

TEN WORD 10