

date 01/12/2021

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SERIES: PYBE30 | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

- up to 30 W isolated output
- industry standard pin-out
- 4:1 input range (9~36 Vdc, 18~75 Vdc)
- single/dual regulated outputs
- 1500 Vdc isolation
- continuous short circuit protection
- efficiency up to 90%
- operating temperature range (-40~+85°C)
- EN 62368-1



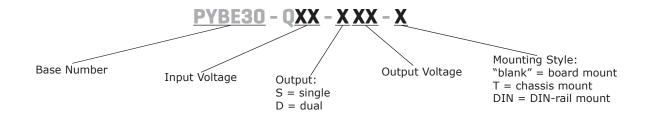


MODEL		put tage	output voltage		tput rent	output power	ripple & noise¹	efficiency ²
	typ (Vdc)	range (Vdc)	(Vdc)	min (mA)	max (mA)	max (W)	max (mVp-p)	typ (%)
PYBE30-Q24-S3 ³	24	9~36	3.3	0	6000	19.8	100	85
PYBE30-Q24-S5 ³	24	9~36	5	0	6000	30	100	86
PYBE30-Q24-S9 ³	24	9~36	9	0	3333	30	100	88
PYBE30-Q24-S12 ³	24	9~36	12	0	2500	30	100	90
PYBE30-Q24-S15 ³	24	9~36	15	0	2000	30	100	90
PYBE30-Q24-S24 ³	24	9~36	24	0	1250	30	100	90
PYBE30-Q24-D5	24	9~36	±5	0	±3000	30	150	86
PYBE30-Q24-D12	24	9~36	±12	0	±1250	30	150	89
PYBE30-Q24-D15	24	9~36	±15	0	±1000	30	150	89
PYBE30-Q24-D24	24	9~36	±24	0	±625	30	150	89
PYBE30-Q48-S3 ³	48	18~75	3.3	0	6000	19.8	100	86
PYBE30-Q48-S5 ³	48	18~75	5	0	6000	30	100	87
PYBE30-Q48-S12 ³	48	18~75	12	0	2500	30	100	88
PYBE30-Q48-S15 ³	48	18~75	15	0	2000	30	100	89
PYBE30-Q48-S24 ³	48	18~75	24	0	1250	30	100	87
PYBE30-Q48-D5	48	18~75	±5	0	±3000	30	150	86
PYBE30-Q48-D12	48	18~75	±12	0	±1250	30	150	88
PYBE30-Q48-D15	48	18~75	±15	0	±1000	30	150	88

Notes:

- 1. From 5~100% load, nominal input, 20 MHz bandwidth oscilloscope, with 10 μF tantalum and 1 μF ceramic capacitors on the output. From 0~5% load, ripple and noise is <5% Vo.
- Measured at nominal input voltage, full load. The typical efficiencies for the chassis mount and DIN-rail mount versions are ~2% less than the board mount versions due to the input reverse polarity protection.
- 3. UL approved.
- 4. All specifications are measured at Ta=25°C, humidity < 75%, nominal input voltage, and rated output load unless otherwise specified.

PART NUMBER KEY



INPUT

parameter	conditions/description	n	min	typ	max	units
operating input voltage⁵	24 Vdc input models 48 Vdc input models		9 18	24 48	36 75	Vdc Vdc
start-up voltage ⁵	24 Vdc input models 48 Vdc input models				9 18	Vdc Vdc
surge voltage	for maximum of 1 secon 24 Vdc input models 48 Vdc input models	nd	-0.7 -0.7		50 100	Vdc Vdc
under voltage shutdown	24 Vdc input models 48 Vdc input models		5.5 12.0	6.5 15.5		Vdc Vdc
	24 Vdc input models	3.3 Vdc output models5 Vdc output modelsall other models			993 1453 1488	mA mA MA
current	48 Vdc input models	3.3 Vdc output models5 Vdc output modelsall other models			485 726 744	mA mA mA
remote on/off (CTRL) ⁶	turn on (3.5~12 Vdc or open circuit) turn off (<1.2 Vdc) input current when switched off			5	8	mA
filter	Pi filter					
input reverse polarity protection	only present on chassis	mount and DIN-rail mount m	odels			
no load power consumption				0.14		W

5. The minimum input voltage and start-up voltage for the chassis mount and DIN-rail mount versions are 1 Vdc higher than the board mount versions. Notes:

6. The voltage of the CTRL pin is referenced to input GND pin.

OUTPUT

parameter	conditions/description	min	typ	max	units
	3.3, 5 Vdc output models			10,000	μF
	9 Vdc output models			4,700	μF
	12 Vdc output models			2,700	μF
mayimum sanasitiya laad?	15 Vdc output models			1,680	μF
maximum capacitive load ⁷	±5 Vdc output models			2,000	μF
	±12 Vdc output models			1,250	μF
	24, ±15 Vdc output models			680	μF
	±24 Vdc output models			470	μF
volta a a a a company 8	0% to 5% load		±1	±5	%
voltage accuracy ⁸	5% to full load		±1	±3	%
	from low line to high line, full load				
line regulation	positive outputs		±0.2	±0.5	%
	negative outputs		±0.5	±1	%
	from 5% to full load				
load regulation ⁹	positive outputs		±0.5	±1	%
	negative outputs		±0.5	±1.5	%

Note:

7. Tested at input voltage range and full load. 8. At $0\sim5\%$ load, the max output voltage accuracy for the ±5 & ±9 Vdc output models is $\pm5\%$. 9. At $0\sim100\%$ load, the max load regulation is $\pm5\%$.

OUTPUT (CONTINUED)

parameter	conditions/description	min	typ	max	units
cross regulation	dual output models: main output 50% load secondary output from 10~100% load			±5	%
start-up time	nominal input, constant resistive load		10		ms
adjustability ¹⁰	see application notes		±10		%
switching frequency ¹¹	PWM mode		300		kHz
transient recovery time	25% load step change, nominal input voltage		300	500	μs
transient response deviation	25% load step change, nominal input voltage 3.3, 5, ± 5 Vdc output models all other models		±5 ±3	±8 ±5	% %
temperature coefficient	at full load			±0.03	%/°C

Note:

PROTECTIONS

parameter	conditions/description	min	typ	max	units
over voltage protection		110		160	%
over current protection		110		190	%
short circuit protection	hiccup, continuous, self recovery				

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	input to output for 1 minute at 1 mA	1,500			Vdc
isolation resistance	input to output at 500 Vdc	1,000			МΩ
isolation capacitance	input to output, 100 kHz / 0.1 V		2,000		pF
safety approvals ^{12, 13}	certified to 62368-1: EN certified to 60950: UL				
conducted emissions	CISPR32/EN55032, class A (no external circui	t); class B (externa	l circuit requi	ired, see Figu	re 3-b, 4-b)
radiated emissions	CISPR32/EN55032, class A (no external circui	t); class B (externa	l circuit requi	ired, see Figu	re 3-b, 4-b)
ESD	IEC/EN61000-4-2, contact ± 4kV, class B				
radiated immunity	IEC/EN61000-4-3, 10V/m, class A				
EFT/burst	IEC/EN61000-4-4, ± 2kV, class B (external cir	cuit required, see F	igure 3-a, 4-	·a)	
surge	IEC/EN61000-4-5, line-line ± 2kV, class B (ex	ternal circuit requir	ed, see Figur	e 3-a, 4-a)	
conducted immunity	IEC/EN61000-4-6, single output, 3 Vr.m.s, cla IEC/EN61000-4-6, dual output, 10 Vr.m.s, cla				
MTBF	as per MIL-HDBK-217F, 25°C	1,000,000			hours
RoHS	yes				

Note:

- 12. UL approval only for single output board mount, chassis mount, and DIN-rail mount models, see page 1 for specific models.

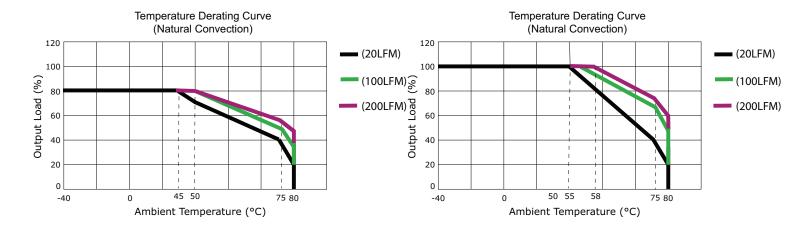
 13. CE approval for all models.

^{10.} For single output models only
11. Value is based on full load. At loads <50%, the switching frequency decreases with decreasing load.

ENVIRONMENTAL

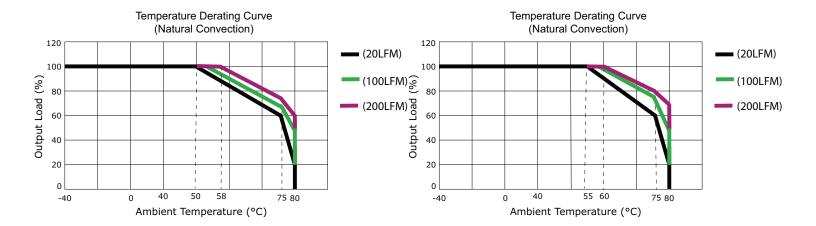
parameter	conditions/description	min	typ	max	units
operating temperature	see derating curves	-40	,	80	°C
storage temperature		-55		125	°C
storage humidity	non-condensing	5		95	%
vibration	10~55 Hz, for 30 minutes on each axis		10		G

DERATING CURVES



Applicable models: PYBE30-Q24-D5 (9-18 Vdc input voltage), PYBE30-Q24-D24 (9-18 Vdc input voltage), PYBE30-Q48-D5 (18-36 Vdc input voltage)

Applicable models: PYBE30-Q24-D5 (18-36 Vdc input voltage), PYBE30-Q24-D24 (18-36 Vdc input voltage), PYBE30-Q48-D5 (36-75 Vdc input voltage), PYBE30-Q24-D12, PYBE30-Q24-D15, PYBE30-Q48-D12, PYBE30-Q48-D15



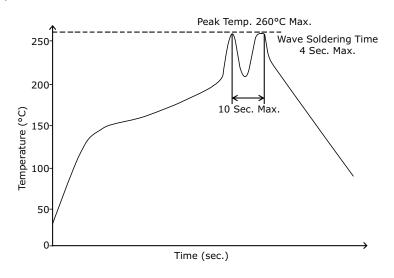
Applicable models: PYBE30-Q24-S3, PYBE30-Q24-S5, PYBE30-Q48-S3, PYBE30-Q48-S5

Applicable models: PYBE30-Q24-S9, PYBE30-Q24-S12, PYBE30-Q24-S15, PYBE30-Q24-S24, PYBE30-Q48-S12, PYBE30-Q48-S15, PYBE30-Q48-S24

SOLDERABILITY¹⁴

parameter	conditions/description	min	typ	max	units
hand soldering	1.5 mm from case for 10 seconds			300	°C
wave soldering	see wave soldering profile			260	°C

Note: 14. For board mount models only.



MECHANICAL

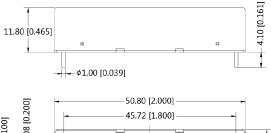
parameter	conditions/description	min	typ	max	units
	board mount: 50.80 x 25.40 x 11.80 [2.000	x 1.000 x 0.465 inch]			mm
dimensions	chassis mount: 76.00 x 31.50 x 21.20 [2.99	2 x 1.240 x 0.835 inch]		mm
	DIN-rail mount: 76.00 x 31.50 x 25.80 [2.992 x 1.240 x 1.016 inch]				mm
case material	aluminum alloy				
	board mount		26		g
weight	chassis mount		48		g
	DIN-rail mount		68		g

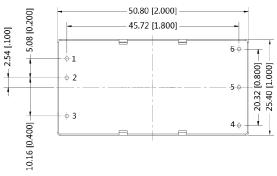
MECHANICAL DRAWING (BOARD MOUNT)

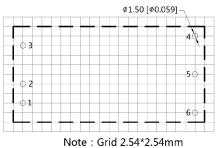
units: mm [inch] tolerance: ±0.50[±0.020]

pin diameter tolerance: $\pm 0.10[\pm 0.004]$

PIN CONNECTIONS					
PIN	Fund	ction			
PIN	Single	Dual			
1	Vin	Vin			
2	GND	GND			
3	CTRL	CTRL			
4	trim	-Vo			
5	0V	0V			
6	+Vo	+Vo			







Recommended PCB Layout Top View

MECHANICAL DRAWING (CHASSIS MOUNT)

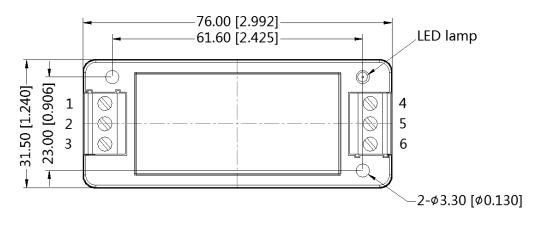
units: mm [inch]

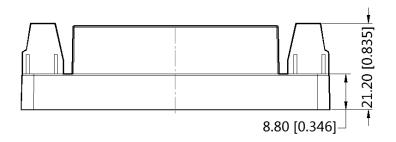
tolerance: $\pm 0.50[\pm 0.020]$

wire range: 24~12 AWG

tightening torque: max 0.4 N*m

PIN CONNECTIONS					
PIN	Function				
PIN	Single	Dual			
1	CTRL	CTRL			
2	GND	GND			
3	Vin	Vin			
4	trim	-Vo			
5	0V	0V			
6	+Vo	+Vo			





MECHANICAL DRAWING (DIN-RAIL MOUNT)

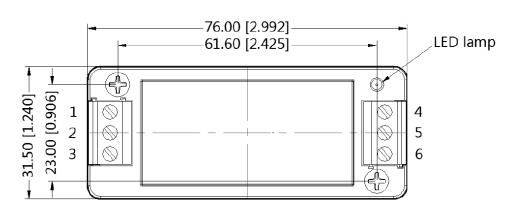
units: mm [inch]

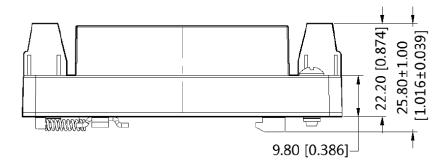
tolerance: $\pm 0.50[\pm 0.020]$

installed on DIN rail TS35 wire range: 24~12 AWG

tightening torque: max 0.4 N*m

PIN CONNECTIONS					
PIN	Fund	ction			
PIN	Single	Dual			
1	CTRL	CTRL			
2	GND	GND			
3	Vin	Vin			
4	trim	-Vo			
5	0V	0V			
6	+Vo	+Vo			





APPLICATION CIRCUIT

This series has been tested according to the following recommended circuits (Figures 1 & 2) before leaving the factory. If you want to further reduce the input and output ripple, you can increase the input and output capacitors or select capacitors of low equivalent impedance provided that the capacitance is less than the maximum capacitive load of the model.

Figure 1
Single Output Models



Figure 2 Dual Output Models

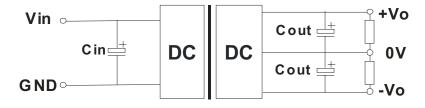


Table 1

Vout (Vdc)	Cin (µF)	Cout (µF)
3.3/5/9	100	220
12/15/24	100	100
±5/±12/±15	100	220
±24	100	100

EMC RECOMMENDED CIRCUIT

Figure 3
Single Output Models

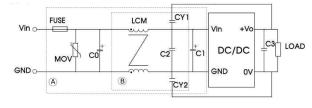


Figure 4 Dual Output Models

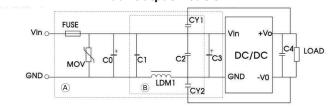


Table 2

Recommended External Circuit Components			
Vin (Vdc)	24 48		
FUSE	choose according to actual input current		
MOV	S20K30	S14K60	
C0	680 μF / 50 V	330 μF / 100 V	
C1	330 μF / 50 V	330 μF / 100 V	
C2	4.7 μF / 50 V	2.2 μF / 100 V	
C3	Refer to the Cout in Table 1		
LCM	1 mH		
CY1, CY2	1 nF / 2 kV		

Table 3

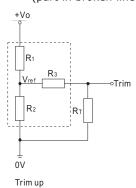
Recommended External Circuit Components			
Vin (Vdc)	24	48	
FUSE	choose according to actual input current		
MOV	S20K30	S14K60	
C0	680 μF / 50 V	330 μF / 100 V	
C1	2.2 μF / 50 V	2.2 μF / 100 V	
C2	2.2 μF / 50 V	2.2 μF / 100 V	
C3	330 μF / 50 V	330 μF / 100 V	
C4	Refer to the Cout in Table 1		
LDM1	3.3 μH		
CY1, CY2	2.2 nF / 4 kV		

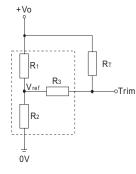
APPLICATION NOTES

Output voltage trimming Leave open if not used.

Figure 5

Application Circuit for Trim pin (part in broken line is the interior of models)





Trim down

Formula for Trim Resistor

up:
$$R_T = \frac{aR_2}{R_{2}-R_3} - R_3$$

$$a = \frac{Vref}{Vo' - Vref} \cdot R_1$$

down:
$$R_T = \frac{aR_1}{R_1-a} -R_3$$

$$a = \frac{\text{Vo'} - \text{Vref}}{\text{Vref}} \cdot R_2$$

Note: Value for R1, R2, R3, and Vref refer to Table 4 R_{T} : Trim Resistor

a: User-defined parameter, no actual meanings

Vo': The trim up/down voltage

Table 4

Vout (Vdc)	R1 (kΩ)	R2 (kΩ)	R3 (kΩ)	Vref (V)
3.3	4.801	2.87	12.4	1.24
5	2.883	2.87	10	2.5
9	7.500	2.87	15	2.5
12	11.000	2.87	15	2.5
15	14.494	2.87	15	2.5
24	24.872	2.87	17.8	2.5

Additional Resources: Product Page | 3D Model

CUI Inc | SERIES: PYBE30 | DESCRIPTION: DC-DC CONVERTER date 01/12/2021 | page 9 of 9

REVISION HISTORY

rev.	description	date
1.0	initial release	01/24/2019
1.01	safeties updated in features and safety line, packaging removed	01/12/2021

The revision history provided is for informational purposes only and is believed to be accurate.



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