

Project Report

2896

Designing Oscillator for an Antenna at $\sim 3.5~\mathrm{GHz}$

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For the project instructor:

I approve the submission of the following report

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Contents

1	Introduction	4
2	Theoretical Background 2.1 Oscillators	5 5 5 7 7
	st of Figures	
	Block Diagram	2 5
\mathbf{L}	st of Tables	

Abstract

In the dynamic realm of wireless communication, the pursuit of efficient and dependable antennas operating at high frequencies has emerged as a pivotal endeavor. This project embarks on a comprehensive exploration of the intricacies involved in implementing an oscillator finely tuned for integration into an antenna system operating at approximately 3.5 GHz. By interfacing with a matching network, this oscillator aims to augment the functionality of the transmitter antenna. The paramount significance of this undertaking stems from its potential to propel the performance benchmarks of communication systems, thereby aligning with the constantly evolving landscape of modern connectivity.

At its core, this project delves into the interdisciplinary domain encompassing Radio Frequency (RF) engineering, Microwave engineering, and Analog Integrated Circuits. The realization of RF systems, particularly those operating at challenging frequencies around 3.5 GHz, necessitates a nuanced comprehension of various domains including circuit theory, analog circuits, frequency response, electromagnetic principles, and communication systems. The amalgamation of these disciplines forms the bedrock upon which the project endeavors to advance the frontier of wireless communication technology.

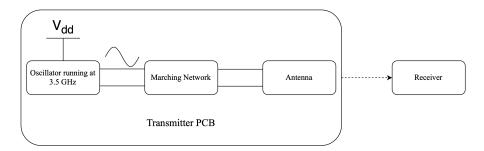


Figure 1: Block Diagram

Oscillators, owing to their intrinsic capability to generate stable and precise periodic waveforms, find extensive utility across a myriad of applications. The envisioned 3.5 GHz high-frequency oscillator, poised for development in this project, holds promise in realms ranging from 5G networks to satellite communication systems. Moreover, the utility extends to radar systems for navigation, RFID tags utilized in smartcards, and the intricate realm of Magnetic Resonance Imaging (MRI). This underscores the multifaceted impact that the successful realization of such an oscillator could wield across diverse industries, transcending mere advancements in antenna systems to usher in efficiency, precision, and reliability across a spectrum of technological applications.

Central to the project's endeavor is a meticulously structured implementation plan, delineated into several distinct phases. The initial phase entails the construction of an oscillator model, a pivotal step that sets the foundation for subsequent developments. Through the utilization of Spice simulation, this phase aims to validate the efficacy of the oscillator model at the targeted frequency of 3.5 GHz. Subsequently, the project pivots towards the selection of appropriate components, a process encompassing the careful identification of the optimal transistor—be it an NPN BJT or an NMOS—capable of operating within the designated frequency range. This necessitates the creation and validation of a corresponding spice model, which is subsequently subjected to simulation using Cadence Virtuoso to ascertain S-parameters.

The integration of the oscillator with the transmitter antenna necessitates meticulous attention to antenna matching—a process crucial for optimizing power transfer and minimizing signal reflections. The intricate interplay between the oscillator and the antenna, facilitated by the matching network, underscores the project's commitment to realizing seamless integration within

the broader communication ecosystem. Furthermore, the project envisages the creation of a compact and meticulously optimized Printed Circuit Board (PCB) layout, meticulously designed to mitigate parasitic elements and minimize signal loss—a pivotal consideration in the realm of RF design.

The culmination of these meticulously orchestrated phases culminates in a rigorous testing regimen, aimed at validating the integrity and efficacy of the developed oscillator. Through the utilization of a pre-built receiver, the performance of the oscillator is meticulously scrutinized, with particular emphasis placed on parameters such as stability, frequency accuracy, and signal fidelity. This comprehensive testing phase serves as the litmus test for the viability and functionality of the developed oscillator, affirming its readiness for integration within real-world communication systems.

In conclusion, the project encapsulates a holistic exploration of the intricacies involved in the development of a high-performance 3.5 GHz oscillator, poised to enhance the efficacy and reliability of communication systems operating within this frequency band. Through a meticulous amalgamation of theoretical insights, simulation-based validation, and real-world testing, the project aspires to not only advance the frontier of antenna systems but also foster a broader paradigm shift towards enhanced efficiency, precision, and reliability across diverse technological applications.

1 Introduction

2 Theoretical Background

2.1 Oscillators

2.1.1 Introduction to Oscillators

2.1.2 Collpits Oscillator

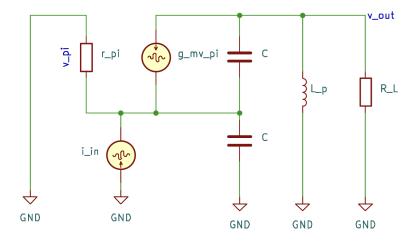


Figure 2: Small Signal Model for Collpits Oscillator

We start with the given equations:

$$i_{in} = -\frac{v_{\pi}}{r_{\pi}} - sC(v_{out} + 2v_{\pi}) \tag{1}$$

$$0 = g_m v_\pi + sC(v_{out} + v_\pi) + \frac{v_{out}}{sL_p} + \frac{v_{out}}{R_L}$$
 (2)

First, we simplify equation (2):

$$\begin{split} g_m v_\pi + s C v_{out} + s C v_\pi + \frac{v_{out}}{sL_p} + \frac{v_{out}}{R_L} &= 0 \\ g_m v_\pi + s C v_\pi + \left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right) v_{out} &= 0 \end{split}$$

Solving for v_{π} :

$$v_{\pi}(g_m + sC) = -\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}$$
$$v_{\pi} = -\frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{g_m + sC}$$

Substitute v_{π} back into equation (1):

$$\begin{split} i_{in} &= -\frac{-\frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{g_m + sC}}{r_\pi} - sC\left(v_{out} + 2\left(-\frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{g_m + sC}\right)\right) \\ &= \frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{r_\pi(g_m + sC)} - sCv_{out} - 2sC\left(-\frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{g_m + sC}\right) \\ &= \frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{r_\pi(g_m + sC)} - sCv_{out} + \frac{2sC\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)v_{out}}{g_m + sC} \\ &= v_{out}\left(\frac{\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)}{r_\pi(g_m + sC)} - sC + \frac{2sC\left(sC + \frac{1}{sL_p} + \frac{1}{R_L}\right)}{g_m + sC}\right) \end{split}$$

Combine the terms:

$$i_{in} = v_{out} \left(\frac{sC + \frac{1}{sL_p} + \frac{1}{R_L}}{r_{\pi}(g_m + sC)} - sC + \frac{2sC^2 + \frac{2sC}{sL_p} + \frac{2sC}{R_L}}{g_m + sC} \right)$$

Since $i_{in} = \frac{v_{out}}{Z_{in}}$, where Z_{in} is the input impedance:

$$\begin{split} \frac{v_{out}}{i_{in}} &= Z_{in} \\ &= \left(\frac{r_{\pi}C^{2}L_{p}R_{L}s^{3} + (-r_{\pi}CL_{p}R_{L}g_{m} + 2r_{\pi}CL_{p} + CL_{p}R_{L})s^{2} + (2r_{\pi}CR_{L} + L_{p})s + R_{L}}{r_{\pi}L_{p}R_{L}s^{3} + (-r_{\pi}CL_{p}R_{L}g_{m} + 2r_{\pi}CL_{p} + CL_{p}R_{L})s^{2} + (2r_{\pi}CR_{L} + L_{p})s + R_{L}}\right) \\ &= r_{\pi}L_{p}R_{L}\left(s^{2}C + sg_{m}\right) \end{split}$$

Therefore, the frequency response is:

$$\frac{v_{out}}{i_{in}} = \frac{r_{\pi}L_{p}R_{L}(s^{2}C + sg_{m})}{r_{\pi}C^{2}L_{p}R_{L}s^{3} + (-r_{\pi}CL_{p}R_{L}g_{m} + 2r_{\pi}CL_{p} + CL_{p}R_{L})s^{2} + (2r_{\pi}CR_{L} + L_{p})s + R_{L}}$$
(3)

Put $s = i\omega$ and let Im of denominator $\rightarrow 0$

$$\omega_0 = \sqrt{\frac{2r_\pi C R_L + L_p}{r_\pi C^2 L_p R_L}} \tag{1}$$

Assuming $r_{\pi} \to \infty$, as is the case is MOS, we reach the well-known expression $\omega_0 = \sqrt{\frac{1}{L\frac{C.C}{C+C}}}$ For sustained oscillations, we need

$$-(-r_{\pi}CL_{p}R_{L}g_{m} + 2r_{\pi}CL_{p} + CL_{p}R_{L})\omega_{0}^{2} + R_{L} > 0$$
(2)

This gives us

$${}^{1}R_{L}g_{m} - \frac{R_{L}}{r_{\pi}} - 2 > 0 \tag{3}$$

Thus, we need to set R_L accordingly at the oscillating frequency, giving us a lower bound for load and thus a need for a matching network

¹This is similar to the condition found in Razavi, but for NMOS in place of NPN BJT

2.2 Matching Networks

We assume a central impedance Z_c such that $Z_c > \max(Z_s, Z_L)$, and then calculate the series and parallel reactive components on both sides.²

Source:

Load:
$$\begin{cases} Q_{src} = \sqrt{\frac{Z_c}{Z_s}} - 1 \\ X_{src}^{parallel} = \frac{Z_c}{Q_{src}} \\ X_{src}^{series} = Q_{src}Z_s \end{cases}$$
 (4)
$$\begin{cases} Q_{ld} = \sqrt{\frac{Z_c}{Z_L}} - 1 \\ X_{ld}^{parallel} = \frac{Z_c}{Q_{ld}} \\ X_{ld}^{series} = Q_{ld}Z_L \end{cases}$$
 (5)
$$L = \frac{X}{2\pi f_0} \quad \text{and} \quad C = \frac{1}{2\pi f_0 X}$$
 eries/parallel

for chosen X series/parallel

After this, the values of V_{cc} , V_{ee} , and I_c were changed so as to get the maximum efficiency. R_L was matched to $1[K\Omega]$.

Variable	Value
f_0	3.5×10^{9}
$Z_{ m out_osc}$	1000
$Z_{ m load}$	50
$Z_{ m center}$	1002
$Q_{ m src}$	0.0447214
$X_{\text{paralle_src}}$	22405.4
$\hat{X}_{\text{series_src}}$	44.7214
$L_{ m src}$	2.03361×10^{-9}

Variable	Value
$C_{ m src}$	2.02955×10^{-15}
Q_{ld}	4.36348
$X_{\text{paralle_ld}}$	229.633
$X_{\text{series_ld}}$	218.174
$L_{ m ld}$	1.04421×10^{-8}
C_{ld}	2.08424×10^{-13}
C_{com}	2.10454×10^{-13}
$L_{\rm com}$	1.70212×10^{-9}

2.3 Antenna

 $^{^2}$ In our case, all impedances are real

Improve the speeddddd