# Avnet SD Card Advice for Zynq<sup>®</sup>7000 All Programmable SoC Products



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## 1. Introduction

This document is a list of suggestions as well as helpful information that will guide Engineers working with Xilinx Zynq®-7000 All Programmable SoC based solutions from Avnet. Advice is provided for selecting and working with SD cards for their own system designs. This document is *not* an all-inclusive list. There are many brands and types of SD cards available. This document contains the brands and types of cards with which Avnet has experience.

#### 2. About SD Cards

There is a *large* variety of SD cards available, with no end in sight to new cards coming out. Design Engineers will find it useful to learn *how to* identify a card in order to understand selection for optimal compatibility. Information in this section is excerpted from the SD Association (Appendix A) webpage.

As of the publication of this document, there are 3 general types of SD cards to be aware of:

- 1. Secure Digital Standard card (SD)
- 2. SD High Capacity (SDHC)
- 3. SD Extended Capacity (SDXC)

There are also 4 BUS types to be aware of:

Туре	Max Speed	Spec. Version
Default	12.5 MB/s	1.01
High Speed	25 MB/s	2.00
UHS-I	104 MB/s	3.01
UHS-II	312 MB/s	4.00

While there is no card marking for Default/Normal or High Speed, a card will have a mark to indicate UHS-I and UHS-II compatibility. This marking employs Roman Numerals similar to the examples shown here:

Туре	Typical Marking			
UHS-I	I SEI SEI			
UHS-II	II Sar Sar			



Cards are also typically marked with a symbol indicating the speed grade. Here the speed grade indicates the minimum non-fragmental Write Speed. As a result, the Write Speeds shown here are the MINIMUM performance for the class rating but the SD bus speeds can be *much* higher.

Minimum Write Speed	Typical Marking
2 MB/s	<b>e</b>
4 MB/s	<b>@</b>
6 MB/s	6
10 MB/s	(E)
10 MB/s	1)
30 MB/s	3

The Standard card can have speed grades of ②, ④, ⑥ or ⑩ with capacities up to 2GB. The typical cards to use with Avnet products are the Full SD as well as microSD versions. The miniSD card is not typically used on Avnet products.



An SDHC card can have any of the speed grades or bus types, with capacities from 4GB to 32GB. They also have three (3) physical sizes of cards.



An SDXC card can also have any of the speed grades or bus types, with capacities over 32GB up to 2TB. By standard, there only appears to be Full SD and microSD size cards.





## 3. Review Zyng Requirements from Xilinx

First, locate the Xilinx document UG585 - Technical Reference Manual ( Appendix B). The Zynq Processing System has two hard peripheral SD/SDIO Controllers. The following bulleted information came from page 35:

- Bootable SD Card mode (option)
- Built-in DMA
- Host mode support only
- Support for version 2.0 of SD specification
- Full speed and low speed support
- 1-bit and 4-bit data interface support
- Low speed clock 0–400 kHz
- Support for high speed interface
- Full speed clock 0-50 MHz with maximum throughput at 25 MB/s
- Support for memory, I/O, and combination cards
- Support for power control modes
- Support for interrupts
- 1 KB Data FIFO interface

The following image is from the MIO configuration page inside Vivado (See Appendix BAppendix A for instructions on how to access this configuration page).

						SD0					
SD1											
						SF	P10				mos
	SPI1										
UAI	RT0			UA	RT0			UART0			
		UA	RT1			UA	RT1			UART1	
12	C0			12	CO			1200			
		12	C1			12	C1			I2C1	
CA	NO			CA	NO			CA	ANO		
Г		CA	N1			CA	CAN1				N1
								тт	CO		
						тт	C1				
				SW	/DT						
	PJTAG					MI	0-	GΡ	10		
		Tra	ace				•				
10	11	12	13	14	15	16	17	18	19	20	21

Next refer to page 188 in the Xilinx Zynq Technical Reference Manual (Appendix A) section "6.3.7 SD Card Boot".

Note that Zynq supports

- Boot from standard SD or SDHC cards
- FAT 16/32 file system
- Up to 32 GB card densities

Note: The SD card boot mode is not supported in the 7z010 CLG225 device.



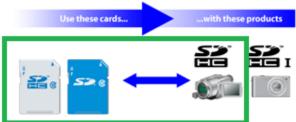
Note: The SD card boot mode does not support header search or multiboot.

The rest of this section of the Technical Reference Manual also has detailed information on the process that the SD card uses to boot from an SD card.

#### 4. Summary of Zyng Requirements

Since the Zynq BootROM requires FAT16 or FAT32 to boot during Stage 0, we immediately know we cannot use SDXC cards for this purpose. Also note that SD card spec 2.0 is required for booting Zynq in SD mode. As such, we can also eliminate UHS-I and UHS-II cards for booting in SD mode. It is NOT recommended to use UHS compatible cards with Zynq, as it is also not a recommended compatibility path for the SD card standard (Appendix A).

Zynq BootROM requirements fall into an SDHC, non-UHS compatible bus configuration.



#### 5. Troubleshooting

If you are using a compatible card and are having issues there are a few steps you can take to isolate the source of the problem.

- Look at Xilinx Answer Record AR# 59476 which can be used as a SD Programming / Booting Checklist (Appendix C)
- Validate your boot configuration jumpers / switches are configured properly (see your hardware user guide / schematic / Zynq TRM)
- Try a prebuilt boot image from an existing out of box experience, such as the MicroZed Linux microSD Card Image Archive listed on the microzed.org website (Appendix D)
- Try a different SD card to eliminate the possibility of a corrupted SD card
- Try a different brand of SD card, such as one of the recommended SD cards listed below
- Validate your hardware by changing the boot configuration to QSPI mode to verify that the Zynq device is functional
- Contact Avnet through our support forums located at <a href="www.picozed.org">www.picozed.org</a>
- Contact your local Avnet/Silica FAE



#### 6. Avnet Tested SD cards

Here at Avnet, we have tested a few different brands and types of SD cards across our reference designs and products. Although cards have markings or are sold as a specific type of card, in our testing of different cards with our own Zynq solutions we have found minor differences in compatibility.

Zyng devices Avnet has tested include:

	ZC7010	ZC7015	ZC7020	ZC7030	ZC7035	ZC7045	ZC7100
ZedBoard			X				
MicroZed	X		X				
PicoZed	X	X	X	X			
PicroZed SDR					X		
Mini-Module+						X	X
Zynq Mini-ITX						X	X

In general a high quality name-brand card which meets Zynq requirements is most consistent. Name brand cards typically have better Write Amplification and Wear Leveling (Appendix E). Our internal testing shows SanDisk cards, which meet the Xilinx recommended speed/type/size requirements, can work across all Avnet solutions without issue. It has not been brought to our attention that there have been issues with SanDisk branded cards. We have tested various capacity/speed grade cards across compatible configurations on the above table of Avnet solutions and have not experienced issues.



There are other SD cards tested in specific configurations and known to work without issue. The testing performed for the following configurations is more observational in nature rather than scientific. Any feedback we have received from the field is anecdotal in nature and the results should be treated accordingly.

Delkin Devices 4GB SDHC SD04AJEQZ-CX000-2 and Transcend 4GB SDHC have both been tested with a Zedboard without reported failures.

Kingston 4GB/8GB SDHC and Delkin Devices DDSDPRO3-32GB SDHC have been tested with the MicroZed and PicoZed product line without reported failures.

Lexar 32GB SDHC and Delkin Devices 32GB SDHC UHS-I have been tested and reported to *not* work with Mini-Module Plus.

PNY 32GB SDHC UHS-I **1** and Delkin Devices 32GB SDHC **6** both have been reported to work with Mini-Module Plus. Note that PNY 32GB SDHC UHS-I **1** does not fit within the specification by Xilinx. As such, this is not a recommended card as results may vary.

Note, this list is **not** all inclusive! There could be SD cards that work **very** well that we have **not** tested. There is also a possibility that cards that do not fit within the specification from Xilinx will also work. If you have a card that works well, please let your local Avnet/Silica FAE know or contact Avnet through our support forums at the picozed.org community site. Properly documented test result information from the field will be considered for inclusion in future revisions of this document.



## 7. Appendix A

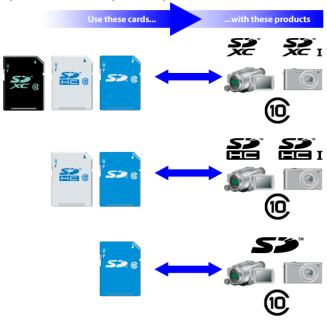
**SD Association Speed Choices** 

https://www.sdcard.org/consumers/speed/speed\_class/index.html

SD Association Optimize UHS Performance

https://www.sdcard.org/consumers/speed/uhs/index.html

**Speed Class Compatibility** 



# 8. Appendix B

Zynq-7000 All Programmable SoC Technical Reference Manual

- UG585 (v1.10) February 23, 2015
- <a href="http://www.xilinx.com/support/documentation/user\_guides/ug585-Zynq-7000-TRM.pdf">http://www.xilinx.com/support/documentation/user\_guides/ug585-Zynq-7000-TRM.pdf</a>

Opening Zynq7 Processing System IP configuration, from a newly created design:

- 1. Open Vivado
- 2. Create a new design
- 3. Walk through the wizard, making certain to choose the Zynq chip that you are intending to use.
- 4. Once you get to the Project Navigator page, click on "Create Block Design"
  - Create Block Design
- 5. Right click your mouse in the Diagram window and select Add IP

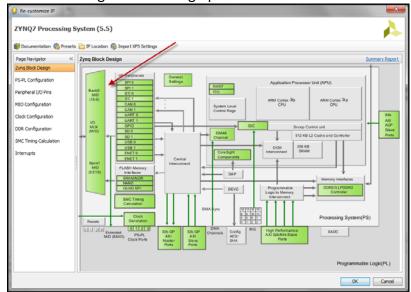
  P Add IP... Ctrl+I
- 6. Choose the ZYNQ7 Processing System



7. After you select Run Block Automation, you will see the below block



8. Double clicking this will bring up the Re-customize IP Selector



- 9. Double click the MIO MUX, shown by the red arrow in the above image
- 10. From here, you can select various peripherals and see valid configurations for the multitude of peripherals



## 9. Appendix C

AR# 59476 Zynq-7000 AP SoC: SD Programming/Booting Checklist

http://www.xilinx.com/support/answers/59476.html

## 10. Appendix D

MicroZed Linux microSD Card Image Archive

- http://microzed.org/support/documentation/1519
- Listed under Getting Started Guides section header

# 11. Appendix E

Various information sources regarding flash memory

- http://www.sandisk.com/assets/docs/WP001 Flash Management Final FINAL.pdf
- https://en.wikipedia.org/wiki/Write amplification
- https://en.wikipedia.org/wiki/Wear leveling
- General Information throughout documentation

