

V1-4	8 PIN	17-450-08
V5	9 PIN	17-450-09
AP1	200 OHM	71-201-11
S1	SWITCH	88-220-01
J1	9 PIN	17-310-09
J2,3	.250 FASTON	17-836-01
J4,5	.187 FASTON	17-894-01
J6	5 PIN	17-310-05
J7	2 PIN	17-310-02

2) UNLESS NOTED, RESISTOR VALUES IN OHMS, 1/2W-1% TOL. CAPACITOR VALUES IN MICROFARADS, 400V-5% TOL.

3) VOLTAGES ARE MEASURED WITH 1 MEGOHM DSCILLOSCOPE AND 10 MEGOHM DIGITAL VOLTMETER.

4) CIRCUIT GROUND → SWITCH GROUND + CHASSIS GROUND +

CAUTION:

THIS SCHEMATIC IS PROVIDED FOR USE BY QUALIFIED PERSONNEL. TO AVOID RISK OF ELECTRIC SHOCK, REFER SERVICING TO QUALIFIED SERVICE PERSONNEL. DO NOT PERFORM ANY SERVICING BEYOND THAT EXPLAINED IN THE OPERATING INSTRUCTIONS.

SIGNATURES:	DATE:	TISSO BORMAN DR.	
DRAWN: S.R.	2-21-91	63143	
CHK'D: L JACKSON		PROJECT NAME: VL-1001/1002	
APP'D: L JACKSON			
ORIGINAL ISSUED:	9/12/91	TUBE BD. SCHEMATIC	
PLOT DATE:	09/12/91		
PLOT TIME:	10:08:19	- DRAWING 07S491-01	
FILE NAME:	07549101	SCALE: NONE SHEET: 1 of 1	