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Document Revision History

Rev - : Initial Release

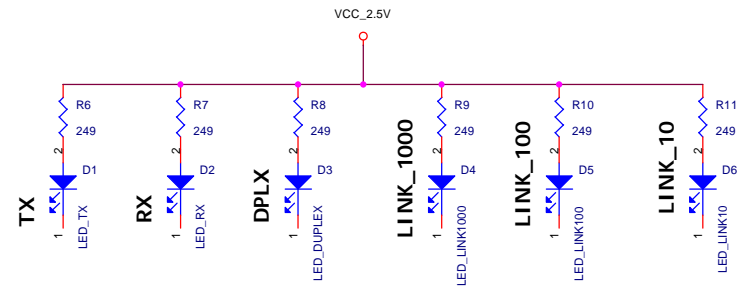
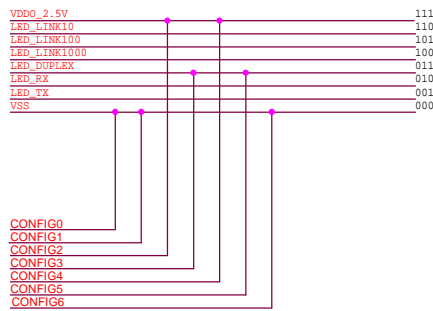
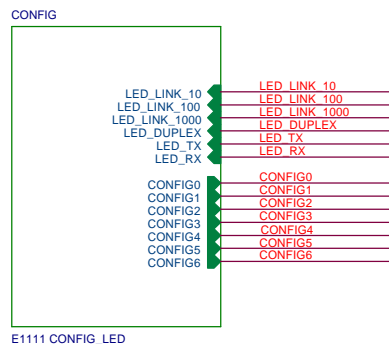
Overview

This is the reference schematics for the 88E1111-117TFBGA in the GMII to copper / fiber mode.

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Title		
Overview		
Size	Document Number	Rev
A	MV-L100209-01	-
Date: Thursday, May 08, 2003		Sheet 1 of 7



Pin To Constant Mapping	
PIN	BIT[2:0]
VDDO	111
LED_LINK10	110
LED_LINK100	101
LED_LINK1000	100
LED_DUPLEX	011
LED_RX	010
LED_TX	001
VSS	000

Pin To Configuration Mapping			
PIN	BIT [2]	BIT [1]	BIT [0]
CONFIG0	PHYADR[2]	PHYADR[1]	PHYADR[0]
CONFIG1	ENA_PAUSE	PHYADR[4]	PHYADR[3]
CONFIG2	ANEG[3]	ANEG[2]	ANEG[1]
CONFIG3	ANEG[0]	ENA_XC	DIS_125
CONFIG4	MODE[2]	MODE[1]	MODE[0]
CONFIG5	DIS_FC	DIS_SLEEP	MODE[3]
CONFIG6	SEL_BDT	INT_POL	75/50 OHM

000 PHY address '00000' and don't advertise the pause bit.

000

111 Autoneg enabled, advertise all capabilities, and prefer master. Auto crossover enabled, and disabled 125CLK option

011

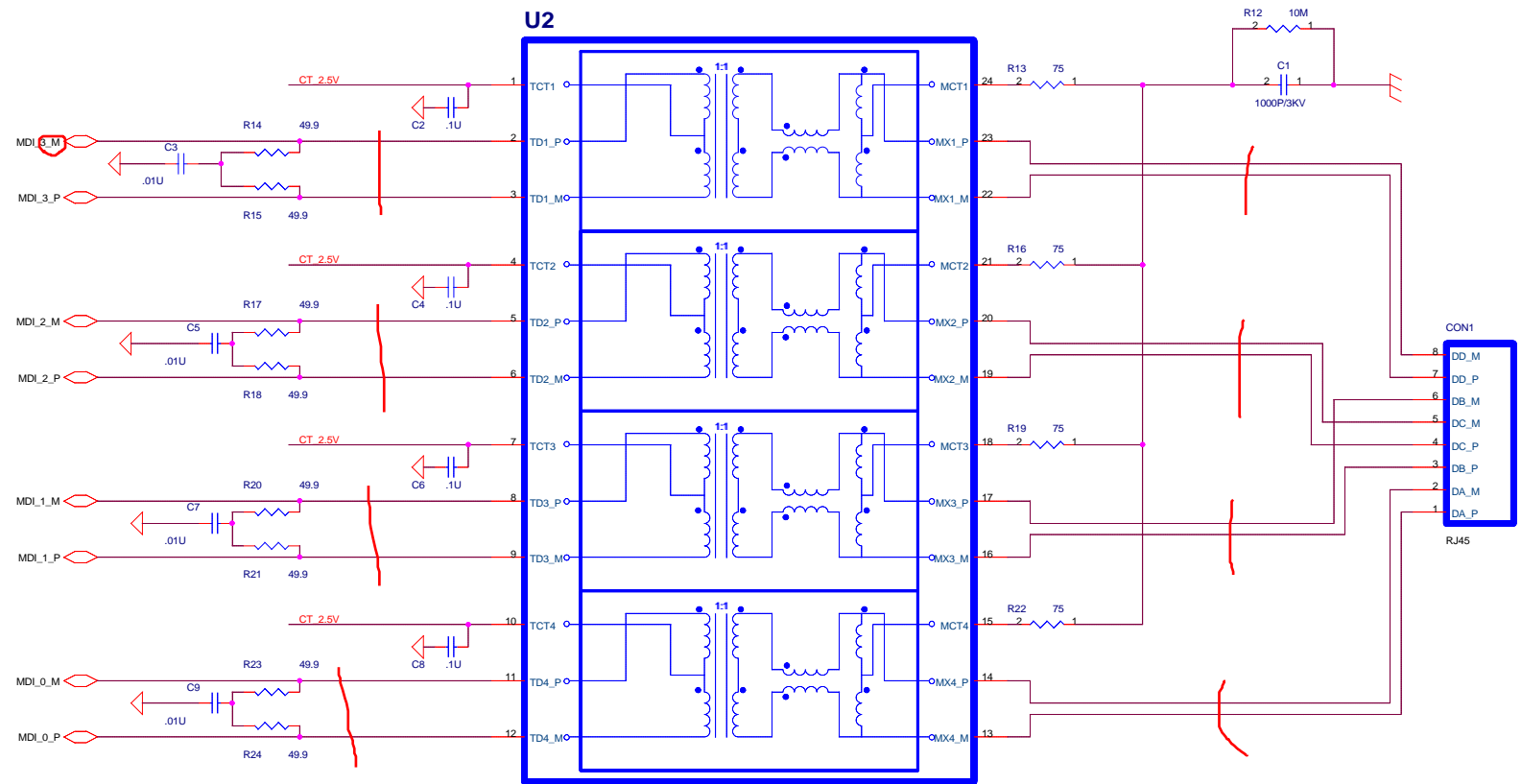
111 GMII to copper mode, fiber/copper auto detect enabled, and disabled sleep mode.

011

000 MDC/MDIO selected, active high interrupt, and 50 ohm serdes option.



Title		
<Title>		
Size	Document Number	Rev
B	MV-L100209-01	-
Date:	Thursday, May 08, 2003	Sheet 4 of 7

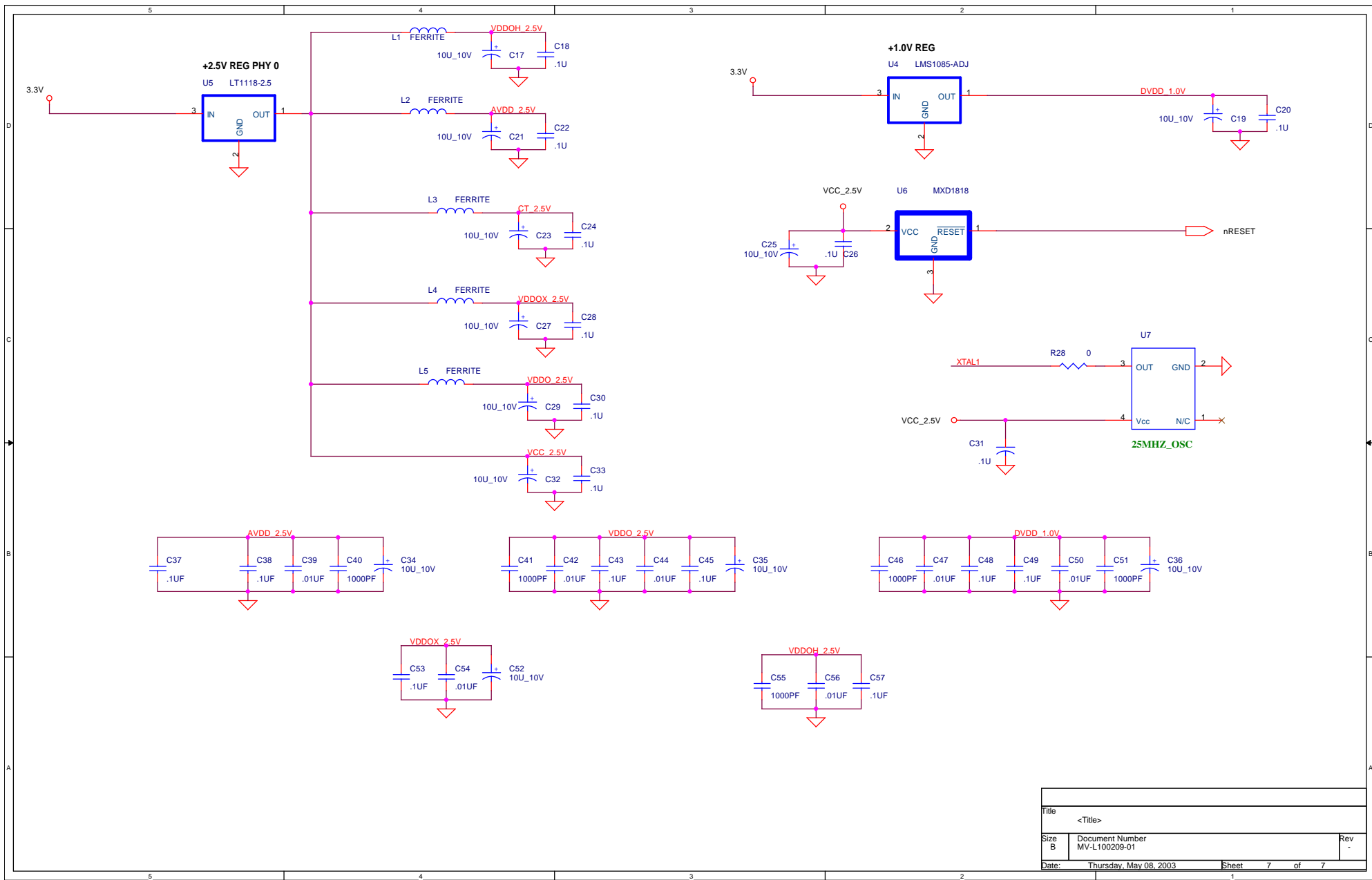


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Title			<Title>
Size	Document Number	Rev	
Custom	MV-L100209-01	-	
Date:	Thursday, May 08, 2003	Sheet	5 of 7



Title		
<Title>		
Size	Document Number	Rev
B	MV-L100209-01	-
Date:	Thursday, May 08, 2003	Sheet 7 of 7