MD. Rukunuzzaman Jibon



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Profile

I am a VLSI enthusiast. I gained knowledge of different technical subjects, especially electronics and VLSI-related courses, and developed skills in different related tools. Now, with my knowledge and passion, I am eagerly waiting to dedicate myself to the VLSI industry.

Thesis and Projects

Thesis: Design And Optimization Of Indium Arsenide Thin Film Solar Cell.

2021 - 2022

VLSI Project: Performing DC Analysis, Gain, Drawing Layout of CMOS Circuit in Cadence and Also Performing Design Rule Check (DRC) Error and Layout Vs. Schematic (LVS) Error.

2021 - 2022

Microprocessor Project: Vehicle Density Based Automatic Traffic Management with Emergency Override System via Android App.

2019

Skills

Programming Language — Competent C, C++

Harware Description Language — Competent Verilog

Embedded System — Competent Keil Microvision, STM32CubeMX

Circuit Design and Simulation — Competent Multisim

Education

B.Sc. in Electrical and Electronic Engineering,

Chittagong University of Engineering and Technology 2016 – 2023 | Chattogram, Bangladesh CGPA: 2.92 out of 4.00

Higher Secondary Certificate (H.S.C),

Birshreshtha Noor Mohammad Public College 2013 – 2015 | Dhaka, Bangladesh GPA: 5.00 out of 5.00

Secondary School Certificate (S.S.C),

Thanahat A.U. Pailot High School 2008 – 2013 | Kurigram, Bangladesh GPA: 5.00 out of 5.00

Training

Professional

15 days of technical training on DV, DFT & RTL from Ulkasemi Pvt. Limited August-2023

Academic

21 days of industrial training in Training Institute for Chemical Industries (TICI).

November-2018

Languages

Bangla — Native/Bilingual

English — Fluent

Organizations

CUET Rag of 2015 Batch, *Convenor* 2021

CUET Debating Society, *Organizing Secretary* 2019 – 2020

House of Language and Literature, CUET,

General Secretary 2019 – 2020

Interests

- VLSI
- Computer Science
- Internet of Things (IoT)
- Research and Development
- History and Geography
- Travelling