21L-5288 BCS-3C Muhammad Abdullah

Assembl Language (C, D, E) Fall 2022

Assignment-1

Total Marks:85

Submission: Submit hard copies. Deadline -Friday, 9th September 2022, 5:00 pm

Question 1: [0.2 marks per cell = 20 marks approx]

Fill the status of these flags and values of ax and bx after each instruction of the program is executed. Assume ax and bx are initially 0.

Program Instructions	OF	SF	ZF	AF	PF	CF	ax	bx
xor ax, ax	. 0	0	2	0	1	0	0000	0000
mov ax, 1	0	0	1	0	Ī	0	1	0000
add ax, 2	0	0	0	0	i	0	3	0000
add ax, 1	0	0	0	0	0	0	4	0000
mov bx, 0x3410	0	0	0	0	0	0	0004	3410
mov ax, 0xEECB	0	0	0	0	0	0	EECB	3410
add ax, bx	0	0	0	0	1	1	22DB	3410
xor bx, bx	0	0	1	0	1	0		0000
mov ax, -32768	0	0	1	0	i	0	8000	0000
add ax, -1	1	0	0	0	(10	1		0000
xor ax, ax	0	0	1	0	1	0	0000	0000
sub ax, 2	0	1	0	1	0	1	FFFE	0000

Question 2: [1 mark per register and memory cell = 13 marks]

Given this memory snapshot where memory starting from offset 0x123D, what will be updated memory values after the following snippet of code is executed. Also show the final values of ax, cx and dx registers. Show full working.

an				- Control						
7D	12	CF	5A	37	89	9A	BD	C9	34	

mov dx, 0x123E mov cx, 4 mov [0x123F], cx add dx, cx mov ax, [0x1242] mov [0x1243], ch mov word [0x123F], 5

Question 3: [4 marks]

Suppose CS=2667 and IP=107E and DS=199F then what should be the offset of DS to access the same memory location as CS: IP?

Offset for DS is (DCFE), working on attached Pages.

working or attached sheet

Question 4:
Given that: CS=0x5645,DS=0x1000, ES=0x6783,SS=0x0FFF, BX=0x4567,SI=0x1000,
DI=0x2000, RD=0x4500 Write the physical address of the memory locations read or written by the following instructions 12*6 - 12

instructions. : [2*6 = 12 marks]

• mov ax,[si] Ox 1 000 * mov ax,[st] Ox 1100 Ox 144FO

* mov ax,[sp] Gx 14500 Ox 144FO

· mov ax,[cs:bp+20] Ox 5A964

· mov ax,[bx+si+10] 0x16571

• mov [ss:bx+di],ax ⊙x 16557

mov [bp+si+0x200],200x111 FO

Question 5: [1*8 = 8 marks]

Identify which of the following are valid statements?

Identify which of the fo	Valid/Invalid? Justification?
Instruction	1.1. Cast stove 16 bit and operate
mov al, bx	I walls, Cutting the of the contraction of
add bx, bl	
mov 3, ax	imalia; register to addressable are to memory
mov IP, 5	iwalid: It is not addressable energy to memory invalid: Can't move data from memory location.
mov [0117], [0119]	valid; Can't move don't value at memory located.
mov [0117], 40	valid: Storing Coulcant videx registers as offset
mov ax, [si+di+100]	invalid; Carnol have Council be manifulated
add sp.2	invalid; Stack Pointer Cannot be manifulated
THE PARTY OF THE P	directly.

Question 6: [2*9 = 18 marks] Calculate the physical address generated by the following segment: offset pairs. Show working on attached Sheet calculation.

	U	
Logical Address	Physical Addr	ress
FFFF:4006	0×103FF6	
AB01:FFFF	Ox BBOOF	
1DDD:0436	Ox 1E206	
1234:7920	0x19C60	
74F0:2123	0×77023	
0000:6727	0× 6727	
FFFF:4336	0×104326	
1080:0100	0×10400	
AB01:FFFF	0× BBOOF	

Question 7: [2*5 = 10 marks]

What are the first and the last physical memory addresses accessible using the following segment values?

a. 1000

First = 600x10000 last = Ox IFFFF

10000 + FFFF

- b. 0FFF c. 1002 d. 0001 e. E000
- (b) OFFF: First = 0x OFFFO Cast = 0x 1FFEF
- (c) 1002: First = 0×10020 Past = $0 \times 2001F$
- (d) 0001: First = 0 × 00010 Past = 0 × 1000F
- (e) E000:first=QE0000

 Bost=OxEFFFF

 Lost=OxEFFFF

