

BRENDAN ENGH

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EDUCATION

Santa Clara University, School of Engineering

Santa Clara, CA

M.S. in Electrical & Computer Engineering

Expected Graduation June 2022

- Focus: Digital Systems

B.S. in Electrical & Computer Engineering

Graduated June 2020

- Minor: Computer Science & Engineering

RELEVANT EXPERIENCE

Robotics Systems Lab – Santa Clara University

Santa Clara, CA

Satellite Operator and Engineering Intern

June 2019 – March 2020

- Utilized ground station software and hardware to perform round-the-clock mission-critical tests, calibrations, and communication to ensure reliable uplink to NASA and private industry satellites
- Received Technician Class Amateur Radio License and worked with the Campus Safety Director to develop emergency communication between SCU satellite facilities

Underwriters Laboratory

Fremont, CA

Web Development Intern

June 2019 – August 2019

- Tested and developed a project management web application for all UL offices that resulted in significant time and cost savings
- Found 200+ bugs and issues using Python and the Selenium library to create scripts that simulated user activity within all modules of the web app
- Fixed bugs within GitHub repository using MongoDB, React.js, and Node.js

Mobile Solar

Atascadero, CA

Engineering Intern

June 2018 – July 2018

- Created electrical schematics using CAD software to obtain city permits for customers
- Used Excel to code self-populating line-setting tickets that efficiently provided factory workers with detailed model specifications for ongoing orders
- Assembled off-grid solar array units and performed quality assessments

RELEVANT PROJECTS AND COURSEWORK

Adaptive Navigation Utilizing a Drone Cluster, Senior Design

- Researched, designed, and integrated communication and control systems to fly multiple drones simultaneously as a single cluster
- Worked with group members to document, present, and demonstrate our drone system for research on adaptive navigation

Advanced Computer Architecture

- Built and simulated five-stage pipeline with hazard detection, forwarding, and caching using Verilog

PCB Speaker, Junior Design

- Used Eagle to create schematic and layout for a speaker PCB

Additional Coursework

- Real-Time Embedded Systems, Digital Signal Processing, Digital Integrated Circuit Design

ADDITIONAL SKILLS AND LICENSES

- Coding Languages: C/C++, Python, JavaScript, Verilog, ARM Assembly
- Software: MATLAB, Eagle, Synopsis Custom Compiler, Vivado, LTspice, LabVIEW
- Licenses: FAA Small UAS License, Technician Class Amateur Radio License

INTERESTS

- Investing, Traveling, Fishing, Surfing, Skiing