Candidates are required to give their answers in their own words as far as practicable.

Figure in the margin indicate full marks.

Group A

Answer TWO questions.　　　　　　　　　　　　　2×12=24

1. Explain Microprogrammed control unit. Also explain about address sequencing　　8+4

2. Explain Booth's algorithm using flowchart and compute $(1001)_2 \times (1011)_2$ using it.　　4+8

3. Describe about the operation of I/O interface using block diagram.

Group B

Answer SEVEN questions.　　　　　　　　　　　　7×8=56

4. Draw a basic computer architecture using block diagram and explain it.

5. Explain about various types of computer registers.

6. How CISC machine operates? Explain.

7. Explain concepts of parallel processing.

8. Explain about pipelining and show basic example showing its efficiency.

9. Explain about vector processor.

10. Write about data transfer using interrupt.

11. What is Auxiliary memory? Explain in brief.

12. Discuss on characteristics of multiprocessor system.

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