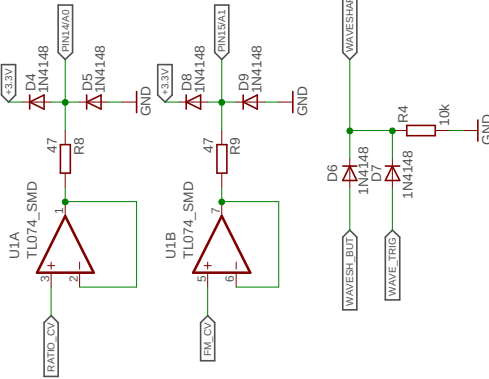
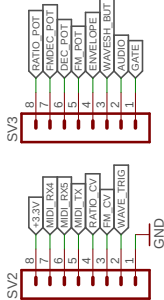


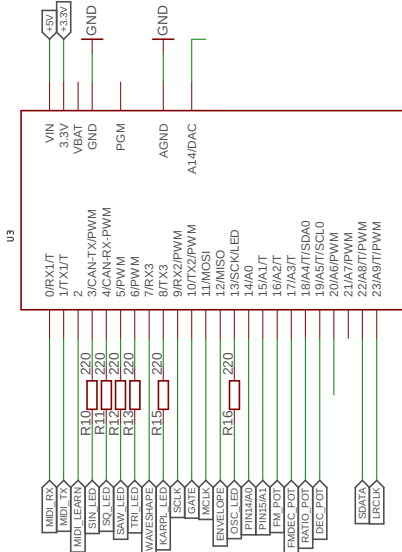
MIDI Receiving



CV IN stages

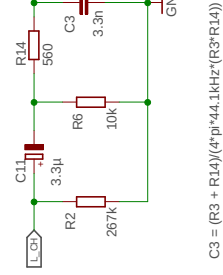


Header connectors
between boards



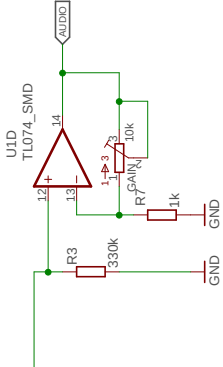
TEENSY 3.1 BASIC

Reconstruction Filter

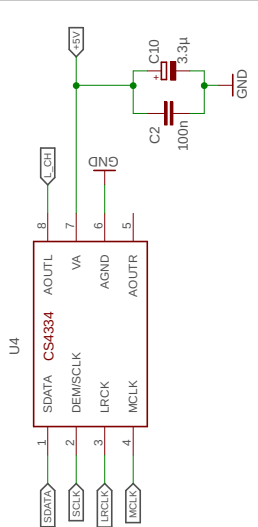


$$C3 = (R3 + R14) / (4 * \pi * 44.1 \text{ kHz} * (R3 * R14))$$

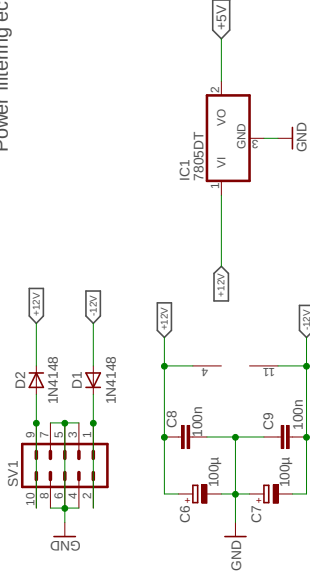
Output buffer/amplifier



DAC



Power filtering ect



Halldorsson

TITLE: Board-Back

Document Number:

REV:

Date: 26/11/2018 15:26

Sheet: 1/1