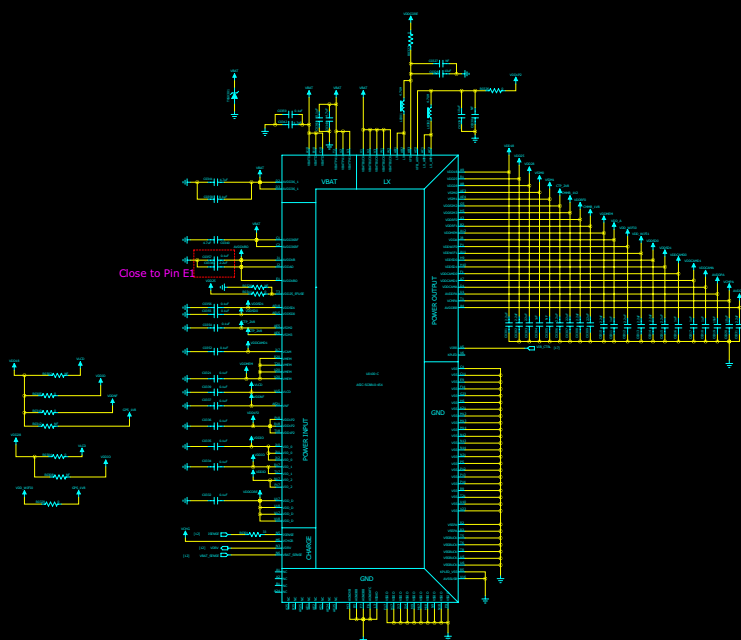


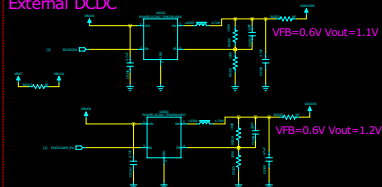


I2C1 For Camera
I2C0 For G/M/L-Sensor
I2C3 For FM

BB_POWER



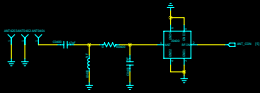
External DCDC



LDO	Output Voltage (V)	Output Current	Used Voltage
VDD18	1.2/1.5/1.8/2.8	200mA	1.8V
VDD25	2.5/2.75/2.9/3.0	60mA	2.5V
VDD28	1.8/2.65/2.8/3.0	200mA	2.8V
VSIM0	1.8/2.9/3.0/3.1	60mA	1.8/3.0V
VSIM1	1.8/2.9/3.0/3.1	60mA	1.8/3.0V(N/A)
VDDSIM2	1.2/1.8/2.8/3.0	200mA	2.8V
VDDSIM3	1.2/1.8/2.8/3.0	200mA	1.2V
VDDRF0	1.8/2.75/2.85/2.95	200mA	2.85V
VDDRF1	1.8/2.5/2.85/2.95	200mA	1.8V
VDDMEM	1.8	300mA	1.8V
VDDA	1.8	80mA	1.8V
VDDWIF0	1.2/1.8/2.8/3.3	200mA	1.8V
VDDWIF1	1.2/1.8/2.8/3.3	200mA	1.8V
VDDSD0	1.8/2.5/2.8/3.0	150mA	3.0V
VDDSD1	1.8/2.5/2.8/3.0	150mA	1.8V
VDDCAMD0	1.3/1.5/1.8/2.8	100mA	1.8V
VDDCAMD1	1.2/1.8/2.8/3.3	100mA	1.8V
VDDCAMA	1.8/2.5/2.8/3.0	150mA	2.8V
AVDDBB	2.8/2.9/3.0/3.1	60mA	3.0V
AVDDVB	2.9/3.2/3.3/3.4	100mA	3.3V
VDDUSB	3.1/3.2/3.3/3.4	60mA	3.3V
VBATBK	2.6/2.8/3.0/3.2		2.8V
VDDRTC	1.5/1.6/1.7/1.8		1.8V
VDDCORE	0.7/0.8/0.9/1.1/1.2/1.3	400mA	1.1V
VDDARM	0.7/0.8/0.9/1.1/1.2/1.3	500mA	1.2V

Title		S0810_07810_1_30x_V1.0.0	
Author		Hardware DEPT.	
Date		2011-03-08	
Drawing No.		S0810_07810_1_30x_V1.0.0	
Drawing Date		2011-03-08	

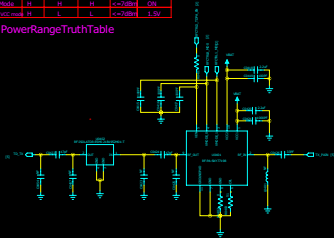
RF ANT



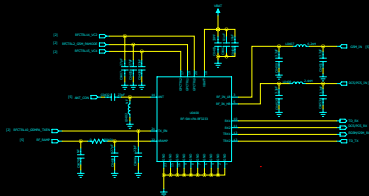
TD PA

Power Mode	VEN	Vmode0	Vmode1	Range	VCC
Power Class Mode	L	L	L	--	ON
Standby Mode	L	L	L	--	ON
High Power Mode	H	L	L	37.26dBm	ON
Medium Power Mode	H	L	L	37.26dBm	ON
Low Power Mode	H	H	H	<+20dB	ON
External Control VCC mode	L	L	L	<+20dB	1.5V

PowerRangeTruthTable



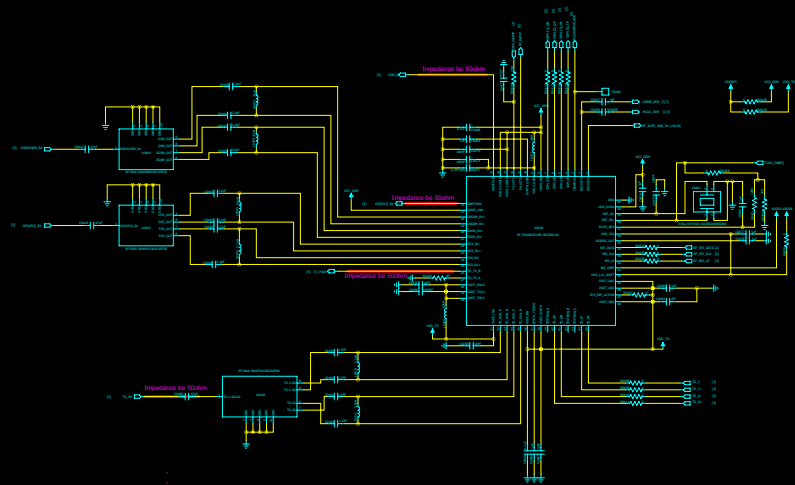
GSM PA AND RF SWITCH



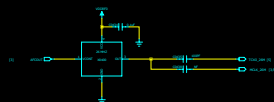
Antenna Switch Control Logic

Mode	TX EN	gctrl0	gctrl1	gctrl2
TD_RX	L	H	L	L
ESPRESSO_RX	L	L	H	L
ESPRESSO_TX	L	L	H	L
TD_TX	L	L	L	H
LOW BAND GSM	H	L	H	L
HIGH BAND GSM	H	L	H	L
LOW BAND GSM	H	L	L	L
HIGH BAND GSM	H	L	L	L

RF TRANCEIVER



26MHz TCXO Module

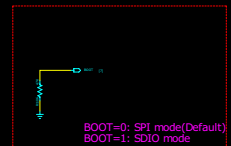
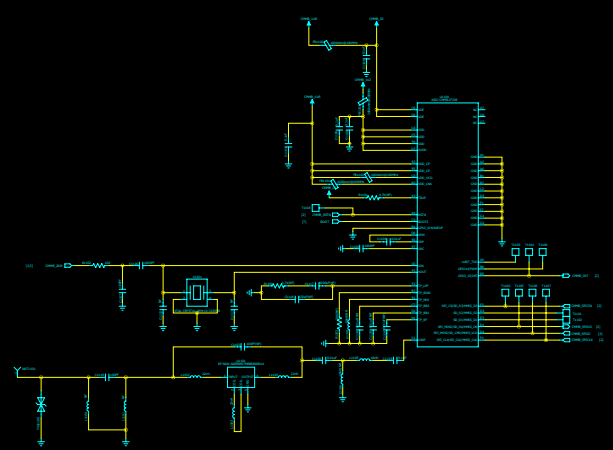


Title: SC8810, SP8810, 1.5GHz V1.0.0	
Revision: 1.0	
Author: Design Dept.	Designer: Design Dept.
Checker: Design Dept.	Checker: Design Dept.
Appr: Design Dept.	Appr: Design Dept.
Category: Design Dept.	Category: Design Dept.

[illegible]

Rev	Rev	Rev	Rev
1.0	1.0	1.0	1.0
1.0	1.0	1.0	1.0
1.0	1.0	1.0	1.0

CMMB

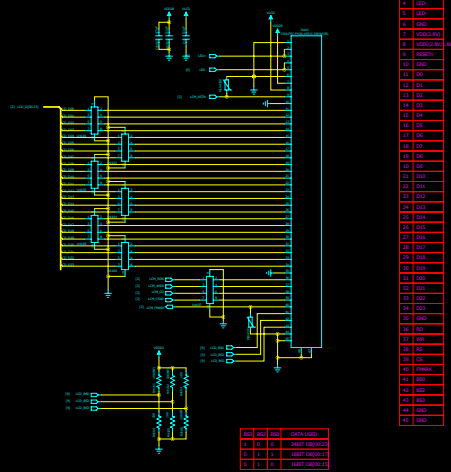


BOOT=0: SPI mode(Default)
BOOT=1: SDIO mode

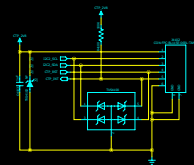
SCH-01_SCH-01_1.0			
Hardware DEPT.			
Design	2011-01-18	Design	2011-01-18
Check	2011-01-18	Check	2011-01-18
Rev	2011-01-18	Rev	2011-01-18

Revision History		
Rev	Reason	Author
1	Initial Release	W. Wang

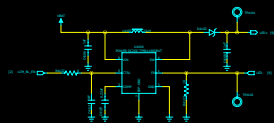
3.5" HVGA LCM



3.5" Capacitive TP



Back Light Driver for 7LEDs In Series



Revision History		
Rev	Reason	Author
1	Initial Release	W. Wang

SC8810, SP8810, 1.5CH, V1.0.0

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Trademark: SP8810

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

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SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

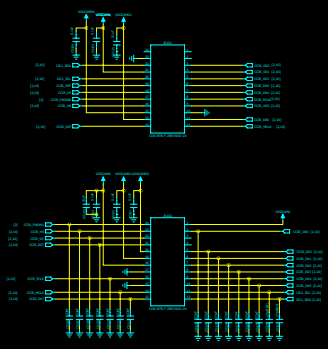
SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0

SP8810, SP8810, 1.5CH, V1.0.0



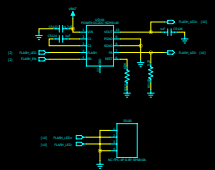
Sensor Module:0.3M+5M



Pin	NET	Pin	NET
01	GN0	21	Q1
02	GN0	22	Q2
03	GN0	23	Q3
04	AVDDQ2.BV	24	Q4
05	SLA	25	Q5
06	VS0M2	26	Q6
07	VS0M2	27	Q7
08	VS0M2	28	Q8
09	VS0M2	29	Q9
10	VS0M2	30	Q10
11	VS0M2	31	Q11
12	VS0M2	32	Q12

Pin	NET	Pin	NET
01	GN0	21	Q1
02	GN0	22	Q2
03	GN0	23	Q3
04	GN0	24	Q4
05	GN0	25	Q5
06	GN0	26	Q6
07	GN0	27	Q7
08	GN0	28	Q8
09	GN0	29	Q9
10	GN0	30	Q10
11	GN0	31	Q11
12	GN0	32	Q12

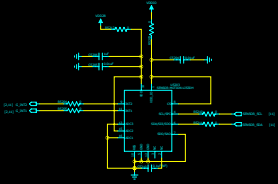
FLASH LED



Revision		SCM010-SPB010-1-000-V1.0.0	
Version		1.0.0	
Author		Hardware DEPT.	
Date		2013-10-10	
Project		Spreadtrum communications, Inc.	
Company		Spreadtrum communications, Inc.	
Product		SPB010-SPB010-1-000-V1.0.0	
Part		SPB010-SPB010-1-000-V1.0.0	

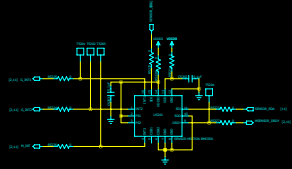
REV	DESCRIPTION	DATE	BY
1	Initial Release	2011-03-14	HW
2	Added Pin 10	2011-03-14	HW
3	Added Pin 11	2011-03-14	HW
4	Added Pin 12	2011-03-14	HW

G Sensor



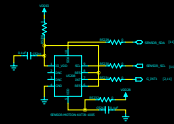
CS=1: IIC
SDA=0: IIC ADDRESS is 0x2B
power down(communicating), I = 0.5uA

G&MAG Sensor



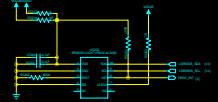
power down, I < 3uA
IIC ADDRESS: GSENSOR:0x2B
IIC ADDRESS: HSENSOR:0x2D

G Sensor

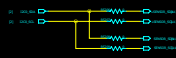


IIC ADDRESS is 0x2B
power down(communicating), I = 0uA

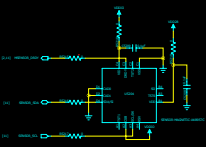
Ambient Light Sensor with Proximity Sensor



power down, I < 1uA
IIC ADDRESS is 0x2B
U202 and U203 must be isolated carefully.

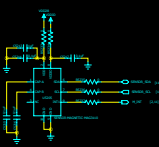


MAG Sensor



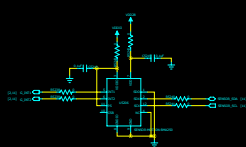
CS=1: IIC
IIC ADDRESS is 0x2B
power down(communicating), I < 0.5uA

MAG Sensor



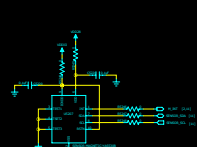
IIC ADDRESS is 0x2B
power down(communicating), I = 3uA

G Sensor



PS=1: IIC
IIC ADDRESS is 0x2B
power down(communicating), I = 0.5uA

MAG Sensor



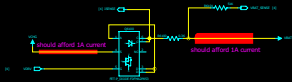
IIC ADDRESS is 0x2B
power down(communicating), I = 1uA

File Name: G20810_09810_1_SCH_V1.0.0

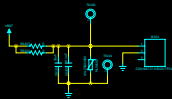
REV	DESCRIPTION	DATE	BY
1	Initial Release	2011-03-14	HW
2	Added Pin 10	2011-03-14	HW
3	Added Pin 11	2011-03-14	HW
4	Added Pin 12	2011-03-14	HW

Rev	Description	Author	Check
1	Initial Release	Wang, Jia	Wang, Jia
2	Update	Wang, Jia	Wang, Jia
3	Update	Wang, Jia	Wang, Jia
4	Update	Wang, Jia	Wang, Jia
5	Update	Wang, Jia	Wang, Jia
6	Update	Wang, Jia	Wang, Jia

CHARGE



BATTERY



SC8810_SPS810_1_SCH_V1.0.0

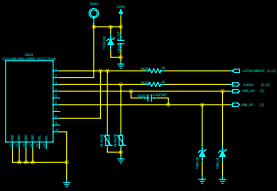
Rev	Description	Author	Check
1	Initial Release	Wang, Jia	Wang, Jia
2	Update	Wang, Jia	Wang, Jia
3	Update	Wang, Jia	Wang, Jia
4	Update	Wang, Jia	Wang, Jia
5	Update	Wang, Jia	Wang, Jia
6	Update	Wang, Jia	Wang, Jia

10pin USB

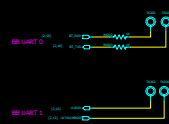
```

USB BOOT CONTROL
NBOOT = Float----->NAND BOOT
NBOOT = 0      ----->USB BOOT

```



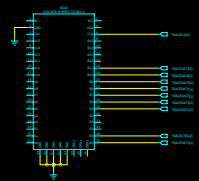
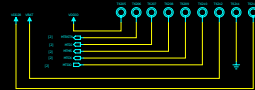
Test Points

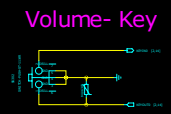
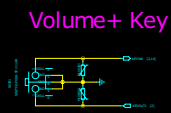


MARK POINT

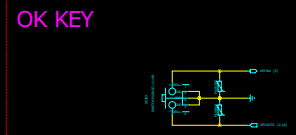
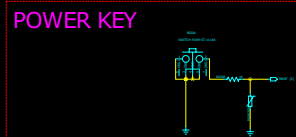


ARM JTAG



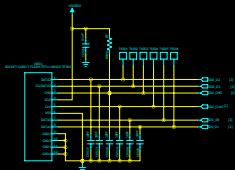


Press KEYIN0+KEYOUT0 when power up
will make the phone boot from USB



Rev	Description	Author	Date
1	Initial Design	John Doe	2013-01-15
2	Added Component X	John Doe	2013-02-01
3	Final Review	John Doe	2013-02-10

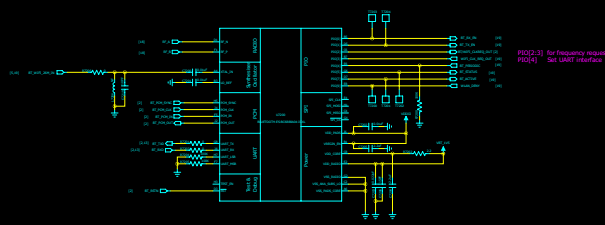
T_Flash



Project Name		SC2610_SPRINT_1_SCH_V1.0.0	
Project No.		123456789	
Author	2013-01-15	Hardware Dept.	
Rev	2013-01-15	Project	Spreadtrum communications, Inc.
Rev	2013-01-15	Reviewed By	John Doe

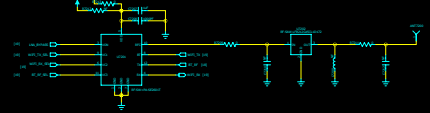
Rev	Rev Description	Author	Date
1	Initial Release	Hardware Dept.	2011-08-18
2	Minor Updates	Hardware Dept.	2011-08-18
3	Major Updates	Hardware Dept.	2011-08-18

BT



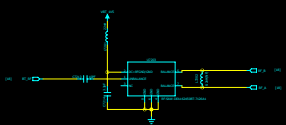
PRO2.3 for frequency request
PRO2.3 for UART interface

BT&WIFI RF



Control Logic Table

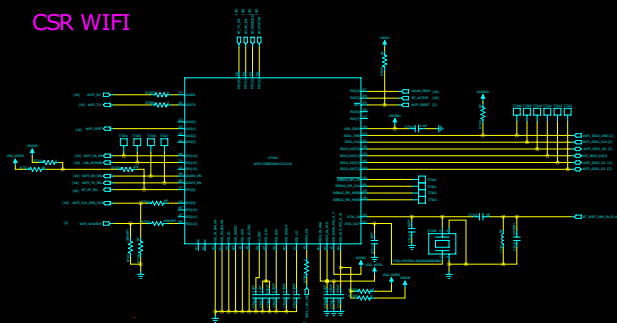
Mode Description	VC1	VC2	VC3	VC4
ALL OFF	0	0	0	0
BT OFF	0	0	1	0
WIFI ON	1	0	0	0
WIFI RF High Gain	0	1	0	0
WIFI RF Low Gain	0	0	1	0



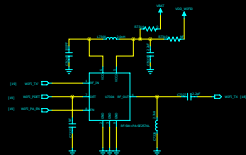
Rev	Rev Description	Author	Date
1	Initial Release	Hardware Dept.	2011-08-18
2	Minor Updates	Hardware Dept.	2011-08-18
3	Major Updates	Hardware Dept.	2011-08-18

REV	DESCRIPTION	DATE	BY	CHK
1	Initial Release	2011-05-18	Wang, Jia	
2	Update BOM	2011-05-18	Wang, Jia	
3	Update BOM	2011-05-18	Wang, Jia	
4	Update BOM	2011-05-18	Wang, Jia	
5	Update BOM	2011-05-18	Wang, Jia	
6	Update BOM	2011-05-18	Wang, Jia	

CSR WIFI



WIFI PA



CSR WIFI PA

REV	DESCRIPTION	DATE	BY	CHK
1	Initial Release	2011-05-18	Wang, Jia	
2	Update BOM	2011-05-18	Wang, Jia	
3	Update BOM	2011-05-18	Wang, Jia	
4	Update BOM	2011-05-18	Wang, Jia	
5	Update BOM	2011-05-18	Wang, Jia	
6	Update BOM	2011-05-18	Wang, Jia	

Change_List

V1.0.0
SC8810 SMARTPHONE SCH V1.0.0 Release