

# Parshvanath Charitable Trust's

# A. P. SHAH INSHMUMD OF TECHNOLOGY

(Approved by AICTE New Delhi & Govt. of Maharashtra, Affiliated to University of Mumbai) (Religious Jain Minority)

# **Department of CSE Data Science UNIT TEST – I**

### Academic Year 2023-24

Class: TEDS Semester: VI Subject: CSDLO6011 High Performance Computing

Date: 22<sup>nd</sup> Feb 2024 Time: 2:00 to 3:30pm Max marks: 40

#### Note the following instructions

1. All questions are mandatory (Q.1, Q.2, Q.3)

2. Draw neat diagrams wherever necessary.

3. Write everything in Black ink (no pencil) only.

4. Assume data, if missing, with justification.

Q. N.	Questions	MARKS	СО	Blooms Taxonomy Level	PO
Q.1	Attempt any two.				
1	Explain Demand Driven Computation. Mention one Advantage and one Disadvantage of this model.	[5]	CO1	L2	
2	Outline the fundamental limits of serial computing.	[5]	CO1	L2	
3	Explain SPMD Model.	[5]	CO1	L2	
4	Compare and Contrast Data and Task Level Parallelism.	[5]	CO1	L2	
Q.2	Attempt any two				
1	1. Identify the above network topology in parallel platforms and explain it in detail.  2. Make use of 8 processors to calculate the following for the above network:	[10]	CO2	L3	PO1, PO12

	D'			1	
	a. Diameter				
	<ul><li>a. Bisection Width</li><li>a. Arc Connectivity</li></ul>				
	a. Cost (No. of links)				
2	Pl pl pl pl write #3, x    x = 1	[10]	CO2	L3	PO1, PO12
	P0 P1 P0 P1				
	Consider the following initial state of two processors P0 and P1. If there is a write operation performed on P0 with a new value x=3. <b>Apply</b> Invalidate Protocol to construct the parallel program execution flow with the simple three-state coherence protocol (Shared, Invalid and Dirty). <b>Develop</b> a state machine diagram for invalidate protocol of cache coherence. Also, <b>Explain</b> the Invalidate Protocol for Cache Coherence in Multiprocessor Systems.				
3	Calculate the communication cost using Store and Forward Routing mechanism for the following scenario: We need to send 20 words from Processor P0 to P7, the path has 14 communication links, the startup time required for the communication is 10s, per-hop time is 12s and time required for transfer of each word is 7s. Also, Explain Communication Costs in Parallel machines with respect to Store and Forward Routing in detail.	[10]	CO2	L3	PO1, PO12
Q.3	Attempt any one				
1	Consider the execution of the query:  MODEL = "CIVIC" AND YEAR = 2001 AND (COLOR = "GREEN" OR COLOR = "WHITE") on the following database:	[10]	CO3	L3	PO1, PO12
	ID#   Model   Year   Color   Dealer   Price				

													_					1		1	1 1
		<ol> <li>Decompose the problem in multiple tasks</li> <li>Build a task dependency graph for the tasks identified</li> </ol>																			
															tified						
		3. <b>Select</b> appropriate mapping of tasks to different processors taking into account data and control																			
						ng i	nto	ac	cou	nt	data	an	d c	cont	trol						
			depend																		
		4.	Assum									ısk	s a	t ea	ıch	le	vel and				
	Calculate the critical path length  5. Calculate the average and maximum degree of																				
		concurrency																			
	6. <b>Create</b> a task interaction graph based on decomposition													osition							
	you have performed																				
	7. <b>Comment</b> on the shortest parallel execution time for the																				
	decomposition you have done for this problem.  Consider the following array for a sorting problem using Quick																				
2					ving	arra	ıy	for a	a so	rt	ing p	rob	ole	m u	ısin	g	Quick	[10]	CO3	L3	PO1,
	Sor	t Al	gorithm	1.																	PO12
	5	17	4 18	25	63	0	8	7	9	١.	1 23	1	5	3	45	:	27				
	For the parallel execution of the above serting problem engineer																				
	For the parallel execution of the above sorting problem answer the following:  1. Select the most optimal decomposition technique for the																				
		1.																			
		above problem and justify the reason for your selection  2. <b>Decompose</b> the problem using quick sort																			
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		٥.	step 2	a tas	ok uc	pen	uc.	псу	gra	ŀΙ	1101	tiic	ıa	SKS	Iuc	١1١ر	illica ili				
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	4. <b>Select</b> appropriate mapping of tasks to different processors taking into account data and control																				
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		5	Assum			ctc a	1001	ocia	ted	TX.	rith to	ac le	c a	t ea	ch	163	vel and				
		٥.										iSK.	s a	t Ca	CII	IC.	ver and				
		<ul> <li>calculate the critical path length</li> <li>6. Calculate the average and maximum degree of concurrency</li> <li>7. Create a task interaction graph based on decomposition you have performed</li> </ul>																			
		8	8. <b>Comment</b> on the shortest parallel execution time for the																		
		0.																			
		decomposition you have done for this problem.																			