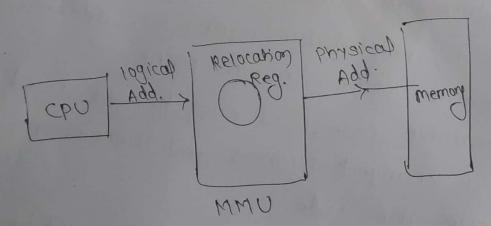
Logical and physical Addresses

In multiprogramming o.s. two types of addresses are generaled.



tig: mapping virtual addresses to 1

parameter	Logical Address	Physical Address
Basic	generaled by CPU	location in a memory
Address	logical address space is set of all logical addresses generaled by cpu in reference	physical address space is a set of all physical addresses mapped to the corresponding logical addresses.
vieibility	no a progress. User can view the logical address of a program.	user can never view physical address of program.
Generation	generated by cru	computed by mmu.
Access	The user can use the logical address to access	the user can indirectly access physical address but not directly.
	the physical address.	