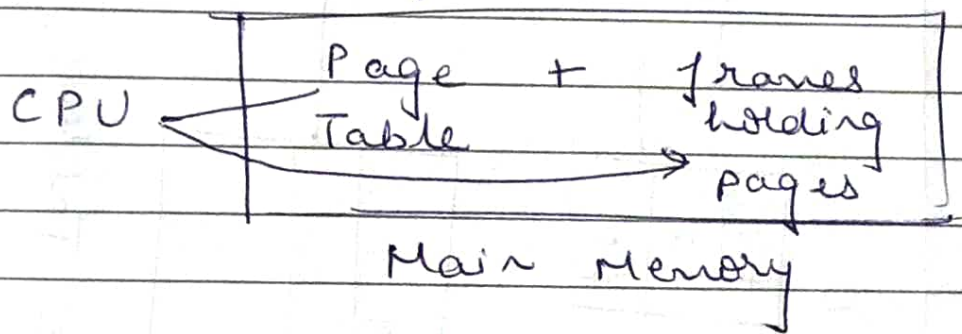


TLBDisadvantage of paging

- 17 ^{When} CPU generates logical address, it refers to page Table in main memory & when CPU finds the page it again refers main memory to pick that particular page in main memory for execution. Here there is wastage of time.



To overcome this problem we use TLB.

TLB stands for Translation Lookaside Buffer.

P.T.O

