



Academic Year: 2022-23
Class/Branch: SE

Semester: IV
Subject: Microprocesso

Operating Modes of 80386

80386 can operate in 3 modes

1. Real Mode
2. Protected Mode
3. Virtual 8086 Mode

Real Mode:

It is the default mode selected when 80386 is reset. In this mode 80386 simply behaves as a fast 8086 machine (fast because its operating frequency is higher than 8086).

All the registers are just like 8086. Even the memory used is only 1 MB, just like 8086.

Physical address calculation is also like in 8086:

$$PA = BA \times 10H + \text{Offset Address}$$

This mode is basically used to run the BIOS on reset.

i.e., 80386 starts in real mode and it creates the environment for protected mode i.e., it initializes all the registers and tables like GDT, LDT, TSS, Page table etc so that the protected mode can work.

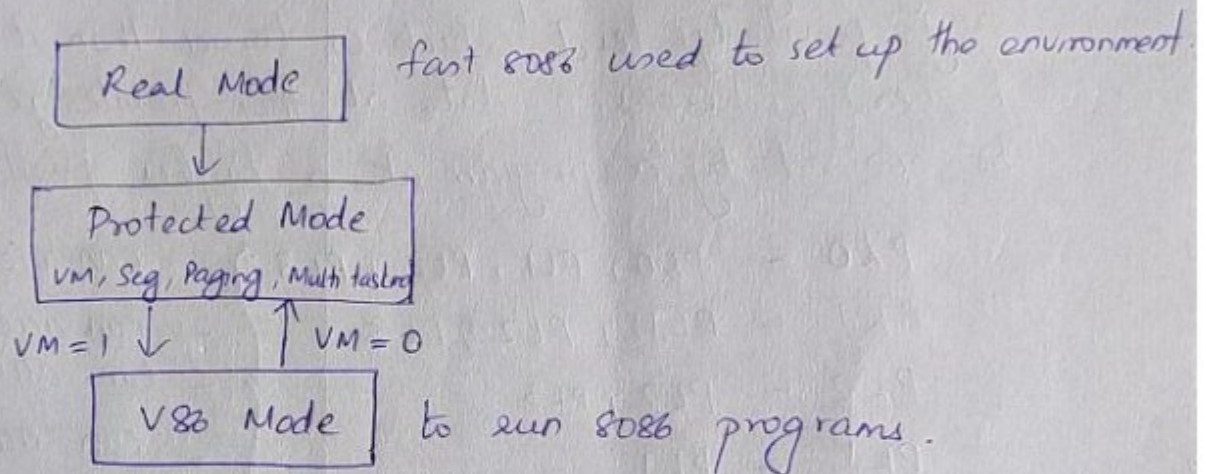
Once the required registers are initialized, we can switch to Protected Mode by making the Protection Enable (PE bit) = 1 in CR0.



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Note :- We can't switch back to real mode from protected mode. But we can go to Virtual 8086 mode by making VM bit in flag register a 1. Here 8086 programs can be run and by making VM=0, we can go back to protected mode.



Protected Mode :

In protected mode, 80386 up provides dedicated hardware to prevent user programs from affecting other user programs and also safeguards the OS from being affected by user programs.

There are 4 privilege levels assigned to programs and data to define their privileges.

Level 0 : Most privileged - OS kernel

Level 1 : 2nd most privileged - S/m Services
such as File Handling, Device Drivers etc.

Level 3 : 3rd most privileged - Custom

Extensions of OS

Level 4 : least Privileged - User applications & programs, Dept. of Computer Engineering





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Note: - Any program at level 0 can access all the data at any privilege level, whereas a data at this privilege level can be accessed by a program at privilege level 0 only.

ie, Any program at a particular level can access the data at any privilege level which is lower than it (numerically higher), whereas a data at this privilege level can only be accessed by a program at a higher privilege level.

PL0	-	PL0, PL1, PL2, PL3	-	can access
PL1	-	PL1, PL2, PL3	-	can access
PL2	-	PL2, PL3	-	can access
PL3	-	PL3	-	can access

Real Mode

Segment Registers:

6, 16 bit segment registers
CS, SS, DS, ES, FS and GS
segment registers give base address of respective segments

Protected Mode

Segment Registers:

6, 16 bit segment registers
Segment registers do not directly give segment base address. Instead they give a "selector" which is an index in the LDT from which the descriptor is loaded. It is the descriptor which gives the base address of the segment.



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Offset Registers

5, 16 bit offset registers containing offset addresses for various segments (IP, SP, BP, SI and DI)

General Purpose Registers

4, 16 bit GPRs (AX, BX, CX and DX)
Can also be used as 8, 8 bit registers
AL, AH, BL, BH, CL, CH, DL and DH

Flag Register

Only the lower 12-bits of the flag register are used in Real Mode.

Control Registers

Only LSB of CR0 is available in Real Mode.

Debug and Test Registers

Not available in Real Mode.

16 bit operations

Default mode after Reset.

Offset Registers:

5, 32 bit extended offset registers (EIP, ESP, EBP, ESI and EDI)
They give 32 bit offset addresses

General Purpose Registers

4 32 bit GPRs (EAX, EBX, ECX and EDX)

Flag Register.

80386 has a 32-bit flag register called EFLAGS used in protected mode

All 4 Control Registers available
CR0, CR1, CR2 and CR3

Debug and Test Registers

Available only in protected mode.

32 bit operations

Protected mode entered by making LSB of CR0 (PE=1)



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Memory Range:

Size of mly available in real mode is 1 MB and has address range from 0000H ... FFFFH, just like in 8086.

16 bit offset \therefore Maximum size of a segment is 64 KB

$$PA = \frac{\text{Segment Address}}{\text{Address}} \times 10H + \text{Offset}$$

No paging, protection and multitasking.

I/O Range: A total of 64K I/O addresses are available having a range from 0000 ... FFFFH, just like in 8086.

Total physical memory
 $2^{32} = 4 \text{ GB}$.

32 bit offset \therefore Maximum size of a segment is 4 GB.

Complex segmentation and paging mechanisms.

Paging, Protection and Multitasking can be done.

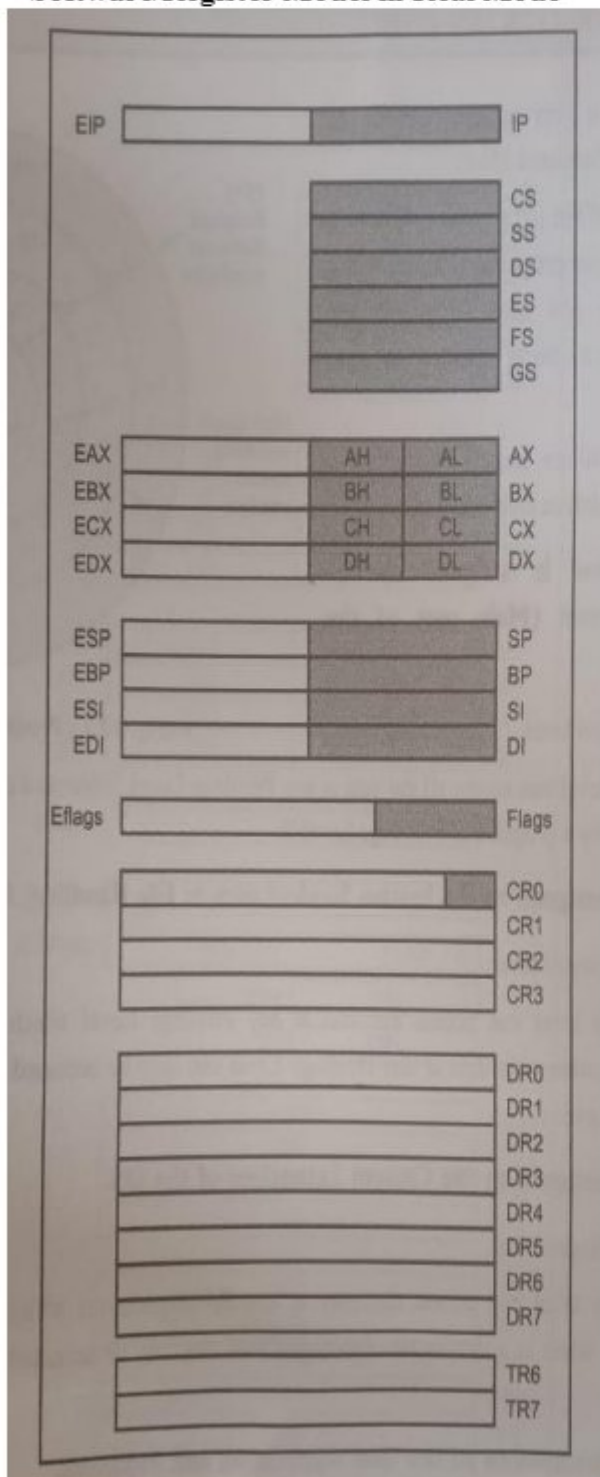
64K I/O addresses available



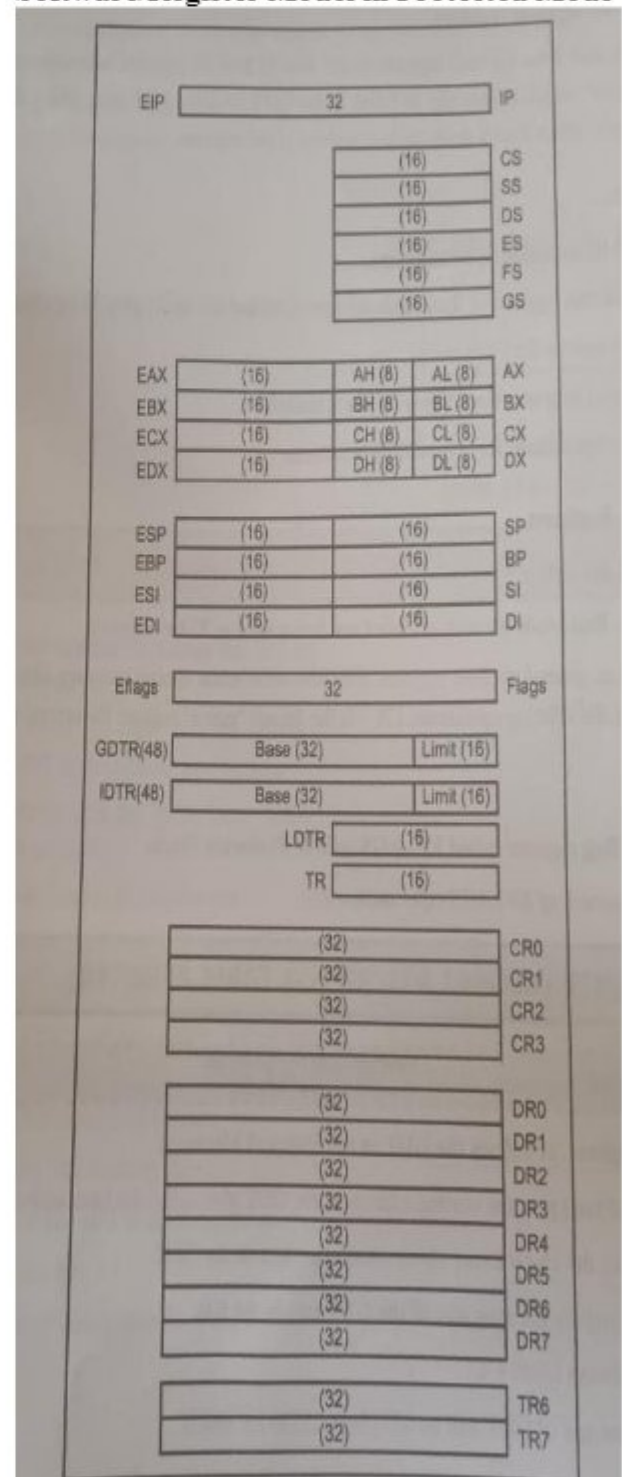
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Software/Register Model in Real Mode



Software/Register Model in Protected Mode





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