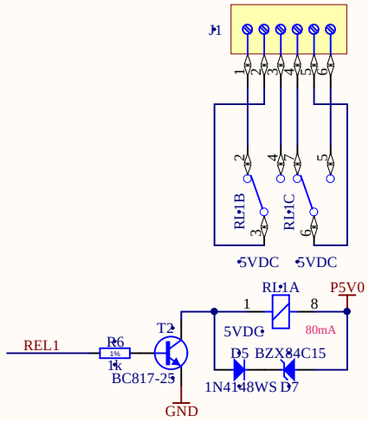

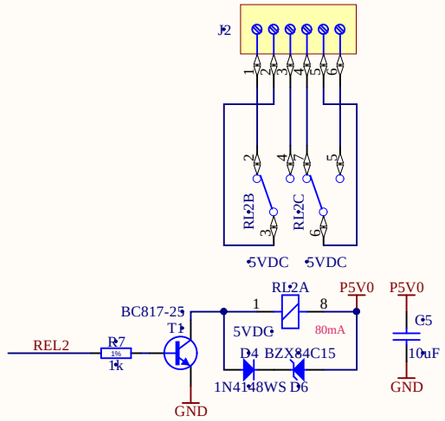

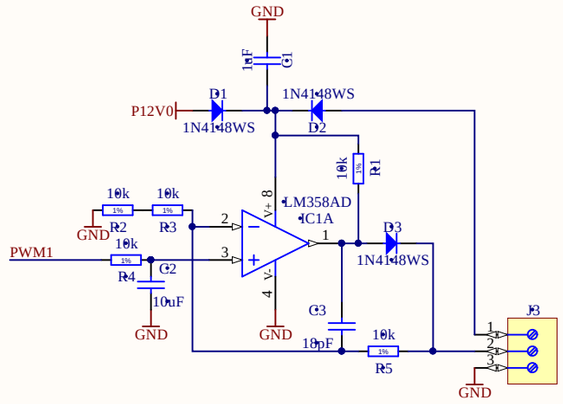

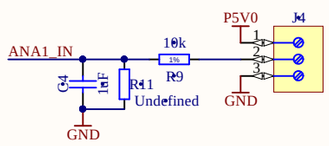

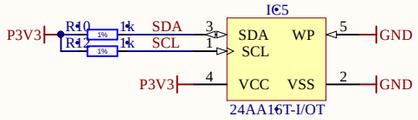

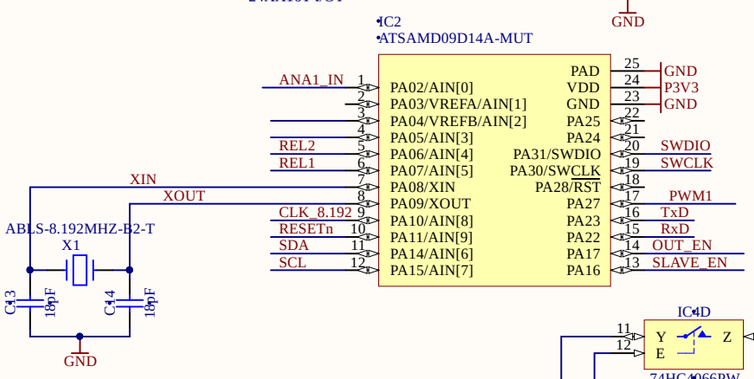

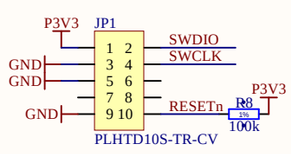

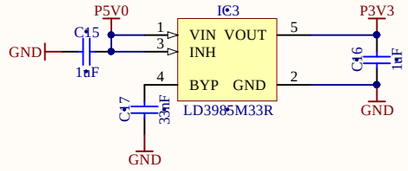

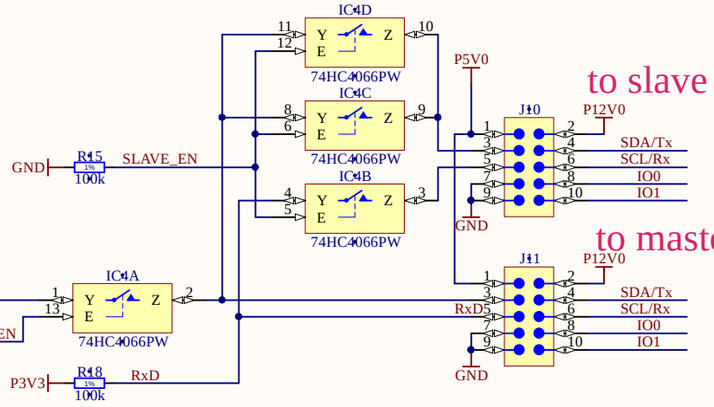

Table 6-1. PORT Function Multiplexing

Pin					A	B	C	D	E	G	H
SAMD09C 14-pin SOIC	SAMD09D 24-pin QFN	I/O Pin	Supply	Type	EIC	REF	ADC	SERCOM[1]	SERCOM-ALT	TC	COM	GCLK
13	1	PA02	VDD		EXTINT[2]		AIN[0]					
	2	PA03	VDD		EXTINT[3]	ADCVREFA	AIN[1]					
14	3	PA04	VDD		EXTINT[4]	ADCVREFB	AIN[2]	SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1/WO[0]		
1	4	PA05	VDD		EXTINT[5]		AIN[3]	SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1/WO[1]		
	5	PA06	VDD		EXTINT[6]		AIN[4]	SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2/WO[0]		
	6	PA07	VDD		EXTINT[7]		AIN[5]	SERCOM0/PAD[1]	SERCOM0/PAD[3]	TC2/WO[1]		
2	7	PA08	VDD		EXTINT[6]			SERCOM1/PAD[2]	SERCOM0/PAD[2]			GCLK_IO[0]
3	8	PA09	VDD		EXTINT[7]			SERCOM1/PAD[3]	SERCOM0/PAD[3]			GCLK_IO[1]
	9	PA10	VDD		EXTINT[2]		AIN[8]	SERCOM0/PAD[2]		TC2/WO[0]		GCLK_IO[4]
	10	PA11	VDD		EXTINT[3]		AIN[9]	SERCOM0/PAD[3]		TC2/WO[1]		GCLK_IO[5]
4	11	PA14	VDD	I²C	NMI		AIN[6]	SERCOM0/PAD[0]		TC1/WO[0]		GCLK_IO[4]
5	12	PA15	VDD	I²C	EXTINT[1]		AIN[7]	SERCOM0/PAD[1]		TC1/WO[1]		GCLK_IO[5]
	13	PA16	VDD		EXTINT[0]			SERCOM1/PAD[2]		TC1/WO[0]		GCLK_IO[2]
	14	PA17	VDD		EXTINT[1]			SERCOM1/PAD[3]		TC1/WO[1]		GCLK_IO[3]
	15	PA22	VDD	I²C	EXTINT[6]			SERCOM1/PAD[0]		TC1/WO[0]		GCLK_IO[1]
	16	PA23	VDD	I²C	EXTINT[7]			SERCOM1/PAD[1]		TC1/WO[1]		GCLK_IO[2]
	17	PA27	VDD		EXTINT[7]							GCLK_IO[0]
6	18	PA28	VDD									
7	19	PA30	VDD		EXTINT[2]			SERCOM1/PAD[0]	SERCOM1/PAD[2]	TC2/WO[0]	CORTEX_M0P/SWCLK	GCLK_IO[0]
8	20	PA31	VDD		EXTINT[3]			SERCOM1/PAD[1]	SERCOM1/PAD[3]	TC2/WO[1]	SWDIO[2]	GCLK_IO[0]
9	21	PA24	VDD		EXTINT[4]			SERCOM1/PAD[2]				GCLK_IO[0]
10	22	PA25	VDD		EXTINT[5]			SERCOM1/PAD[3]				GCLK_IO[0]

Notes:
1. Refer to "Electrical Characteristics" on page 645 for details on the I²C pin characteristics.Only some pins can be used in SERCOM I²C mode. See the Type column for using a SERCOM pin in I²C mode
2. This function is only activated in the presence of a debugger.
3. If the PA24 and PA25 pins are not connected, it is recommended to enable a pull-up on PA24 and PA25 through input GPIO mode. The aim is to avoid an eventually extract power consumption (<1mA) due to a not stable level on pad.

Title		
Size A3	Number	Revision
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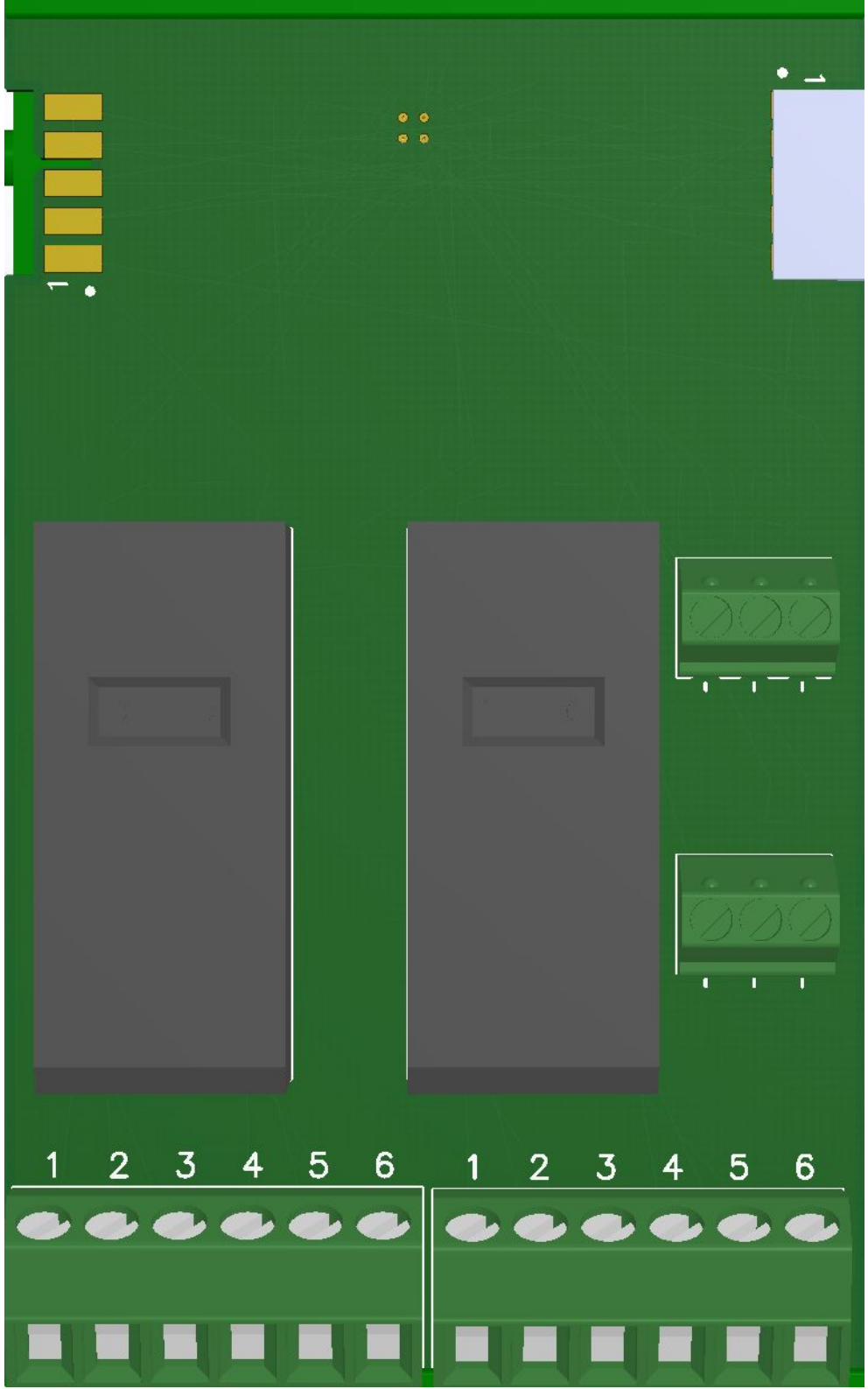
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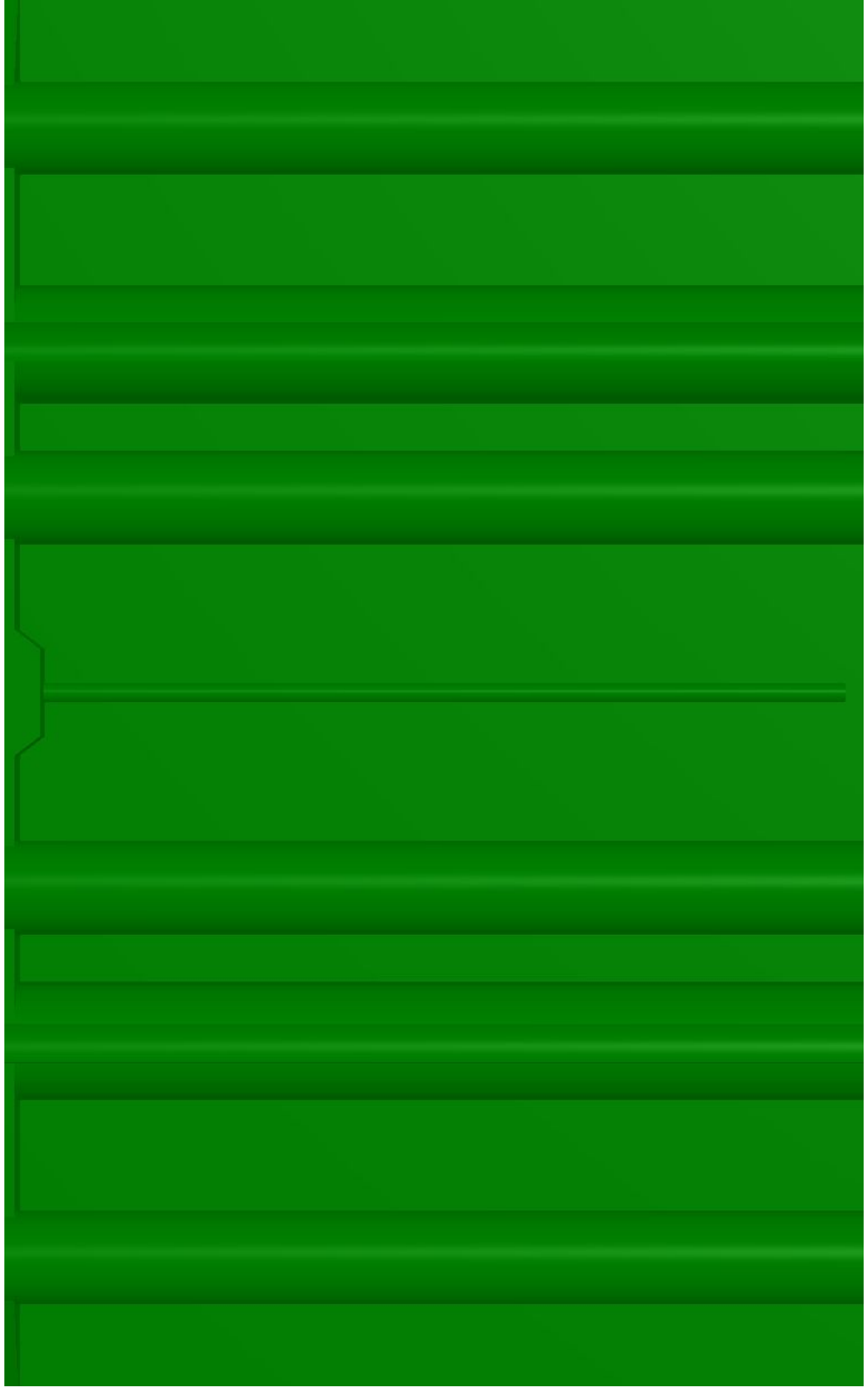

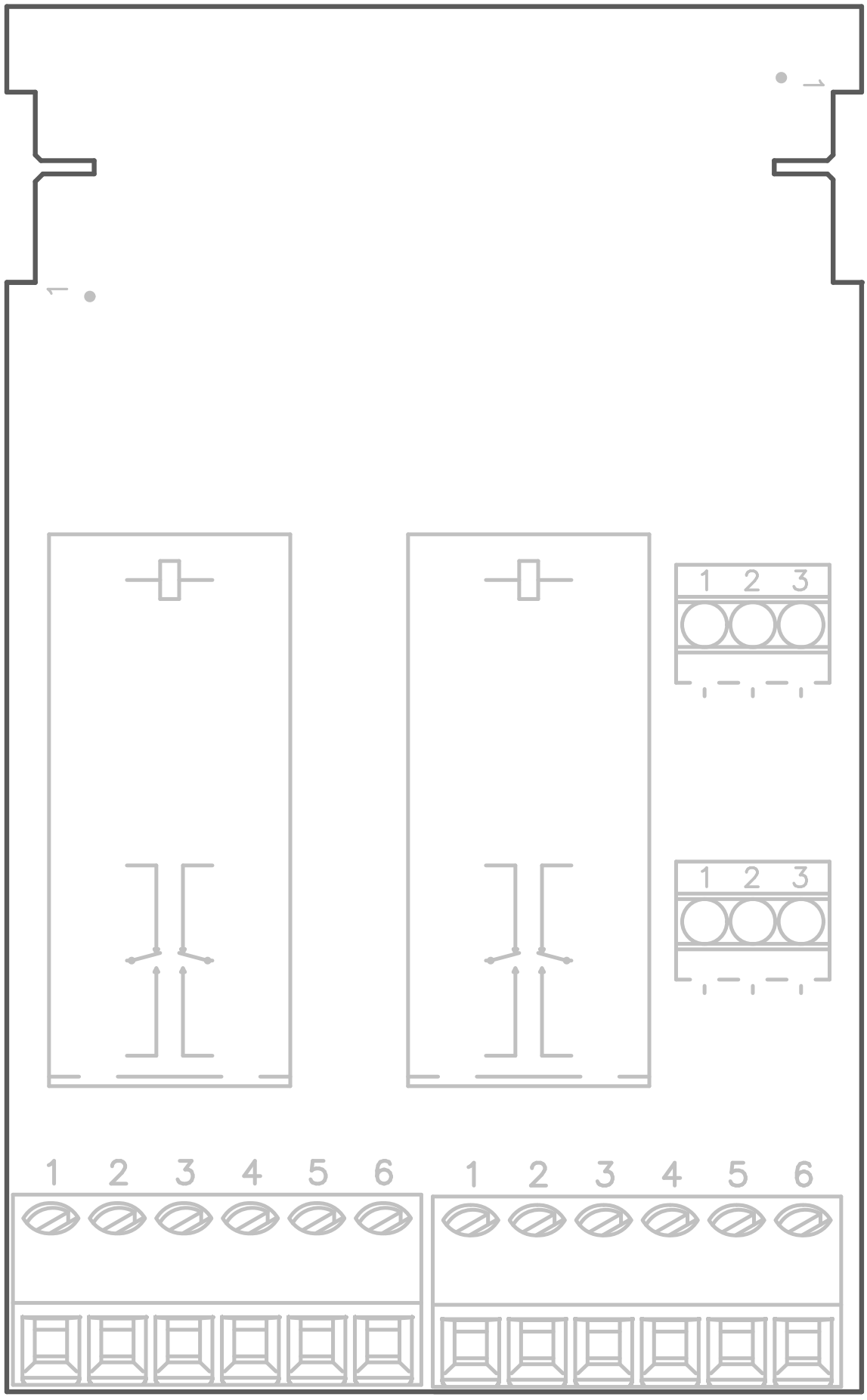
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