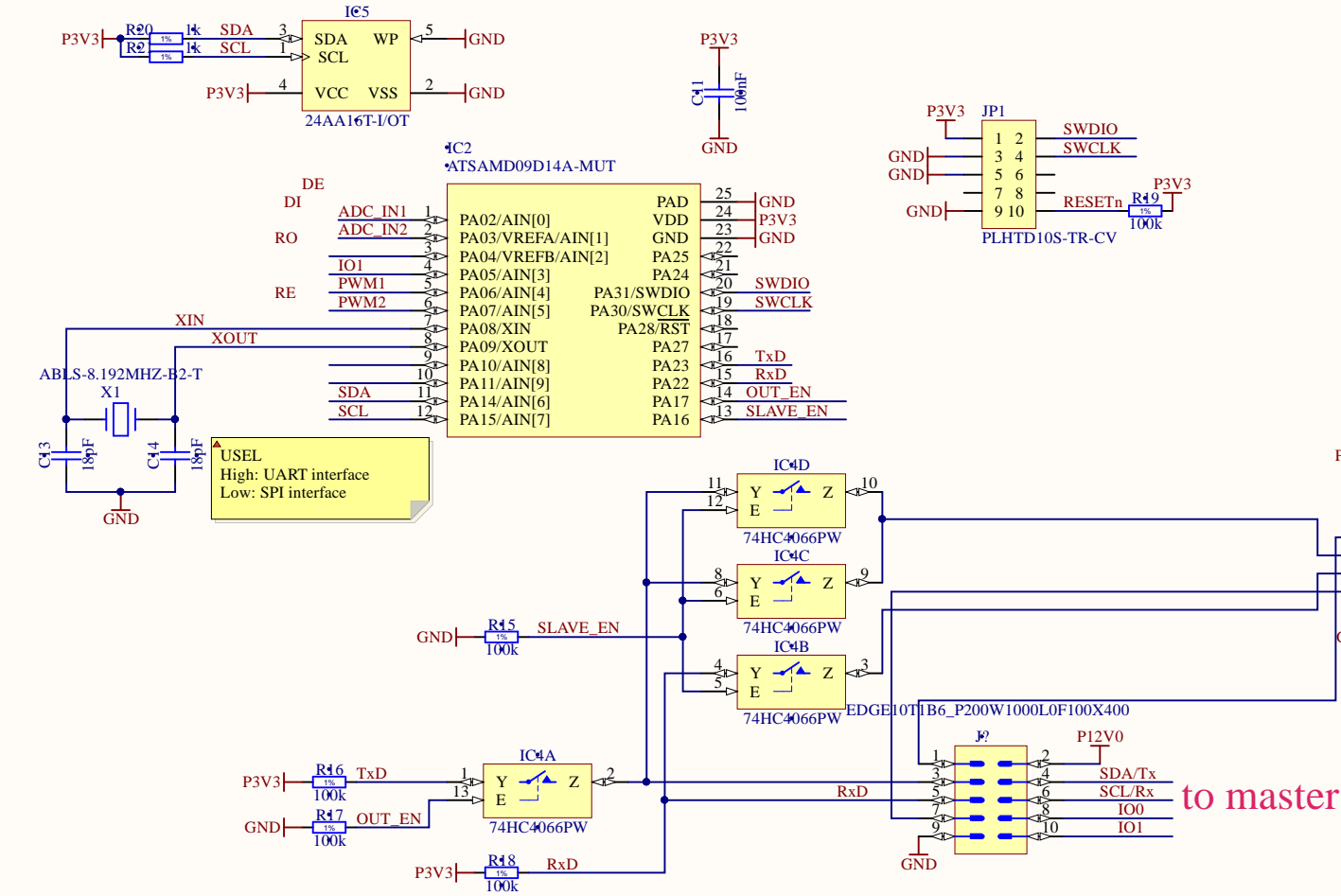


Table 6-1. PORT Function Multiplexing

Pin	14-pin SAMD09C	24-pin SAMD09D	14-pin I/O Pin	Supply	Type	EIC	REF	ADC	SERCOM ⁰	SERCOM-ALT	TC	COM	GCLK
13	1	PA02	VDD	EXTINT[2]	AIN[0]								
14	2	PA03	VDD	EXTINT[3]	ADCM/REFB	AIN[1]			SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1WQ[0]		
15	3	PA04	VDD	EXTINT[4]	ADCM/REFB	AIN[2]			SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1WQ[1]		
16	4	PA05	VDD	EXTINT[5]	AIN[3]				SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2WQ[0]		
17	5	PA06	VDD	EXTINT[6]	AIN[4]				SERCOM0/PAD[1]	SERCOM0/PAD[3]	TC2WQ[1]		
18	6	PA07	VDD	EXTINT[7]	AIN[5]				SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1WQ[0]		
19	7	PA08	VDD	EXTINT[8]	AIN[6]				SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1WQ[1]		
20	8	PA09	VDD	EXTINT[9]	AIN[7]				SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2WQ[0]		
21	9	PA10	VDD	EXTINT[10]	AIN[8]				SERCOM0/PAD[1]	SERCOM0/PAD[3]	TC2WQ[1]		
22	10	PA11	VDD	EXTINT[11]	AIN[9]				SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1WQ[0]		
23	11	PA12	VDD	EXTINT[12]	AIN[10]				SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1WQ[1]		
24	12	PA13	VDD	EXTINT[13]	AIN[11]				SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2WQ[0]		
25	13	PA14	VDD	EXTINT[14]	AIN[12]				SERCOM0/PAD[1]	SERCOM0/PAD[3]	TC2WQ[1]		
26	14	PA15	VDD	EXTINT[15]	AIN[13]				SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1WQ[0]		
27	15	PA16	VDD	EXTINT[16]	AIN[14]				SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1WQ[1]		
28	16	PA17	VDD	EXTINT[17]	AIN[15]				SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2WQ[0]		
29	17	PA18	VDD	EXTINT[18]	AIN[16]				SERCOM0/PAD[1]	SERCOM0/PAD[3]	TC2WQ[1]		
30	18	PA19	VDD	EXTINT[19]	AIN[17]				SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1WQ[0]		
31	19	PA20	VDD	EXTINT[20]	AIN[18]				SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1WQ[1]		
32	20	PA21	VDD	EXTINT[21]	AIN[19]				SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2WQ[0]		
33	21	PA22	VDD	EXTINT[22]	AIN[20]				SERCOM0/PAD[1]	SERCOM0/PAD[3]	TC2WQ[1]		
34	22	PA23	VDD	EXTINT[23]	AIN[21]				SERCOM0/PAD[2]	SERCOM0/PAD[0]	TC1WQ[0]		
35	23	PA24	VDD	EXTINT[24]	AIN[22]				SERCOM0/PAD[3]	SERCOM0/PAD[1]	TC1WQ[1]		
36	24	PA25	VDD	EXTINT[25]	AIN[23]				SERCOM0/PAD[0]	SERCOM0/PAD[2]	TC2WQ[0]		

Notes:
1. Refer to "Electrical Characteristics" on page 640 for details on the PC pin characteristics. Only some pins can be used in SERCOM PC mode. See the Type column for using a SERCOM pin in PC mode.
2. This function is only activated in the presence of a debugger.
3. If the PA24 and PA25 pins are not connected, it is recommended to enable a pull-up on PA24 and PA25 through input GPIO mode. The aim is to avoid an eventually extract power consumption (<1mA) due to a not stable level on pad.



to slave

to master

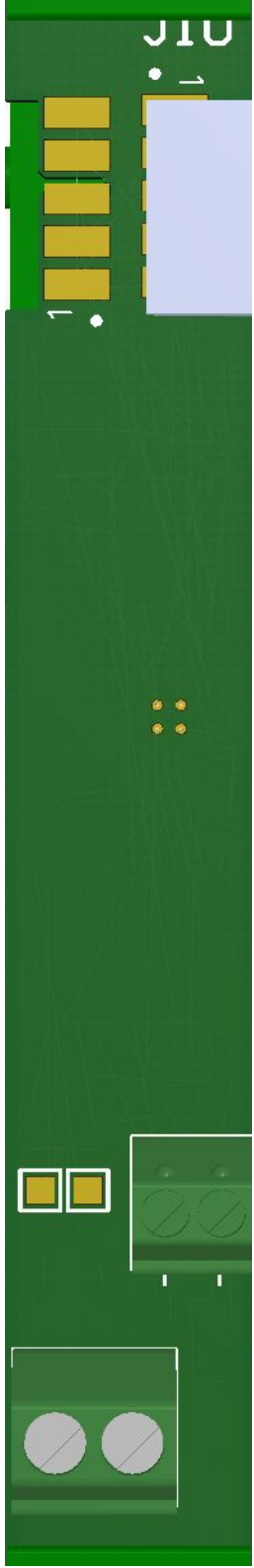
to master

1-Wire

Analog IN

Analog OUT

Title		
Size	Number	Revision
A3		
Date:	04.03.2018	Sheet of
File:	D:\Dropbox\...\Sensor_Box_Smart_IO_Sch	Drawn By:





J11

