

Y86 Processor: seq+-std.hcl

Quit Go Stop Step Reset

Simulator Speed (10*log Hz)
4

Processor State

valM
00000000

Memory Stage

Cnd valE
N FFFFFFFF

Execute Stage

valA valB dstE dstM srcA srcB
00000000 00000000 %ebx ---- ----

Decode Stage

Instr rA rB valC valP
irmovl ---- %ebx FFFFFFFF 00000071

Fetch Stage

PC
0000006B

PC Stage

pCnd pInstr pValC pValM pValP
N addl 00000000 00000000 0000006B

Register File

%eax	%ecx	%edx	%ebx	%esp	%ebp	%esi	%edi
cd	1c	3	4	e8	e8	c0	

Stat AOK

Condition Codes Z 0 S 0 O 0

Controls

Stage signals

Register file

Condition codes

Status