# THIAGARAJAR COLLEGE OF ENGINEERING

(An Autonomous Institution Affiliated to Anna University)

MADURAI - 625 015, TAMILNADU, INDIA

## **GRADE CARD** B.E. DEGREE EXAMINATION

Sl.No: 177733

ANDIDATE NAME :

MEENAKSHI, N.

EG.No. & ROLL.No. :

13D47 & 56535

DATE OF PUBLICATION : 05-06-2017

RANCH ELECTRONICS & COMMUNICATION ENGINEERING

EXAM MONTH & YEAR : APR-2017

	FIRST SEA	ST SEMESTER		SECOND SEMESTER			THIRD SEMESTER				FOURTH SEMESTER				
	CREDITS		M/Y	SUB.CODE			M/Y	SUB.CODE			M/Y	SUB.CODE	CREDITS	GRADE	MY
H11	4	S	N13	D21	4	S	A14	D31	4	Α	N14	D41	4	S	A15
H12	3	S	N13	D22	4	A	A14	D32	4	A	N14	D42	4	A	A15
H13	3	A	N13	D23	3	В	A14	D33	4	A	N14	D43	3	A	A15
H14	3	В	N13	D24	3	S	A14	D34	3	A	N14	D44	3	A	A15
H15	4	A	N13	D25	3	Α	A14	D35	3	В	N14	D45	4	В	A15
H16	4	В	N13	D26	2	S	A14	D36	3	A	N14	D46	4	S	A15
H17	1	A	N13	D27	1	S	A14	D37	1	В	NI4	D47	1	S	A15
H18	i	A	N13	D28	1	S	A14	D38	1	A	N14	D48	1	A	A15
H19	2	A	N13	D29	1	S	A14		- 1			D49	2	A	A15
FIFTH SEMESTER				SIXTH SEMESTER				SEVENTH SEMESTER				EIGHTH SEMESTER			
JB.CODE	CREDITS	GRADE	M/Y	SUB.CODE	CREDITS	GRADE	M/Y	SUB.CODE	CREDITS	GRADE	M/Y	SUB.CODE	CREDITS		MA
D51	4	S	N15	D61	3	A	A16	D71	3	В	N16	D84	12	A	Al
D52	4	S	N15	D62	3	Α	A16	D72	3	A	N16	1 1000			
D53	4	C	N15	D63	3	A	A16	DCE	3	A	N16				
D54	4	A	N15	D64	4	A	A16	DCL	3	A	N16	4			
D55	3	В	N15	CIK	1	A	A16	DCP	3	A	N16	1			
	3	A	N15	DCM	3	S	A16	TGA	3	Α	N16	1000			
D56		S	N15	DCO	3	S	A16	D77	1	S	N16	4			
D56 DCB	3					The Section of the Se	A16	D78	6	A	N16	1			
DCB	3		N15	TGB	3	A									
	3	S	N15 N15	TGB D67	3	S	A16			11					

	I-SEMESTER							
1	ENGINEERING	MATHEMATICS	1					
2	PHYSICS							

CHEMISTRY 114 FNGLISH

BASICS OF MECHANICAL AND CIVIL 115 ENGINEERING

BASICS OF ELECTRICAL AND ELECTRONICS ENGINEERING

417 PHYSICS LAB H19 ENGINEERING GRAPHICS II-SEMESTER

D21 ENGINEERING MATHEMATICS - II D22 LINEAR NETWORKS

D23 BASICS OF DIGITAL SYSTEMS D24 COMPUTERS AND PROGRAMMING

D25 MATERIALS SCIENCE D26 ENVIRONMENT AND ECOLOGY

D27 DIGITAL SYSTEMS LAB D28 COMPUTER PROGRAMMING LAB

D29 WORKSHOP

III-SEMESTER

D31 ENGINEERING MATHEMATICS III D32 ANALOG CIRCUITS AND SYSTEMS

D33 SIGNAL PROCESSING D34 ANALOG COMMUNICATION SYSTEMS

D35 DATA STRUCTURES

D36 MICROPROCESSORS

D37 ANALOG CIRCUITS AND SYSTEMS LAB D38 ANALOG COMMUNICATION LAB

IV-SEMESTER D41 ENGINEERING MATHEMATICS-4

D42 DESIGNING WITH PLDS AND FPGAS MIXED SIGNAL CIRCUITS AND

INTERFACING

D44 DIGITAL COMMUNICATION SYSTEMS D45 ELECTROMAGNETIC FIELD THEORY DIGITAL SIGNAL PROCESSING

D47 MIXED SIGNAL CIRCUITS AND INTERFACING LAB

D48 DIGITAL SIGNAL PROCESSING LAB PROFESSIONAL COMMUNICATION

### V-SEMESTER

051 NUMERICAL METHODS CMOS VLSI SYSTEMS MICROCONTROLLERS WIRELESS COMMUNICATION SYSTEMS ANTENNA AND WAVE PROPAGATION DATA COMMUNICATION NETWORKS

ADVANCED DIGITAL SYSTEMS(E) DIGITAL COMMUNICATION LAB D58 MICROPROCESSOR AND MICROCONTROLLER LAB PRACTICAL APPROACHES TO

NETWORKING(E)

VI-SEMESTER

D68 RF CIRCUITS LAB

D67

D61 MANAGEMENT THEORY AND PRACTICE D62 HIGH PERFORMANCE COMMUNICATION NETWORKS

D63 DIGITAL IMAGE PROCESSING D64 RF AND MICROWAVE ENGINEERING C1K INTRODUCTION TO MICROSOFT ADDS(E)

DCM LOW POWER VLSI SYSTEM(E) DCO DATA COMPRESSION(F) TGB OBJECT ORIENTED PROGRAMMING

WITH JAVA(E) NETWORKING LAR VII-SEMESTER

D71 ACCOUNTING AND FINANCE D72 RF WIRELESS SYSTEMS

DCE ASIC DESIGN(E)

DCL REAL TIME OPERATING SYSTEMS(E)

DCP MICROELECTRONICS(E)

TGA DATA BASE MANAGEMENT SYSTEMS(E) DTT RF AND IMAGE PROCESSING LAB

D78 PROJECT

#### VIII-SEMESTER

D84 PROJECT

\* Excess Credits Earned not Considered for CGPA Calculation

Semester	I	II	III	IV	V	VI	VII	VIII		
GPA	9.00	9.41	8.83	9.19	9.07	9.28	8.92	9.00		
Credits Registered	25	22	23	26	28	25	25	12		
Credits Earned	25	22	23	26	28	25	25	12		
Total Credits Earned		184								
CGPA	9.09									

Class obtained

First Class With Distinction

1 Any Alteration or overwriting makes this statement of marks invalid

7 Any discrepancy in the above entries must be brought to the notice of the CONTROLLER OF EXAMINATIONS immediately

Controller of Examinations

Date: 08-06-2017

