762 }

# **EMBEDDED SYSTEM DESIGN (E3-257)**

## LAB ASSIGNMENT – 6 and 7

→ Lab 6: Implementing Stop watch Timer

1. Setting up the SSD for Stop-Watch.

We use the NVIC SysTick Handler to manage the timer setting in the Seven segment display

```
128
        NVIC ST RELOAD R = 1000000-1;
        NVIC ST CTRL R = 7; // enable counter, interrupt and select system bus clock
 129
        NVIC ST CURRENT R = 0;
 130
 131
746 void SysTick_Handler(void)
747 {
748
749
     if((pause_stat == 1))
750
751
          GPIO_PORTF_DATA_R ^= 0x04;
752
753
      }
754
755
      else
756
757
          count = count+1;
758
          GPIO_PORTF_DATA_R ^= 0x08; //toggle PF3 pin
759
760
      }
761
```

Here the Handler toggles in default and the pause state conditions are checked for when the flag is raised.

Since the 0 pattern in the 4<sup>th</sup> SSD is interfering with the LCD cmnd mode operation its not printing for seconds with 3<sup>rd</sup> digit in 0 value and it starts showing when count becomes 100 ms onwards

```
158
          if(count4 == 0)
159
160
          { for(int i =0;i<60;i++){}
161
           GPIO_PORTA_DATA_R &= \sim(0xF0);
162
           GPIO_PORTA_DATA_R \mid = 0 \times 10;
163
           GPIO_PORTB_DATA_R = 0;
164
           GPIO_PORTB_DATA_R = digitPattern[count];
165
166
167
           for(int i =0;i<60;i++){}</pre>
           GPIO_PORTA_DATA_R &= ~(0xF0);
168
           GPIO_PORTA_DATA_R |= 0x20;
169
           GPIO_PORTB_DATA_R = 0;
170
           GPIO_PORTB_DATA_R = digitPattern[count2];
171
172
173
           for(int i =0;i<60;i++){}</pre>
174
           GPIO PORTA DATA R &= \sim(0xF0);
175
           GPIO PORTA DATA R = 0x40;
176
           GPIO_PORTB_DATA_R = 0;
177
           GPIO_PORTB_DATA_R = digitPattern[count3];
178
179
  180
             else
  181
              {
                  for(int i =0;i<60;i++){}</pre>
  182
  183
  184
                   GPIO_PORTA_DATA_R &= \sim(0xF0);
                   GPIO_PORTA_DATA_R = 0x10;
  185
                   GPIO_PORTB_DATA_R = 0;
  186
                   GPIO_PORTB_DATA_R = digitPattern[count];
  187
  188
  189
                   for(int i =0;i<60;i++){}</pre>
  190
                   GPIO PORTA DATA R &= \sim(0xF0);
                   GPIO_PORTA_DATA_R = 0x20;
  191
  192
                   GPIO_PORTB_DATA_R = 0;
                   GPIO_PORTB_DATA_R = digitPattern[count2];
  193
  194
  195
                   for(int i =0;i<60;i++){}</pre>
  196
                   GPIO PORTA DATA R &= \sim(0xF0);
  197
                   GPIO PORTA DATA R = 0x40;
                   GPIO_PORTB_DATA_R = 0;
  198
  199
                   GPIO_PORTB_DATA_R = digitPattern[count3];
  200
  201
  202
                   for(int i =0;i<60;i++){}</pre>
  203
                   GPIO PORTA DATA R &= \sim(0xF0);
                   GPIO PORTA DATA R = 0x80;
  204
                   GPIO PORTB DATA R = 0;
  205
                   GPIO_PORTB_DATA_R = digitPattern[count4];
  206
  207
  208
  209
             }
  240
```

2. Push Buttons are set as follows and works based on:

SW1: Will be used to start and stop.

SW2: Will be used to pause and resume.

```
762
     if ((GPIO_PORTF_MIS_R & 0x10) == 0x10) /* check if interrupt causes by PF4/SW1*/
763
764
            GPIO_PORTF_ICR_R = 0x10; /* clear the interrupt flag */
765
766
         if((strcmp(cmnd.data, "stop")==0)||(stat_key_1 == 1))
767
768
769
                                               stat_key_1 = 0;
770
771
772
                                               printstring("Stop\n\r");
                                               GPIO_PORTF_DATA_R = 0x08;
773
                                               count = 0;
                                                   count2 = 0;
775
                                                   count3 = 0;
776
777
                                                   count4 = 0;
778
                                               NVIC_ST_CTRL_R = 0;
                                               flag = 1;
stop_stat = 1;
780
781
782
783
                                               lcd_cmd(0x01);
784
                                               lcd_cmd(0x02);
785
                                               lcd_cmd(0x80);
786
                                               lcd_write("Timer ");
                                               lcd_cmd(0xC0);
787
788
                                               lcd write("Ready
789
790
                                               printstring("Valid Entry\n\r");
791
792
793
794
795
         else if((strcmp(cmnd.data, "start")==0)||(stat_key_1 == 0))
796
```

```
821 if ((GPIO_PORTF_MIS_R & 0x01) == 0x01) /* check if interrupt causes by PF4/SW2*/
                  GPIO_PORTF_ICR_R = 0x01; /* clear the interrupt flag */
823
824
825
              if((strcmp(cmnd.data, "pause")==0)||(stat_key_2 == 0))
826
827
828
                                                                stat_key_2 = 1;
829
830
                                                                printstring("Pause \n\r");
831
                                                                GPIO_PORTF_DATA_R &= 0x00;
GPIO_PORTF_DATA_R |= 0x04;
832
833
834
835
                                                                NVIC_ST_CTRL_R |= 7;
flag = 1;
pause_stat = 1;
836
837
838
839
840
                                                                lcd cmd(0x01);
                                                                lcd_cmd(0x02);
lcd_cmd(0x80);
841
                                                                lcd_write("Timer
lcd_cmd(0xC0);
843
844
845
                                                                lcd_write("Paused ");
846
847
                                                                printstring("Valid Entry\n\r");
848
849
850
851
852
              else if((strcmp(cmnd.data, "resume")==0)||(stat_key_2 == 1))
853
854
855
                                                              stat_key_2 = 0;
printstring("Resume\n\r");
GPIO_PORTF_DATA_R &= 0x00;
GPIO_PORTF_DATA_R |= 0x08;
856
857
```

Both SW1 and SW2 are configured to falling edge interrupts and the same is checked in the port F handler as above.

#### → Lab 7: Tic-Tac-Toe

1. Logic:

Detecting a Row Strike.

```
426
                                   for (int r_val = 0; r_val < 3; r_val++) {
    if (arr[r_val][0] == arr[r_val][1]
        && arr[r_val][1] == arr[r_val][2]) {</pre>
427
428
429
                                                    if (arr[r_val][0] == 'x')
{printstring("User x Won\n\r");
430
431
432
                                                            x_flag = 1;
433
                                                     else if (arr[r_val][0] == 'o' )
                                                          {printstring("User o Won \n\r");
436
                                                            o_flag = 1;
437
                                                           }
438
                                               }
439
```

Detecting a Column Strike.

Detecting Diagonal Strike.

Detecting a Draw Game.

Clearing a Game after result:

```
if((x_flag == 1)||(o_flag == 1))
491
492
                          for(int i =0;i<3;i++)</pre>
493
494
                              for(int j = 0; j <3; j++)</pre>
496
                                   arr[i][j] = ' ';
497
498
499
                          inp_count = 0;
501
502
                     }
503
504
505
                     x_flag = 0;
                     o_flag = 0;
507
508
                 }
509
```

- 2. Mapping and Recognizing Key Presses.
- → Implementation of col recognition using interrupts:

```
887 void GPIOC_Handler(void)
888 {
889
       if ((GPIO_PORTC_MIS_R & 0x10) == 0x10)
890
891
                        GPIO PORTC ICR R = 0x10;
                        key_col = 1;
892
894
       if ((GPIO_PORTC_MIS_R & 0x20) == 0x20)
895
896
                        GPIO_PORTC_ICR_R = 0x20;
897
898
                        key_col = 1;
899
900
       if ((GPIO_PORTC_MIS_R & 0x40) == 0x40)
901
902
                        GPIO_PORTC_ICR_R = 0x40;
903
                        key\_col = \overline{1};
904
905
906
907
       if ((GPIO_PORTC_MIS_R & 0x80) == 0x80)
908
                        GPIO_PORTC_ICR_R = 0x80;
909
910
                        key_col = 1;
911
912
                    }
913 }
914
```

→ Recognition of the row pressed:

```
if(key_col)
323
                int row = 0, col = 0, num = 0;
324
325
                for(row = 0; row < 3; row ++)</pre>
326
327
328
                     GPIO_PORTE_DATA_R = ( 0x0F \& \sim(1 << row) );
330
                    num = GPIO_PORTC_DATA_R & 0xF0 ;
331
332
                    if( num == 0xE0)
333
                         col = 0;
334
                        key_val = (row*3 + col);
335
336
337
                        r = row;
338
                        c = key_val%3;
339
340
341
                     else if( num == 0xD0)
342
343
                         col = 1;
344
                         key_val' = (row*3 + col);
345
346
347
                        r = row;
                         c = key_val%3;
348
350
351
352
                     else if( num == 0xB0)
353
                         col = 2;
354
                         key_val = (row*3 + col);
355
356
357
                        r = row;
                        c = key_val%3;
358
359
362
                    }
363
```

Recognizing and storing the x or o intake to the particular position in arr[][] as mapped from the keypad button position(set based on the winner of previous game. On setup it is X)

```
if(isKeyPressed())
373
374
                {
375
376
377
378
                    if((user == 0)&&(arr[r][c]!='o'))
379
                        if(x_flag == 1)
380
381
                            arr[r][c] = 'o';
382
383
                            user = 0;
384
                        }
385
386
387
                        {
                            arr[r][c] = 'x';
388
389
                            user = 1;
390
                        }
391
392
393
394
395
                    }
396
397
                    else if((user == 1)&&(arr[r][c]!='x'))
398
                    {
399
                        if(o_flag == 1)
400
401
                            arr[r][c] = 'x';
402
                            user = 1;
                        }
403
404
405
                        else
406
                        {
407
                            user = 0;
                            arr[r][c] = 'o';
408
409
410
                        }
411
412
                    }
413
414
                    else
```

Pressing of already pressed position in the 3x3 matrix is not allowed and is realised as "Invalid Move"

```
397
                    else if((user == 1)&&(arr[r][c]!='x'))
398
399
                         if(o_flag == 1)
400
401
                             arr[r][c] = 'x';
402
                             user = 1;
404
405
                         else
406
                            user = 0;
arr[r][c] = 'o';
407
409
410
411
412
                    }
413
414
415
416
                        printstring("Invalid Move!!");
417
418
                    inp_count = inp_count + 1;
```

3. UART console commands are also accepted. The commands are parsed as below in the separate\_funct()

And different operation conditions are mapped to the received state as below:

```
615 if ((strcmp(cmnd.data, "stop")==0))
616 {
      // GPIO_init();
617
618
      LCD init();
619
      count = 0;
      count2 = 0;
620
621
      count3 = 0;
      count4 = 0;
622
623
624
625
626
627
      NVIC_ST_CTRL_R = 0;
      flag = 1;
stop_stat = 1;
628
629
630
      GPIO_PORTF_DATA_R = 0x08;
631
      lcd_cmd(0x01);
632
633
      1cd_cmd(0x02);
634
      lcd_cmd(0x80);
635
      lcd_write("Timer
636
      lcd_cmd(0xC0);
637
      lcd_write("Ready
638
639
      printstring("Stop");
640
641
       printstring("\n\r");
642
643
644
       printstring("Valid Entry\n\r");
645
       check= 1;
646
647
648
649 }
650
```