

Profile of Dr.R.Sakthivel

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Publications

1. Sakthivel, R., et.al., “**Design of artificial neuron network with synapse utilizing hybrid CMOS transistors with memristor for low power applications**”, Journal of Circuits, Systems and Computers, 2020, 29(12), 2050187.
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3. Sakthivel, R., et.al., “**Single Bit Fault Detecting ALU Design using Reversible Gates**”, International Conference on Emerging Trends in Information Technology and Engineering, ic-ETITE 2020, 2020, 9077903.
4. Sakthivel, R., et.al., “**Low power area optimized and high speed carry select adder using optimized half sum and carry generation unit for FIR filter**” Journal of Ambient Intelligence and Humanized Computing, 2020.
5. Sakthivel, R., et.al., “**Superior Implementation of Accelerated QR Decomposition for Ultrasound Imaging**” , J.U. IEEE Access, 2020, 8, pp. 156244–156260, 9170633.
6. Sakthivel , , R., et.al., “**Nonlinear System Modelling Using Programmable Hardware for Soft Computing Applications**” Advances in Intelligent Systems and Computing, 2020, 1057, pp. 293–306.
7. Sakthivel, R., et.al., “**Long-lifetime and low latency data aggregation scheduling for wireless sensor network**” Journal of Testing and Evaluation, 2019, 47(6), JTE20180511.

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9. Sakthivel, R , et.al., **"An efficient hardware implementation of finite field inversion for elliptic curve cryptography"** International Journal of Innovative Technology and Exploring Engineering, 2019, 8(9), pp. 827–832.
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