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**PAPERS PUBLISHED IN JOURNALS:**

1. Karthigai Lakshmi S. and Athisha, G. "Efficient design of logical structures and functions using Nano technology based Quantum Cellular Automata Design", International Journal of Computer Applications, Vol. 3, No. 5, pp. 35- 42, June 2010.
2. Karthigai Lakshmi, S. and Athisha, G. "Design and Analysis of Arithmetic Structures" International journal of Recent Trends in Engineering, Vol.4, No.2, Nov.2010.
3. Karthigai Lakshmi, S. and Athisha, G. "Design and analysis of Subtractors using Nanotechnology based QCA" European journal of scientific research, Vol.53, No.4. pp 524-532. May 2011.
4. Karthigai Lakshmi, S. and Athisha, G. "Design and performance analysis of Adders using Nanotechnology based Quantum Cellular Automata", Journal of computer science, Vol. 7, No. 7, pp. 1072-1079, 2011
5. S.Karthigai Lakshmi, S. and Athisha, G. "Design and analysis of Logical Structures and functions using NNI for Nanotechnology based QCA" WSEAS Transactions on circuits and systems, Issue 1, Vol.11, January 2012.
6. S.Karthigai Lakshmi, K.Jaseema Banu, "An On-line Hindrance Computation skill for small delay Deficiency Revealing using Signature Registers" International Journal of Advanced Information Science and Technology, Vol.18, No.18, October 2013
7. S.Karthigai Lakshmi, M.Aarthi, "Modeling of Bio-Inspired Self-Repairing Digital System", IOSR Journal of Electronics and Communication Engineering, Volume 9, Issue 2, 2014, PP 29-36.
8. S.Karthigai Lakshmi, "Design and Analysis of Sequential Circuits Using Nanotechnology Based Quantum Dot Cellular Automata" Journal of Nanoelectronics and Optoelectronics ,Vol. 10, 601-610, 2015
9. S.Karthigai Lakshmi, Kerithi .M "Efficient Evolutionary Algorithms for Network on Chip Systems" International Journal of Engineering and Recent Trends, Vol.4 . 2016
10. S.Karthigai Lakshmi, Dr.C.Sujatha, A Survey on Software and Hardware Implementation of Image Edge Detection using FPGA, International Journal of Advance and Innovative Research, Vol. 4, No. 4, pp: 1-8. 2017
11. S.Karthigai Lakshmi, Dr.C.Sujatha "Hardware Implementation of Road Network Extraction Using Simplified Gabor Wavelet in Field Programmable Gate Array" International Journal of Engineering and Technology Innovation Vol. 8, No.3, pp: 200-216, 2018

12. S.Karthigai Lakshmi, S.Kannaki, Design and Performance Analysis of Serial Communication in QCA, International Journal of Pure and Applied Mathematics Vo.119 No. 12, pp: 825-836,2018
13. S. Karthigai Lakshmi, M. Manikandan, "Design of Implantable Antenna for Patient Monitoring System" Journal of Emerging Technologies and Innovative Research, Vol 5, Issue 11, ISSN 2349-5162, November 2018.
14. S. Karthigai Lakshmi, M. Manikandan, "Novel Multiband Microstrip Loop Antenna for Wearable Application" International Journal of Research and Analytical Reviews, Vol 6 Issue 1, ISSN 2348-1269, March 2019.
15. S. Karthigai Lakshmi "Design of the Parity Preserving Reversible Gate Using Nano QCA", SSRG International Journal of Electronics and Communication Engineering, March.2019. ISSN: 2348 – 8549
16. S.Karthigai Lakshmi,G. Bamila Juliet "Design of Optimized QCA sequential circuits", International Journal of Science Research And Review, March.2019. ISSN: 2379 – 543X. Vol.4, Issue No.8, 2019.
17. Kannaki S, Karthigai Lakshmi S, "Development of Advanced Automatic Sorting Machine using Weighing Mechanism" International Journal of Recent Technology and Engineering (IJRTE) ISSN: 2277-3878, Volume-8 Issue-3, September 2019.

#### **PAPERS PRESENTED IN NATIONAL /INTERNATIONAL CONFERENCES:**

1. S.Karthigai Lakshmi, "VHDL Implementation of Reconfigurable Processor", National conference on OPTEST-06 11- 12<sup>th</sup> April-06 at Bannari Amman Institute of Technology, Sathyamangalam.
2. S.Karthigai Lakshmi, A.Kavitha, "Design of Reconfigurable DSP Processor for DSP Applications Using VHDL", National conference on Communication Techniques conducted at MEPCO, Sivakasi March 2006.
3. S.Karthigai Lakshmi, C.Sujatha, "Design and Implementation DSP Logic Modules in VHDL", National conference on AIECT- 06 at PSNACET, Dindigul.
4. S.Karthigai Lakshmi, A. Kavitha, "Implementation of Fast and Accurate Face Detector", National conference on AIECT- 06 at PSNACET, Dindigul
5. S.Karthigai Lakshmi, M.Ragini, S.Divya, "Multiple Queuing Techniques for server Fast Response", National conference on OPTEST-06 11- 12<sup>th</sup> April-06 at Bannari Amman Institute of Technology, Sathyamangalam.
6. S.Karthigai Lakshmi, A.Kavitha, "Design of Application specific Instruction set Processor for DSP applications using VHDL", International conference in intelligent systems and control, Aug 9-11,06 at Karpagam college of Engg, Coimbatore

7. S.Karthigai Lakshmi, A.Kavitha, "Implementation of Face detector using Neural Network", International conference in intelligent systems and control, Aug 9-11,06.
8. S.Karthigai Lakshmi, G.Athisha, "Implementation of an Arithmetic and Logic Unit using Nanotechnology based Quantum Cellular Automata Design", International Conference on VLSI Design and Embedded Systems, February 24- 28, 2008 at Velammal Engg College, Chennai.
9. S.Karthigai Lakshmi, G.Athisha "Implementation of an Adder using Nanotechnology based Quantum Cellular Automata Design", National Conference on Advanced Communication Techniques" on March 11th 2008 at ACTECH, Karaikudi.
10. S.Karthigai Lakshmi, G.Athisha, "Design of Serial bit adder based on Full adder design using Quantum Cellular Automata" National Conference on Recent trends in IT, Sept-09, AVC College of Engg, Mailadudurai.
11. S.Karthigai Lakshmi, G.Athisha "Implementation of Logical Structures using Nanotechnology Based Quantum Dot Cellular Automata Design" International Conference on Recent Advancements in Electrical Sciences, Jan 8th -9th, 2010 at KSR College of Engg, Tiruchengode.
12. S.Karthigai Lakshmi,G.Athisha "Design of subtractors using QCA", IEEE-International conference on communication control and computing Technologies, St.Xavier's Catholic College of Engg, Nagarcoil, Oct (7,8&9),2010
13. S.Karthigai Lakshmi, Jeffrey Devanand, "A novel Design of QCA based robust adders with limited In out Fan-in Logic" International Conference on Emerging Trends in communication, March 8th & 9th, 2012 at Maria College of Engg and Tech, Kanyakumari.
14. S.Karthigai Lakshmi, Jeffrey Devanand, "A novel Design of QCA based robust adders with limited In out Fan-in Logic", International Conference on Modeling Optimization and Computing, April 11th & 12th, 2012 at Noorul Islam University, Kumarakoil.
15. S.Karthigai Lakshmi, K.Uma, " Fault Identification in Memory using Efficient error Detection Method", National Conference on Emerging Trends in VLSI, Intelligent Signal Processing and Communication, March 21-22, 2013 at Jayaram College of Engg and Tech, Tiruchirappalli.
16. S.Karthigai Lakshmi, K.Uma, " Fault Identification in Memory using Efficient error Detection Method", 4<sup>th</sup> International Conference on Advances in Computer, Communication and Management, February 18,19 and 20, 2013 at Sun College of Engg and Tech, Erachakulam.
17. S.Karthigai Lakshmi, K.Jaseema Banu, "An On Chip Hindrance Computation skill for small delay Deficiency Revealing using Signature Registers", National Conference on Emerging Trends in VLSI, Intelligent Signal Processing and Communication, March 21-22, 2013 at Jayaram College of Engg and Tech, Tiruchirappalli.
18. S.Karthigai Lakshmi, K.Jaseema Banu, "An On-line Impediment Computation method for small delay fault Recognition using Signature Registers", National Conference on Research Issues in

Renewable Energy and Conservation, 20<sup>th</sup> March, 2013 at National Engineering College, Kovilpatti.

19. S.Karthigai Lakshmi, K.Jaseema Banu, "An On-line Impediment extent skill for small delay Deficiency Recognition using Signature Registers", National Conference on Advances in Communication, Computing & Signal Processing 9<sup>th</sup> April, 2013 at University College of Engineering, Nagercoil.
20. S.Karthigai Lakshmi, K.Jaseema Banu, "An On-line Hindrance Computation method using Signature Registers for small delay fault revealing", National Conference on Universal Innovation in Information and Communication Systems, March 7th, 2013 at Cape Institute of Technology, Livingipuram.
21. S.Karthigai Lakshmi, K.Jaseema Banu, "A Novel technique for On-line Latency Measurement for Internal path delay", National Conference on Innovative Engineering, March 14th, 2013 at Satyam College of Engg and Tech, Aralvoimozhy.
22. S.Karthigai Lakshmi, K.Jaseema Banu, "An On-line scheme for latency detection and performance Characterization", 4<sup>th</sup> International Conference on Advances in Computer, Communication and Management, February 18,19 and 20, 2013 at Sun College of Engg and Tech, Erachakulam.