

### **List of Publications**

1. 'Highly Accurate emergency vehicle management for multiple path using support vector machine based predictor', In 872, June 2019 International Journal of innovative Technology and exploring Engineering, ISSN 2278-3075 Vol 8. Issue 8, PP 869 - 872, June 2019
2. "A New Approach for Accurate Path Prediction Using Multiple Prediction System" ScienceDirect Materials Today: Proceedings (2019)
3. VLSI based Low Power Multiply Accumulate Unit Employing Kogge Stone Adder with Modified Pre-Processing and Post-Processing Stages', International Journal of Engineering and Advanced Technology (IJEAT)', ISSN: 2249-8958 (Online), Volume-8 Issue-4, April 2019, Page No.:295-299.
4. 'Implementation of an Algorithms for Improve the Quality of Memory Built-In Self-Test Tools', International Journal for Scientific Research and Development, pISSN/ E-ISSN 2321-0613 ,pp 1840-1844.2019
5. 'Low Power VLSI Design of a modified Brent Kung adder based Multiply Accumulate Unit for Reverb Engines', 'International Journal of Recent Technology and Engineering (IJRTE)', ISSN: 2277-3878 (Online), Volume-7 Issue-6, March 2019. Page No.: 976-980.
6. 'Resistive Open Fault and Detection Methods for Low Power Nanometric ICs', Journal of Computational and Theoretical Nanoscience, Vol. 16,1-3, 2019.
7. ' Modified Han Carlson Adder Based Multiply Accumulate Unit for Low Power Digital Signal Processor', International Journal of Innovative Technology and Exploring Engineering, ISSN: 2278-3075 (Online), Volume-8 Issue-6, April 2019, Page No.:1144-1148
8. ' Review and Study of Intelligent Techniques in Emergency Vehicle Management System' ,International Journal of Computer Sciences and Engineering , Volume6,Issue12-Dec-2018 (366-368).
9. ' Energy-Efficient Approximate Multiplier Design using Bit Significance-Driven Logic Compression, International Journal of Research in Advent Technology, Volume6, Issue11-November2018
10. "A Study on Parallel Prefix Adders with emphasis on the Structure and Performance Parameters" Proceedings of the International Multi-Conference on Computing,

Communication, Electrical & Nanotechnology (I2CN-2K18) held on 26th & 27th April, 2018

11. "Efficient Approximate Multiplier Design using Bit Significance-Driven Logic Compression", International Journal of Research in Advent Technology, Vol.6, No.11, November 2018 E-ISSN: 2321-9637
12. "A Survey on the Design and Performance of various MAC Unit Architectures", Proceedings of 2017 IEEE International Conference on Circuits and Systems (ICCS 2017)
13. "A review analysis of parallel prefix Adders for better performance in Vlsi applications" Proceedings of 2017 IEEE International Conference on Circuits and Systems (ICCS 2017)
14. "A wireless sensor network based oil well health guard system (OWHGS)", International Journal of Applied Engineering Research, ISSN 0973-4562 Vol.10 No.70 (2015)
15. 'Field Oriented Commutation Technique based Control of BLDC Motor using SVPWM Method', International Journal of Applied Engineering Research, ISSN 0973-4562 Vol.10 No.70 (2015)