

Published Journal papers

| S:no | Title of paper | Name of International Journal | Month & Year of Publication |
|-------------|--|--|--|
| 1 | Comparative Analysis of 6 Transistor, 8 Transistor and 12 Transistor SRAM Memories | International Journal of Advanced Information Science and Technology | June 2015 |
| 2 | Design of Nanoscale Memristive Arrays for Memory Applications | Advances in Natural and Applied Sciences | June 2016 |
| 3 | A Multiplier Design based on Ancient Indian Vedic Mathematics Using Reversible Logic: A Review | Jour of Adv Research in Dynamical & Control Systems | September 2019 |
| 4 | The Delineation of Neck of Femur in Pelvic X-ray Images | Jour of Adv Research in Dynamical & Control Systems | October 2019 |
| 5 | Comprehensive Examination on Resistive Random Access Memory | International Journal of Recent Technology and Engineering (IJRTE) | November 2019 |
| 6 | Design and Implementation of Reversible Vedic Multiplier with Trlic | International Journal of Recent Technology and Engineering (IJRTE) | March 2020 |
| 7 | Deforestation a Cause of Pandemic – A Systematic Analysis | Journal of Physics | September 2020 (Submitted) |
| 8 | Power Algorithm to Improve the IOT Device for Lightweight Cryptography Applications | Submitted in Materials Today : Proceedings, | October 2020 (Submitted) |