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List of Publication for Past 5 Years:

S.No	Title	Year
1	Hybrid Energy Sources Fed Reduced Switch 31-level Inverter for Grid	
	Connected System	2020
	MukeshKumar.P, Umadevi.C, GnanaSundari.M	2020
	Journal of Interdisciplinary Cycle Research, xiii (iii), 127-135	
2	H Bridge Multilevel Inverters: State-of-the-art Topologies, Controls and	
	Modulation Strategies	2019
	Umadevi.C, GnanaSundari.M	2019
	Journal of Interdisciplinary Cycle Research, xi (xii)	
3	A Novel Modified Switched Capacitor Nine Level Inverter Topology with	
	Reduced Switch Count for High Frequency Power Distribution	2019
	K.Revathi, M.GnanaSundari	2019
	International Research Journal of Engineering and Technology, 6(6), 1573-1581	
4	Fundamental Frequency Modulated Cascaded H Bridge Inverter using Improved	
	Firefly Algorithm	
	M.GnanaSundari	2017
	International journal of Innovative works in Engineering and Technology,	
	3,424-444	
5	Real Time Implementation of Selective Harmonic Elimination of Multilevel	
	Inverter with non equal and time varying DC sources using Artificial Neural	
	Network.	2016
	M.GnanaSundari	2010
	International Journal of Innovative works in Engineering and Technology, 3,	
	487 - 499	
6	Natural Balancing of the Neutral Point Potential of a ThreeLevel Inverter with	2016
	Improved Firefly Algorithm	2010

	Journal of Power Electronic, 16, 1306 - 1315	
7	Application of improved firefly algorithm for programmed PWM in multilevel inverter with adjustable DC sources M.GnanaSundari, M Rajaram, Sujatha Balaraman Applied Soft Computing-Elsevier, 41,169 - 179	2015
8	FPGA Implementation of Selective Harmonic Elimination for Cascaded Multilevel Inverter with Adjustable DC sources using Artificial Bee colony Algorithm. M.GnanaSundari International journal of Innovative works in Engineering and Technology, 1, 119-130	2015