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Designation : Associate Professor/ECE
No. of journal Publications : 3
Area of Specialization : VLSI Signal Processing

Journal publications:

1. **Rajalakshmi.K**, Nivedita, R.,” VLSI implementation of Smith–Waterman algorithm forbiological sequence scanning”, Electrical Engineering, 453, 231-245, 2018.
2. R. Venkateswari, S. Subha Rani, **K. Rajalakshmi**, “An ultra low power MICS band receiver for implantable wireless body area networks”, International Journal of Information and Communication Technology,Vol 8, Issue 3,184-197,2016.
3. **Rajalakshmi, K.** and Kiruthika, A,” Reconfigurable FIR Architecture for The Filter Bank of Speech Processor in Cochlear Implant”, International Journal of Applied Engineering Research,ISSN 0973-4562, Vol 10, No.29, 2270-223, 2015.