

Publications during the last five years (2015-2020) - Dr. Sreedevi V T

Journal Publications:

1. Nilnjan Tewari, Sreedevi V T, “Family of modular, extendable and high gain dc–dc converter with switched inductor and switched capacitor cells”, IET Power Electronics, 13(7), 1321-1331, May 2020.
2. Reena Monica P, Sreedevi V T, “One instruction set computer with optimized polarity tunable model of double gate CNTFETs”, IET Circuits, Devices & Systems, vol.14, issue 6, pp 770-779, 2020.
3. Meenakshi J & Sreedevi V.T., “Analysis of a novel (LCR) trap-LC-RC filter with improved performance for standalone inverters”, International journal of electronics, 107(2), pp.310-330, Feb. 2020.
4. P. Sriramalakshmi & Sreedevi V T, “Design and Implementation of a dual DC source based Quasi switched boost inverter for renewable energy applications,” IETE Journal of Research, published online June 2020.
5. Sriramalakshmi, P., A. Arvindh, SR Sanjay Kumar, M. Prasanth, and V. T. Sreedevi, Analysis of Three-Phase Quasi-Switched Boost Inverter Topology for Renewable Energy Applications, Advances in Energy Research, Springer, Vol. 2, 863-876. May 2020.
6. Kumar, Rahul, Rahul Rambhad, P. Sriramalakshmi, and V. T. Sreedevi, Five-Level Switched Inductor-Based Cascaded H-Bridge Quasi-Switched Boost Inverter for Renewable Energy Applications, Advances in Greener Energy Technologies, Springer, 325-336., May 2020.
7. KanimozhiG, Sreedevi.V.T “Semibridgeless Interleaved PFC Boost Rectifier for PHEV BatteryChargers”, IETE Journal of Research, vol.65, issue1, pages- 128-138, 2019.
8. Reena Monica, P., Sreedevi, V.T.,” Suppression of ambipolar conduction in Schottky barrier carbon nanotube field effect transistors: Modeling, optimization using particle swarm intelligence, and fabrication”, CMES - Computer Modeling in Engineering and Sciences, vol.119, no.3, pp.577-591, 2019.

9. Nilanjan Tewari, VT Sreedevi, “ A novel single switch dc-dc converter with high voltage gain capability for solar PV based power generation systems, Solar energy, vol.171, pages-466-477, September 2018.
10. Devi Venkatesh, Sreedevi V.T,” Design and analysis of an integrated LC3–Valley fill passive LED driver, International Journal of Electronics, Vol.105, issue 12, Pages: 2052-206, 2018.
11. Sriramalakshmi Palanidoss, VT Sreedevi, “Experimental verification of three phase quasi switched boost inverter with an improved PWM control”, International Journal of Power Electronics and Drive Systems, Vol.10. issue 3, September 2019.
12. Devi Maheswaran, VT Sreedevi,” A Commercial Low Cost, Highly Efficient UC3842 based High Brightness LED (HBLED) Lamp, International Journal of Power Electronics and Drive Systems, vol.1, issue1, pp.1-7, March 2018.
13. Meenakshi Jayaraman, Sreedevi V T, “FPGA implementation of a passive filter for harmonic reduction in inverter outputs in PV-based renewable energy systems”, Advances in intelligent systems and computing (Springer), vol.632, pp.501-514, 2018.
14. Sriramalakshmi, P., Sreedevi, V.T.,” Single-Stage Boost Inverter Topologies for Nanogrid Applications, Advances in Smart Grid and Renewable Energy (Springer), vol.135, pp-215-226, 2018.
15. Meenakshi Jayaraman and Sreedevi V T, “Multilevel inverters a survey on topologies and modulation techniques”, Journal of advanced research in dynamical and control systems”, vol.10 no.15, pp.367-377, 2018.
16. Kanimozhi. G , Sreedevi V .T , “Improved resettable integrator control for a bridgeless interleaved AC/DC converter, Turkish journal of Electrical and Computer Engineering, Vol.25, No.5, Pages 3578-3590, 2017.
17. Meenakshi Jayaraman, Sreedevi V T, “Power quality Improvement in a cascaded multilevel inverter interfaced grid connected system using a modified inductive- capacitive-inductive filter with reduced power loss and improved harmonic attenuation”, Energies, Vol.10, Issue 11, Pages-1834, 2017.

18. G Kanimozhi, J Meenakshi, V T Sreedevi, "Small signal modeling of a dc – dc type double boost converter integrated with SEPIC converter using state space averaging approach, *Energy Procedia*, vol.117, pages-835-846, June 2017.
19. Sanjunath S., Meenakshi, J., and Sreedevi, V.T., "Analysis of a Passive Filter with Improved Power Quality for PV Applications," *International Journal of Control Theory and Applications*, vol. 10, no. 22, pp. 57-70, 2017.
20. Revanth Mallavarapu, Meenakshi Jayaraman, V. T. Sreedevi, "Simulation of a Seven Level Inverter and Its Comparison with a Conventional Inverter," *Advances in Intelligent Systems and Computing*, Vol. 467, Oct. 2016.
21. Eniaval R., Meenakshi, Jayaraman, and Sreedevi, V.T., "Simulation and Analysis of a Five Level Cascaded Inverter," *Journal of Advanced Research in Dynamical and Control Systems*, vol. 9, no. 1, pp.249-263, 2017.
22. K. Abinaya, Nilanjan Tewari and V.T. Sreedevi, "Steady State Analysis and Hardware Implementation of a Switched Capacitor Based High Gain DC-DC Boost Converter," *International Journal of Pure and Applied Mathematics*, Volume 117, No. 21, pp: 445-454, 2017.
23. Kanimozhi. G, and Sreedevi.VT, "Comparative analysis of Level-2 battery chargers for Plug-in Automotive Applications", *International Journal of Control Theory and its Applications*, Vol.9, Issue.5, 2016, pp.11-18.
24. Subham Ghosha, Nilanjan Tewari and Sreedevi V.T, "Modeling and Analysis of a Hybrid Switched Inductor Based DC-DC Boost Converter for Solar PV Application", *International Journal of Control Theory and Applications*, vol.9, no.52, pp:1-9, 2016.
25. Manasa K.V Nilanjan Tewari and Sreedevi V.T, "Design and Analysis of a Modified DC – DC Interleaved Buck Converter for Battery Charging Application," *International Journal of Control Theory and Applications*, vol.9, no. 52, pp: 9-14, 2016.

26. Reena Monica, P., Chaubey, N., Sreedevi, V.T., "An optimized geometry-physics based compact model of CNTFET, Materials Today: Proceedings, Volume 3, Issue 6, Pages 2295-2304, 2016.
27. Sai, M.V., Meenakshi, J., and Sreedevi, V.T., "Harmonic Reduction in a Neutral Point Clamped Multilevel Inverter," International Journal of Control Theory and Applications, vol. 9, no. 29, pp. 239-248, November, 2016.
28. Mallavarapu, R., Jayaraman, M., Sreedevi, V.T., "Simulation of a seven level inverter and its comparison with a conventional inverter, Advances in Intelligent Systems and Computing (Springer), Vol. 467, pp. 281-300, October 2016.
29. R Lakshmi Narayanan, Sriramalakshmi P and V T Sreedevi, "Comparison of High Gain DC-DC Boost Converters" International Journal of Advances in Electronics and computer science, Volume: 2, Issue: 5, pp. 56-62, May 2015.
30. Kanimozhi, G., Sreedevi, V.T., "Modeling and Control of Bridgeless Interleaved PFC boost converter", Transylvanian Review, Vol. 24, issue 11, pp. 2915-2924, 2016.
31. Harikrishna V., Meenakshi, J., and Sreedevi, V.T., "Simulation and Analysis of a Diode Clamped Inverter," International Journal of Control Theory and Applications, vol. 9, no. 52, pp. 435-448, 2016.
32. Venkateswara Rao Yamarthi, Meenakshi J, Sreedevi V.T., "Neutral Point Clamped and Cascaded H-Bridge multilevel inverter topologies A comparison," Indian Journal of Science and Technology, vol. 9, no. 45, pp. 1-8, December 2016.
33. Rejil, C., Jayaraman M., Sreedevi, V.T., "Analysis and design of single phase Z-source inverter with LC filter for PV applications, International Journal of Applied Engineering Research, Vol. 10, issue 55, pp. 642-647, 2015.
34. Gunturi, S.K., Kanimozhi, G., Sreedevi, V.T., "Comparative analysis of digital linear and non-linear controller for a PFC boost converter, International Journal of Applied Engineering Research, Vol. 10, (issue 20), pp. 15689-15693, 2015.

35. Sriramalakshmi, P., Sreedevi, V.T., Modified PWM control methods of Z source inverter for drive applications, ARPN Journal of Engineering and Applied Sciences Vol.10 (issue 16), pp.6932—6943, 2015.
36. Gowrinathan, M., Devi Maheswaran, V., Sreedevi, V.T. “Design and implementation of SMR based bidirectional laptop adapter, Lecture Notes in Electrical Engineering (Springer), Vol. 326 ,pp.475-483, 2015.
37. Kanimozhi, G., Sreedevi, V.T.,”ZVS implementation in interleaved boost rectifier, ARPN Journal of Engineering and Applied Sciences, Vol.10 (issue 16), pp.6988-6993, 2015.

National/International conferences (2015-2020)

1. Tewari N, Sreedevi. V.T., “Switched Inductor –Switched Capacitor based high gain hybrid dc-dc converter” IEEE- 8th India International conference on Power Electronics, IICPE, pages1-6, 2018.
2. Sakshi Singh; Somnath Ghosh; Srivatsan Sathiyamurthy; Nilanjan Tewari; Meenakshi J; V.T. Sreedevi, Performance evaluation of a high gain DC-DC converter for DC microgrid applications, 2020 IEEE Region 10 Symposium (TENSYP), June 2020.
3. P.Sriramalakshmi, Sreedevi V.T., A Novel Switched Inductor Switched Capacitor based quasi Switched Boost Inverter, ICAER 2019, IIT Bombay, 10-12 Dec 2019.
4. M. Priyanka, R. Niranchana, V. Gurjar, N. Tewari and V. T. Sreedevi, A dual switch non-isolated high gain dc-dc converter, TENCON 2019 – 2019 IEEE Region 10 Conference (TENCON), Kochi, India, 17-20 Oct 2019, pp. 1541-1546.

5. Jayaraman M. Sreedevi V T., “Design of a passive damped filter for harmonic reduction in multilevel inverters used in PV applications”, 8th IEEE Power India International Conference, PIICON -2018, pages-1-6.
6. Jayaraman, M., Sreedevi, V.T.” Implementation of LC and LCL passive filters for harmonic reduction in PV based renewable energy systems”, Pages- 363-369, IEEE- National Power Electronics Conference, NPEC 2017.
7. Meenakshi Jayaraman, VT Sreedevi,” FPGA implementation of a passive filter for harmonic reduction in inverter outputs in PV-based renewable energy systems”, International Conference on Intelligent Computing and Applications, pp 501-514,2017.
8. P. Sriramalakshmi, V.T. Sreedevi, A single phase cascaded five level quasi switched boost inverter based on switched capacitor structure,” IEEE International Conference on Power Electronics, Drives and Energy systems (PEDES), pp: 12-14, 2018.
9. Reena Monica P, Naveen Chaubey, Sreedevi V.T “An optimized geometry-physics based compact model of CNTFET”Materials Today: Proceedings, Volume 3, Issue 6 (2016), pp 2295-2304.
10. Sudheer L, Kanimozhi G, Sreedevi V T, “Integrator controlled semibridgeless PFC boost converter,” IEEE International Conference on Circuit, Power and Computing technologies, ICCPCT 2015.
11. Prajapati, P., Jayaraman, M., and Sreedevi, V.T., “Harmonic Elimination in a five level multilevel inverter,” Proc. 2016 International Conference on Computer, Communication and Informatics, 2016, pp. 1-6.
12. Kanimozhi .G, Sreedevi V.T, “ Comparative Analysis of level-2 battery chargers for plug in automotive applications, 3rd International conference on applied research in Engineering Methodology, Coimbatore, pp-55-60, 2016.
13. Sandeep kumar G, Kanimozhi G, Sreedevi.V.T, “Comparative analysis of digital linear and nonlinear controller for a PFC boost converter, IEEE -2nd International conference on Innovations in Information Embedded and Communication systems (ICIIECS’15) pp-304-308, 2015.