

1. List of Publications :

S. No.	Title of paper	Name of the journal Month and year of acceptance	Publication details Vol. No., Pg. No, Month, Year	ISSN/ ISBN Number
1.	FPGA Implementation of Selective Harmonic Elimination for Cascaded Multilevel Inverter with Adjustable DC sources using Artificial Bee colony Algorithm.	International journal of Innovative works in Engineering and Technology <i>Accepted August 2015</i>	Vol.1 PP.119-130, August 2015	ISSN 2455-5797
2.	Application of improved firefly algorithm for programmed PWM in multilevel inverter with adjustable DC sources	Applied Soft Computing-Elsevier <i>Accepted December 2015</i>	Vol.41 , PP169 - 179 , December 2015	ISSN 1568-4946
3.	Natural Balancing of the Neutral Point Potential of a ThreeLevel Inverter with Improved Firefly Algorithm	Journal of Power Electronics <i>Accepted November 2015</i>	Vol.16, PP1306 - 1315 , July 2016	ISSN 1598-2092
4.	Real Time Implementation of Selective Harmonic Elimination of Multilevel Inverter with non equal and time varying DC sources using Artificial Neural Network.	International journal of Innovative works in Engineering and Technology <i>Accepted December 2016</i>	Vol.(3) PP.379-390 December 2016	ISSN 2455-5797
5.	Fundamental Frequency Modulated Cascaded H Bridge Inverter using Improved Firefly Algorithm	International journal of Innovative works in Engineering and Technology <i>Accepted February 2017</i>	Vol.(3) PP.424-444 February 2017	ISSN 2455-5797

