Dr. D. KANNADASSAN- Publications

1. Kadiyam Rajshekar, Hsiao Hsuan Hsu, P. Sathyanarayanan, V. Velmurugan, Chun-Hu Cheng, and D. Kannadassan, “Physical Modeling of p-type Fluorinated Al-doped Tin-Oxide Thin Film Transistors, IEEE Journal of the Electron Devices Society, June 2020, Accepted for Publication
2. Kadiyam Rajshekar, Hsiao Hsuan Hsu, P. Sathyanarayanan, V. Velmurugan, Chun-Hu Cheng, and D. Kannadassan, “Effect of plasma fluorination in p-type SnO TFTs: Experiment, Modeling and Simulation” in IEEE Transactions on Electron Devices, vol. 66, no. 3, pp. 1314-1321, March 2019 (DOI: 10.1109/TED.2019.2895042)
3. P. Sumithra and D. Kannadassan, "Characteristic Mode Analysis of Circular Microstrip Patch Antennas," 2018 IEEE Indian Conference on Antennas and Propogation (InCAP), Hyderabad, India, 2018, pp. 1-4
4. P. Sumithra and D. Kannadassan, "Characteristic Mode Analysis of Concentric Circular Ring Antenna," 2018 3rd International Conference on Communication and Electronics Systems (ICCES), Coimbatore, India, 2018, pp. 1037-1040
5. Aparna Sanal, Sathyanarayanan P, V. Velmurugan, D. Kannadassan “Negative Voltage linearity in SiO2 MIM capacitors” Computational Electronics, March 2018, Volume 17, Issue 1, pp 458–462
6. Gagandeep Sing Ranu, D. Kannadassan, “Effect of Scalling in InGaZnO TFTs on the performance of LCD back-panel” Submitted to Journal of Nanoelectronics and Optoelectronics, May 2017.
7. Kadiyam Rajshekar, D. Kannadassan, “Subband gap states and Origin of P-type in CuO and Cu2O” Submitted to Displays (Elsevier), May 2017.
8. Gagandeep Singh Ranu, C. R. Lakshmi, D. Kannadassan, “Radiation Losses in Defected Ground Structures” 2nd IEEE International Conference on Engineering & Technology - 2016 (ICETECH-2016), Rathinam Technical Campus, Tamil Nadu, Coimbatore, India Feb 2016 {Best paper award}
9. Lochan Vyas et al, “Impact of bias, doping and High-k dielectric on RF Stability Performance of Junctionless Tri-Gate Transistor” IEEE SPONSORED 2’ND INTERNATIONAL CONFERENCE ON ELECTRONICS AND COMMUNICATION SYSTEMS(ICECS ‘2015)
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11. Karthik, D. Kannadassan, M. S. Baghini and P S Mallick, “Nanostructured Anodic Multilayer Dielectric Stacked Metal-Insulator-Metal Capacitors”, Journal of Nanoscience and Nanotechnology, 2015 Dec;15(12):9938-43.
12. R Karthik, D Kannadassan, P S Mallick, and Maryam Shojaei Baghini “High Performance Multi-layer Metal-Insulator-Metal Capacitors for future Integrated Circuits” IEEE Nano conference 2015 (Accpeted for publications)
13. Mohan K N. S R Zinka and Kannadassan D, “Design and analysis of Linear P;anar and Circular Array Using Array Tool”, International Journal of Applied Engineering Research,

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14. D Kannadassan, R Karthik, Maryam Shojaei Bhagini and P S Mallick, “Modeling the Voltage Nonlinearity of High-k MIM capacitors”, Solid State Electronics, Elsevier, 91, pp 112-117, Jan 2014.
15. D Kannadassan, R Karthik, Maryam Shojaei Bhagini and P S Mallick, “Nanostructured Metal-Insulator-Metal Capacitor using anodic titania”, Material Science for Semiconductor processing, Elsevier, 16, pp 274-281, 2013.
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21. K Sivasankaran, D Kannadassan, K Seetaram P S Mallick, “Geometry and Bias Optimization of Si nanowire transistor; RF stability perspective”, Microwave and Optical Technology Letters, Vol. 54, No. 9, September 2012.
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