Publications List

S.No	Author(s)	Title	Name of Journal	Volume	Page	Year
1.	Rajalakshmi, Nivedita,R.	VLSI implementation of Smith–Waterman algorithm for biological sequence scanning	Lecture Notes in Electrical Engineering	453,	231-245	2018
2.	R. Venkateswari, S. Subha Rani, K. Rajalakshmi	An ultra low power MICS band receiver for implantable wireless body area networks	International Journal of Informationand Communication Technology	Vol. Issue 8, 3	184 - 197	2016
3.	Rajalakshmi, K. and Kiruthika, A	Reconfigurable FIR Architecture for The Filter Bank of Speech Processor in Cochlear Implant	International Journal of Applied Engineering Research, ISSN 0973-4562	Vol. No.2 10 9	22370 -223	2015
4.	Rajalakshmi K., ,	An Efficient Digital	Australian	Vol.10,	217 –	2013.

		Gamma tone Filter Architecture for Cochlear implant	Journal of Electrical and Electronics Engineering,	No.2,	223	
5.	Rajalakshmi, K., Kandaswamy, A . and SwathiPriya,M.	"A Folded Architecturefor Digital Gamma Tone Filter of Speech Processor for Cochlear Implant",	ETRI Journal,	Vol.35, No.4,	697-705	August 2013
6.	Rajalakshmi, K., Kandaswamy, A. and Swathi Gondi	"A Fractional Delay FIRFilter based on Lagrange Interpolation of FarrowStructure",	International Journal of Electrical and Electronics Engineering	Vol. 10, No. 2,	104-107	2013
7.	Swathi Gondi, Rajalakshmi, K and Kandaswamy, A.	"Design of VariableFractional Delay based FIR filter",	National Journal of Technology,	Vol. 9, No. 1	48-53	March 2013.
8.	Rajalakshmi, K. and Kandaswamy, A.	"VLSI Architecture of Digital Auditory Filter for Speech Processor f Cochlear Implant",	Journal of Computer Applications,	Vol.39, No.7	19-22	February 2012