

## RESEARCH PUBLICATIONS

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3. S. Narendran, **J.Selvakumar**, "Digital Simulation of Superconductive Memory System Based on Hardware Description Language Modeling", Advances in Condensed Matter Physics, Article ID 2683723, 5 pages, <https://doi.org/10.1155/2018/2683723>, 2018.
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5. Narendran S, **Selvakumar J**, Vijayan K, "Reconfigurable supercomputing FFT processor using bit-slice Block Share Processing unit", Journal of microprocessor and microcontroller, Elsevier, vol.79, oct.2020. <https://doi.org/10.1016/j.micro.2020.103297>
6. Gomatheeswari B, J Selvakumar, "Appropriate allocation of workloads on performance asymmetric multicore architectures via deep learning algorithms", vol.73, Mar.2020, [www.sciencedirect.com/science/article/pii/S0141933119305885](http://www.sciencedirect.com/science/article/pii/S0141933119305885)
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11. **J.Selvakumar**, Edwin Prakash, "Data Acquisition System and Signal Processing Technique for Bearing Fault Analysis", Vol 9(31), DOI: 10.17485/ijst/2016/v9i31/92769, August 2016.
12. Sonal Sharma, **J.Selvakumar**, "Image Denoising using Median Filter Having SDC Comparator", International Journal of Science and Technology, vol.1, no.11, May.2015.
13. **J.Selvakumar**, S. Deepa, "High Energy Density of Emergency Capacitors", Asian Consortium on Computational material Science Theme Meeting on First principles Analysis & experiment: Role in Energy Research, ACCMS-TM 2016, pp: 119
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15. Koudinya, **J.Selvakumar**, "Clock Mesh Synthesis with Blockage Placement for STUB and Mesh wire Minimization", Recent Journal of recent Sciences
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