

List of Publications

1. "Performance Investigation of Gate Engineered tri-Gate SOI TFETs with Different High-K Dielectric Materials for Low Power Applications", Silicon (Springer), Impact factor: 1.210, First Online: 04 November 2019.
2. "Performance enhancement of triple material double gate TFET with heterojunction and heterodielectric", Solid State Electronics Letters (Elsevier publisher), vol. 1, pp. 64–72, Nov 2019.
3. "Impact of uniform and nonuniform doping variations for ultrathin body junctionless FinFETs" Materials Science in Semiconductor Processing (Elsevier publisher, impact factor: 2.72), Vol.104, Dec 2019.
4. "Impact of two gate oxide with no junction metal oxide semiconductor field effect transistor- an analytical model", Physica E: Low dimensional Systems and Nanostructures (Elsevier publisher, impact factor: 3.17), Vol. 113803, Available online 31 October 2019.
5. "Performance analysis of triple material tri gate TFET using 3D analytical modelling and TCAD simulation", AEÜ - International Journal of Electronics and Communications (Elsevier publisher, impact factor:2.115), Vol.110, Oct 2019.
6. "Improved drain current characteristics of tunnel field effect transistor with heterodielectric stacked structure", International Journal of Nano Dimension, Vol.10, No.4, pp.413-419, July 2019.
7. "An Analytical Modeling and Simulation of Surrounding Gate TFET with an Impact of Dual Material Gate and Stacked Oxide for Low Power Applications", Journal of Nano Research, Impact Factor: 0.6, Vol. 57, pp 68-76, April 2019.
8. "A Holistic Approach on Junctionless Dual Material Double Gate (DMDG) MOSFET with High k Gate Stack for Low Power Digital Applications", Silicon (Springer), Impact factor: 1.210, First Online: 27 March 2019.
9. "A new 2 D mathematical modeling of surrounding gate triple material tunnel FET using halo engineering for enhanced drain current", AEÜ - International Journal of Electronics and Communications (Elsevier publisher) impact factor:2.115, Vol.99, pp:34-39, Feb 2019.

10. "Mathematical Modeling of Junctionless Triple Material Double Gate MOSFET for Low Power Applications", Journal of Nano Research, Impact Factor: 0.6 Vol. 56, pp 71-79, Feb 2019.
11. "Resource minimization and power reduction of ESPFFIR filter using unified adder/subtractor", Analog Integrated Circuits and Signal Processing (Springer), Impact Factor: 0.8, Vol.98, No.1, Jan 2019.
12. "Implementation of VIP for bus interface logic of 32-bit processor using System Verilog, Journal of Microelectronics, Electronic Components and Materials Impact Factor: 0.476, Vol. 48, No. 4, pp.205 – 211, 2018.
13. "IOT Based Milk Monitoring System For Detection Of Milk Adulteration", International Journal of Pure and Applied Mathematics, Vol.118, No.9, pp.21-32, 2018.
14. "Analytical Modelling and Simulation of Triple Material Quadruple Gate Tunnel Field Effect Transistors", Journal of nano research, Impact Factor: 0.6, Vol. 54, pp 146-157, 2018.
15. "Design of adiabatic logic based comparator for low power and high speed applications", ICTACT Journal On Microelectronics, Vol.3, No.1, 365-369, 2017.
16. "Improved dynamic response of isolated full bridge DC to DC converter using BATA optimization tuned fuzzy sliding mode controller for solar applications", International Journal of Hydrogen Energy- (Elsevier), Impact Factor: 4.084 Vol. 42, pp. 21648 -21658.
17. "Low power VLSI architecture design of BMC, BPSC and PC scheme" Analog Integrated Circuits and Signal Processing (Springer), Impact Factor: 0.8 Vol.93, pp.169-178.
18. "Analytical Approach and Simulation of GaN Single Gate TFET and Gate All around TFET", ECTI transactions on electrical eng., electronics, and communications, Vol.15, No.02, pp. 1-7, 2017.
19. "Comparative Performance Analysis of Multi Gate Tunnel Field Effect Transistors", Journal of Nano Research, Impact Factor: 0.6, Vol. 41, pp 1-8), 2016.
20. "Drain Current Characteristics Of Silicon Nanowire Field Effect Transistor", ICTACT Journal On Microelectronics, Vol. 2, No.3, pp 284-287, 2016.

21. 'Potential and electric field model for 18 nm Germanium based Dual Material Gate tunnel field effect transistor', International Journal of Applied Engineering Research, Publisher: Research India Publications, Vol.10, No.1, pp.253-257., 2015.
22. 'A QOS Based EQGOR Protocol for WSN and VANET, International Journal of Applied Engineering Research, Publisher: Research India Publications, Vol.10, No.55, pp.1762-1766.
23. "2-D Analytical Modeling and Simulation of Dual Material Surrounding Gate Tunnel FET (DMSGTFET) For Diminished SCES", International Journal of Applied Engineering Research, Publisher: Research India Publications, Vol.10, No.7, pp. 18551-18564, 2015.