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3. Bharat Babu, Reeba Korah, A. Swarnalatha, " Vedic Multiplier using Nikhilam Navatascaramam Dasatah Sutra", Journal of Electrical Engineering. 419 – 425, Volume 18 issue 2, 2018, ISSN: 1335-36324

4. S.Daphin Lilda, A.Swarnalatha & R. Avudaiammal "Survey on Memristor Applications", International Journal of Pure and Applied Mathematics, Volume 118, No. 11 (2018).Pg 103 – 109 ISSN: 1311-8080 (printed version), doi: 10.12732/ijpam.v118i11.13

5. Lakshmi Narayanan S, Dr.ReebaKorah, Dr. A. Swarnalatha Novel 10-T Write Driver SRAM Design Using45 Nm CMOS Technology With Leakage Current Reduction Scheme For FPGA Routing Switch Architecuture, Journal of Nanoelectronics and Optoelectronics. 2017, Vol 12, Pp 1- 13, ISSN 1555-1318, doi:10.1166/jno.2017.2252

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