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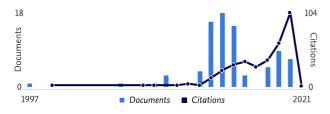
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· · · · · · · · · · · · · · · · · · ·	<., Arulchakkaravarthi	, A.,Sankar, S., Ramasamy, P.	Cit
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New analytical reffects	nodel for nano	scale tri-Gate SOI MOSFETs including quantum	16
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Analytical mode	eling and simul	ation of dual material gate tunnel field effect	15
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New dual mater modeling and si	_	junctionless tunnel FET: Subthreshold	14
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surrounding gat	e (TMSG) MOS	FETs	Cit
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			L. Cit
modeling and si		iya, G.	5
modeling and si Vanitha, P., Balamurug		ce, 2015, 15(6), pp. 585–593	
modeling and si Vanitha, P., Balamurug	r Technology and Scien	ce, 2015, 15(6), pp. 585–593 Related documents	
modeling and si Vanitha, P., Balamurug Journal of Semiconductor View abstract V	r Technology and Scien		
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Article

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biosensing application	Cited by
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View abstract View at Publisher Related documents	
Article	
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Vimala, P., Balamurugan, N.B.	Cited by
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stacked triple material surrounding gate tunnel field effect transistor	9 Cited by
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Superlattices and Microstructures, 2019, 130, pp. 485–498	
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Subthreshold Performance Analysis of Germanium Source Dual Halo Dual	8
Dielectric Triple Material Surrounding Gate Tunnel Field Effect Transistor for	Cited by
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Analytical threshold voltage modeling of surrounding gate silicon nanowire	7
transistors with different geometries	Cited by
Karthigai Pandian, M., Balamurugan, N.B. Journal of Electrical Engineering and Technology, 2014, 9(6), pp. 2079–2088	
View abstract View at Publisher Related documents	
Article Analytical modeling and simulation of germanium single gate silicon on	7
insulator TFET	7
Samuel, T.S.A., Balamurugan, N.B.	Cited by
Journal of Semiconductors, 2014, 35(3), 034002	
View abstract ✓ View at Publisher Related documents	
Article	
Modeling and simulation of nanoscale tri-gate MOSFETs including quantum	7
effects	Cited by
Vimala, P., Balamurugan, N.B. Journal of Semiconductors, 2014, 35(3), 034001	,
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Quantum mechanical compact modeling of symmetric double-gate MOSFETs	7
using variational approach Vimala, P., Balamurugan, N.B.	Cited by
Journal of Semiconductors, 2012, 33(3), 034001	

View abstract View at Publisher Related documents

Analytical Modeling of Dual Material Gate All around Stack Architecture of	C
Tunnel FET	
Balamurugan, N.B., Priya, G.L., Manikandan, S., Srimathi, G. Proceedings of the IEEE International Conference on VLSI Design, 2016, 2016-March, pp. 294–299, 7434968	
View abstract View at Publisher Related documents	
Article • Open Access	
Analytical surface potential model with TCAD simulation verification for evaluation of surrounding gate TFET	6
Arun Samuel, T.S., Balamurugan, N.B., Niranjana, T., Samyuktha, B. Journal of Electrical Engineering and Technology, 2014, 9(2), pp. 655–661	
View abstract View at Publisher Related documents	
Article • Article in Press Influence of Threshold Voltage Performance Analysis on Dual Halo Gate Stacked Triple Material Dual Gate TFET for Ultra Low Power Applications	5
Venkatesh, M., Balamurugan, N.B. <i>Silicon</i> , 2020	
View abstract View at Publisher Related documents	
Article	
Impact of uniform and non-uniform doping variations for ultrathin body junctionless FinFETs	5
Manikandan, S., Balamurugan, N.B., Arun Samuel, T.S. Materials Science in Semiconductor Processing , 2019, 104, 104653	
View abstract ∨ View at Publisher Related documents	
Article Performance analysis of fully depleted triple material surrounding gate (TMSG) SOI MOSFET	5
Suveetha Dhanaselvam, P., Balamurugan, N.B. Journal of Computational Electronics, 2014, 13(2), pp. 449–455	
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Article • Open Access A 2D analytical modeling of single halo triple material surrounding gate (SHTMSG) MOSFET	5
Suveetha Dhanaselvam, P., Balamurugan, N.B., Vivek Chakaravarthi, G.C., Ramesh, R.P., Sathish Kumar, B.R. Journal of Electrical Engineering and Technology , 2014, 9(4), pp. 1355–1359	
View abstract View at Publisher Related documents	
Article	
A 2D sub-threshold current model for single halo triple material surrounding gate (SHTMSG) MOSFETs	5
Suveetha Dhanaselvam, P., Balamurugan, N.B. Microelectronics Journal, 2014, 45(6), pp. 574–577	
View abstract View at Publisher Related documents	
Article Influence of Germanium Source Dual Halo Dual Dielectric Triple Material Surrounding Gate Tunnel FET for Improved Analog/RF Performance Venkatesh, M., Suguna, M., Balamurugan, N.B. Silicon, 2020, 12(12), pp. 2869–2877	4
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Chandra, S.T., Balamurugan, N.B. Journal of Semiconductors, 2014, 35(4), 044001

Subthreshold behavior of AlInSb/InSb high electron mobility transistors	Cit
Theodore Chandra, S., Balamurugan, N.B., Lakshmi Priya, G., Manikandan, S. Chinese Physics B, 2015, 24(7), 076105	
View abstract ✓ View at Publisher Related documents	
Article C. L. C. L	
Analysis of charge density and Fermi level of AlInSb/InSb single-gate high electron mobility transistor	2 Cit
Chandra, S.T., Balamurugan, N.B., Bhuvaneswari, M., Anbuselvan, N., Mohankumar, N. <i>Journal of Semiconductors</i> , 2015, 36(6), 064003	
View abstract ✓ View at Publisher Related documents	
Article	
2-D analytical modeling and simulation of dual material surrounding gate tunnel fet (DMSGTFET) for diminished SCES	2 Cit
Vanitha, P., Balamurugan, N.B., Arun Samuel, T.S. International Journal of Applied Engineering Research, 2015, 10(7), pp. 18551–18563	
View abstract ✓ Related documents	
Article	
Compact analytical model for single gate AlInSb/InSb high electron mobility transistors	2 Cit
Chandra, S.T., Balamurugan, N.B., Subalakshmi, G., Shalini, T., Priya, G.L. Journal of Semiconductors, 2014, 35(11), 114003	3.0
View abstract ✓ View at Publisher Related documents	
Article	
Analytical modeling of drain current, capacitance and transconductance in symmetric double-gate mosfets considering quantum effects	2 Cit
Palanichamy, V., Balamurugan, N.B. International Journal of Nanoscience, 2013, 12(1), 1350005	
View abstract ✓ View at Publisher Related documents	
Article	
An optimal strategy for analysing the characteristics of optical fiber waveguide Sundaravadivelu, S., Perumal Sankar, S., Balamurugan, N.B., Rajaram, R.	2
Journal of Optics (India), 1997, 26(3), pp. 147–156	Cit
View abstract VRelated documents	
Article	
The improved RF/stability and linearity performance of the ultrathin-body Gaussian-doped junctionless FinFET	$1 \ _{ ext{Cit}}$
Manikandan, S., Balamurugan, N.B. Journal of Computational Electronics, 2020, 19(2), pp. 613–621	
View abstract ✓ View at Publisher Related documents	
Article	
Influence of Temperature in Scattered SiNW MOSFET	1
Sheik Arafat, I., Balamurugan, N.B., Bismillah Khan, S. Proceedings of the National Academy of Sciences India Section A - Physical Sciences, 2019, 89(1), pp. 35–40	Cit
View abstract ✓ View at Publisher Related documents	
Article	
Subthreshold modeling of triple material gate-all-around junctionless tunnel FET with germanium and high-K gate dielectric material	1 Cit
Priya, G.L., Balamurugan, N.B. Informacije MIDEM, 2018, 48(1), pp. 53–61	-
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semiconductor field effect transistor	
Sheik Arafat, I., Balamurugan, N.B.	
Journal of Nanoscience and Nanotechnology, 2016, 16(6), pp. 6032–6036	
View abstract View at Publisher Related documents	
Article	1
Triple material gate work function engineering in surrounding gate nanoscale	C
mosfets for reduced short channel effects (Sce's): Scale length model	
Lakshmi Priya, G., Balamurugan, N.B. International Journal of ChemTech Research, 2015, 7(2), pp. 1005–1013	
View abstract ✓ Related documents	
Article	
TCAD simulation of single-electron transistor with the recessed channel	1
Sheela, L., Balamurugan, N.B., Thanga Ananth, J. International Journal of Applied Engineering Research, 2014, 9(24), pp. 23781–23788	C
View abstract ∨ Related documents	
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Potential and quantum threshold voltage modeling of gate-all-around	1
nanowire MOSFETs	1
Pandian, M.K., Balamurugan, N.B., Pricilla, A.	
Active and Passive Electronic Components, 2013, 2013, 153157	
View abstract View at Publisher Related documents	
Conference Paper	
Fuzzy processor based on VLSI a review	1
lothi, M., Balamurugan, N.B., Harikumar, R.	-
Proceedings - 2013 IEEE International Multi Conference on Automation, Computing, Control, Communication and Compressed Sensing, iMac4s 2013, 2013, pp. 216–222, 6526411	
View abstract View at Publisher Related documents	
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Conference Paper	
Performance analysis and threshold voltage modeling of Surrounding Gate	1
Silicon Nanowire Transistors	C
Regila Manohari, M., Karthigai Pandian, M., Balamurugan, N.B.	
2013 International Conference on Emerging Trends in VLSI, Embedded System, Nano Electronics and Telecommunication System, ICEVENT 2013, 2013, 6496583	
View abstract ✓ View at Publisher Related documents	
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MOSFETs for nano-scale CMOS circuit design	1
Balamurugan, N.B., Sankaranarayanan, K., Priyadharsini, V., Ishwarya Devi, M.	C
Proceedings of ICSCN 2008 - International Conference on Signal Processing Communications and Networking, 2008, pp. 354–358, 4447218	
View abstract View at Publisher Related documents	
Conference Paper A new analytic description of short-channel effects in fully depleted single	-
A new analytic description of short-channel effects in fully depleted single gate SOI MESFETs for low power VLSI applications	1
Balamurugan, N.B., Sankaranarayanan, K., Suguna, M., Balasubadra, K., Kalaivani,	C
Proceedings of ICSCN 2007: International Conference on Signal Processing Communications and Networking, 2007, pp. 382–387, 4156649	
resident and the Company of Distriction of Challeton decreases and	
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Article Three-dimensional analytical modeling for small-geometry AlInSb/AlSb/InSb	

Venish Kumar, T., Balamurugan, N.B. Journal of Computational Electronics, 2020, 19(3), pp. 1107-1115 View abstract View at Publisher Related documents Article Analytical Model of Double Gate Stacked Oxide Junctionless Transistor 0 Considering Source/Drain Depletion Effects for CMOS Low Power Applications Cited by Manikandan, S., Balamurugan, N.B., Nirmal, D. Silicon, 2020, 12(9), pp. 2053-2063 View abstract View at Publisher Related documents Article • Article in Press Investigation of Ambipolar Conduction and RF Stability Performance in Novel 0 Germanium Source Dual Halo Dual Dielectric Triple Material Surrounding Gate Cited by Venkatesh, M., Priya, G.L., Balamurugan, N.B. Silicon, 2020 View abstract View at Publisher Related documents Article • Article in Press Analytical Modeling of Surrounding Gate Junctionless MOSFET Using Finite 0 Differentiation Method Cited by Preethi, S., Balamurugan, N.B. Silicon, 2020 View abstract View at Publisher Related documents **Editorial** Preface 0 Rajaram, S., Gracia Nirmala Rani, D., Balamurugan, N.B., Singh, V. Communications in Computer and Information Science, 2019, 892, pp. V-VI Cited by Conference Paper A Comparison of Analytical Modeling of Double Gate and Dual material Double 0 GateTFETs with high-KStacked Gate-Oxide Structure forLow power Cited by **Applications** Dharshana, V., Fathima, A.Y., Harinie, S., ...Ajitha, V., Balamurugan, N.B. Proc. IEEE Conference on Emerging Devices and Smart Systems, ICEDSS~2018,~2018,~pp.~92-96,8544357 View abstract View at Publisher Related documents Conference Paper Two Isolated Depletion Regions Analytical Model of Sheet Carrier Density and 0 Threshold Voltage for InAlAs/InGaAs HEMTs Cited by Sowmya, K., Venkatesh, M., Rojana, R., ... Swathika, K., Balamurugan, N.B. Proc. IEEE Conference on Emerging Devices and Smart Systems, ICEDSS 2018, 2018, pp. 88-91,8544330 View abstract View at Publisher Related documents Comparative analytical analysis of various configurations of nanoscaled 0 dielectric-modulated double gate MOSFET based biosensors Cited by Buvaneswari, B., Balamurugan, N.B. Journal of Optoelectronics and Advanced Materials, 2018, 20(9-10), pp. 526-536 View abstract VRelated documents Conference Paper Analytical modeling of nanoscale quad gate MOSFET including quantum 0 mechanical effects Cited by Balamurugan, N.B., Surya Abirami, M., Buvaneswari, B., Sowmya, K.

ICEDSS 2016 - IEEE Conference on Emerging Devices and Smart Systems, Proceedings, 2016, 2016-

March, 7587778

View abstract View at Publisher Related documents Conference Paper A Novel scaling theory for Single Gate AlInSb/InSb High Electron Mobility 0 **Transistors** Cited by Lakshmipriya, G., Manikandan, S., Balamurugan, N.B., Chandra, S.T. 2014 International Conference on Communication and Network Technologies, ICCNT 2014, 2015, 2015-March, pp. 211-215, 7062757 View abstract View at Publisher Related documents Article Temperature effect in scattered SiNW MOSFET 0 Sheik Arafat, I., Balamurugan, N.B. Cited by International Journal of Applied Engineering Research, 2015, 10(55), pp. 123-127 View abstract VRelated documents Article 2-D analytical modeling and simulation of dual material surrounding gate 0 tunnel field effect transistor with halo doping Cited by Vanitha, P., Balamurugan, N.B. International Journal of Applied Engineering Research, 2015, 10(13), pp. 32853-32857 View abstract VRelated documents Article TCAD simulation study of Algan/Gan high electron mobility transistors(HEMT) 0 Venishkumar, T., Balamurugan, N.B. Cited by International Journal of Applied Engineering Research, 2015, 10(11), pp. 30083-30089 View abstract V Related documents Article Compact modeling of gate engineered triple material gate (TMG) AlInSb/InSb 0 high electron mobility transistors Cited by Chandra, S.T., Balamurugan, N.B., Priya, G.L., Muralidharan, V., Rani, D.S.S.R. Journal of Optoelectronics and Advanced Materials, 2015, 17(1-2), pp. 222-228 View abstract V Related documents Article Simulation of single electron transistor for low power VLSI circuits 0 Sheela, L., Balamurugan, N.B., Thanga Ananth, J. Cited by International Journal of Applied Engineering Research, 2015, 10(7), pp. 16833-16842 View abstract V Related documents Article Tri-material gate work function engineering of gate-all-around (GAA) nanowire tunnel field effect transistors: Scale length model Cited by Saraswathi, D., Balamurugan, N.B., Lakshmi Priya, G. International Journal of Applied Engineering Research, 2015, 10(2), pp. 3627–3638 View abstract V Related documents Article 0 Analytical approach on the scale length model for tri-material surrounding Cited by gate tunnel field-effect transistors (TMSG-TFETs) Vanitha, P., Priya, G.L., Balamurugan, N.B., Chandra, S.T., Manikandan, S. Advances in Intelligent Systems and Computing, 2015, 343, pp. 231–238 View abstract View at Publisher Related documents Article Comparative analysis of quantum effects in nanoscale multigate mosfets using 0 variational approach Cited by

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view abstract • View at Fublisher Related documents	
Conference Paper Performance evaluation of a fuzzy processor	0
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View abstract ✓ View at Publisher Related documents	
Conference Paper	
FPGA synthesis of SIRM fuzzy system-classification of diabetic epilepsy risk	0
levels from EEG signal parameters and CBF	Ci
Balamurugan, N.B., Jothi, M., Harikumar, R. Lecture Notes in Electrical Engineering, 2013, 221 LNEE(VOL. 1), pp. 313–322	
View abstract ✓ View at Publisher Related documents	
Conference Paper	
High performance double gate silicon nanowire transistors	0
Gandi, M.S., Pandian, M.K., Balamurugan, N.B. 2013 International Conference on Emerging Trends in VLSI, Embedded System, Nano Electronics and Telecommunication System, ICEVENT 2013, 2013, 6496585	Ci
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Parameter extraction of single electron transistor based on Master Equation	0 Ci
approach	
Sheela, L., Sudha, S., Balamurugan, N.B.	
2013 International Conference on Emerging Trends in VLSI, Embedded System, Nano Electronics and Telecommunication System, ICEVENT 2013, 2013, 6496581	
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Conference Paper	
Modeling the inversion charge centroid in Tri-Gate MOSFETs including	0
quantum effects	Ci
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2013 International Conference on Emerging Trends in VLSI, Embedded System, Nano Electronics and Telecommunication System, ICEVENT 2013, 2013, 6496577	
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Conference Paper	
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Samuel, T.S.A., Balamurugan, N.B. 2013 International Conference on Emerging Trends in VLSI, Embedded System, Nano Electronics and	Ci
Telecommunication System, ICEVENT 2013, 2013, 6496580	
View abstract ✓ View at Publisher Related documents	
Conference Paper	
A surface potential model of junctionless surrounding gate(JLSG) MOSFET	0
Suveetha Dhanaselvam, P., Balamurugan, N.B., Nithya Ananthi, A. Proceedings of the 2013 International Conference on Green Computing, Communication and	Ci
Conservation of Energy, ICGCE 2013, 2013, pp. 237–239, 6823435	
Suveetha Dhanaselvam, P., Balamurugan, N.B., Nithya Ananthi, A. Proceedings of the 2013 International Conference on Green Computing, Communication and	0 Cr

Journal of Engineering Science and Technology, 2015, 10(2), pp. 224–234

View abstract View at Publisher Related documents

Conference Paper Electrical characteristics of silicon and germanium nanowire transistors - A 0 simulation study Cited by Chandra, S.T., Balamurugan, N.B. Proceedings of the 2012 International Conference on Communications, Devices and Intelligent Systems, CODIS 2012, 2012, pp. 165-167, 6422162 View abstract View at Publisher Related documents Conference Paper Quantum centroid modeling for surrounding gate MOSFETs 0 Vimala, P., Balamurugan, N.B. Cited by Proceedings of the 2012 International Conference on Communications, Devices and Intelligent Systems, CODIS 2012, 2012, pp. 168-171, 6422163 View abstract View at Publisher Related documents Conference Paper • Open Access FPGA synthesis of SIRM fuzzy system-classification of diabetic epilepsy risk 0 Cited by Balamurugan, N.B., Jothi, M., Harikumar, R. Procedia Engineering, 2012, 38, pp. 391-404 View abstract View at Publisher Related documents Article A new scaling theory for effective conducting path effect of dual material 0 surrounding gate MOSFETs in CMOS applications Cited by Balamurugan, N.B., Sankaranarayanam, K., Fathima John, M. Modelling, Measurement and Control A, 2009, 82(1-2), pp. 17-32 View abstract VRelated documents Article A new 2-D analytical threshold voltage model for dual material Surrounding 0 Gate MOSFETS for low power VSLI applications Cited by Balamurugan, N.B., Sankaranarayanam, K., Younus Ali, M., Sugana, M. Modelling, Measurement and Control A, 2009, 82(1-2), pp. 72-86 View abstract V Related documents Display 200 results < Previous Next > ① Learn more about Scopus profiles Back to top

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