

**Name** : Dr. S. Rajesh  
**Designation** : Professor and Head,  
**Department** : Department of Applied Physics,  
Karunya Institute of Technology and Sciences  
**Address** : Karunya Nagar, Coimbatore - 641112  
**Mobile** : +91- 9486256012  
**E-mail** : [drsrajesh@karunya.edu](mailto:drsrajesh@karunya.edu)

1. Sahayaraj, K., M. Roobadevi, **S. Rajesh**, and S. Azizi. "Vernonia cinerea (L.) Less. silver nanocomposite and its antibacterial activity against a cotton pathogen." *Research on Chemical Intermediates* 41, no. 8 (2015): 5495-5507.
2. Mohan, A., and **S. Rajesh**. "Impact of annealing on the investigation of  $\text{In}_3\text{Se}_2/\text{Cu}_2\text{Se}/\text{In}_3\text{Se}_2$  sandwich structure prepared by thermal evaporation technique for solar cell applications." *Superlattices and Microstructures* 85 (2015): 638-645.
3. Balaji, M., J. Chandrasekaran, M. Raja, and **S. Rajesh**. "Structural, optical and electrical properties of Ru doped  $\text{MoO}_3$  thin films and its P–N diode application by JNS pyrolysis technique." *Journal of Materials Science: Materials in Electronics* 27, no. 11 (2016): 11646-11658.
4. Mohan, A., **S. Rajesh**, and G. Sriresh. "CuInSe<sub>2</sub> formation through  $\text{Cu}_2\text{Se}$ – $\text{In}_3\text{Se}_2$  multilayer structures prepared by thermal evaporation technique." *Superlattices and Microstructures* 93 (2016): 261-268.
5. Tharayil, Trijo, Lazarus Godson Asirvatham, **S. Rajesh**, and Somchai Wongwises. "Effect of nanoparticle coating on the performance of a miniature loop heat pipe for electronics cooling applications." *Journal of Heat Transfer* 140, no. 2 (2018).
6. Chelliah, Cyril RA John, and **Rajesh Swaminathan**. "Current trends in changing the channel in MOSFETs by III–V semiconducting nanostructures." *Nanotechnology Reviews* 6, no. 6 (2017): 613-623.
7. Mohan, A., and **S. Rajesh**. "Temperature induced CuInSe<sub>2</sub> nanocrystal formation in the  $\text{Cu}_2\text{Se}$ – $\text{In}_3\text{Se}_2$  multilayer thin films." *Superlattices and Microstructures* 104 (2017): 186-204.
8. Tharayil, Trijo, Lazarus Godson Asirvatham, **S. Rajesh**, and Somchai Wongwises. "Thermal Management of Electronic Devices Using Combined Effects of Nanoparticle Coating and Graphene–Water Nanofluid in a Miniature Loop Heat Pipe." *IEEE Transactions On Components, Packaging and Manufacturing Technology* 8, no. 7 (2018): 1241-1253.
9. Balasubramani, V., J. Chandrasekaran, R. Marnadu, P. Vivek, S. Maruthamuthu, and **S. Rajesh**. "Impact of Annealing Temperature on Spin Coated  $\text{V}_2\text{O}_5$  Thin Films as Interfacial Layer in  $\text{Cu}/\text{V}_2\text{O}_5/\text{n-Si}$  Structured Schottky Barrier Diodes." *Journal of Inorganic and Organometallic Polymers and Materials* 29, no. 5 (2019): 1533-1547.