

JOURNALS	
1.	S.Manoj prabhakar, K.Kalyani and S.Rajaram, FPGA implementation of efficient FFT architecture for OFDM applications, International journal of control theory and applications,ISSN-0974-5572.volume:No:10(2017), Issue No:6(2017)pages:709-717.
2.	K.Kalyani and S.Rajaram,Hardware Implementation of Novel Symbol Detection Algorithm for 4G LTE Downlink Receiver,Wireless Personal Communications, ISSN-1572-834X, April 2017(online),ISSN-0929-6212,Volume No:96,Issue :1,pp 997-988,sep 2017
3.	K.Kalyani,S.Rajaram,A Novel Hybrid QR Decomposition Algorithm for MIMO OFDM based LTE Downlink Receiver,International journal of printing,packaging and Allied sciences,ISSN-2320-4387,Vol 4,No.1(2016),PP-480-496,December 2016.
4.	Suganya,R, Rajaram.S, Sheik Abdullah.A & V.Rajendran, 2016, “Prediction of Heart Diseases using Hybrid Feature Selection and Modified Laplacian Pyramid Non linear Diffusion with Soft Computing Methods”, Paper published in International Journal of Biomedical Engineering and Technology (IJBET-144704), Inderscience, Updated Annexure list.
5.	Suganya, R, Rajaram.S, Sheik Abdullah.A & Rajendran. V, 2016, “A Novel Feature Selection method for Predicting Heart Diseases with Data mining Techniques”, paper published in Asian Journal of Information Technology (AJIT) Updated Annexure list, volume:15, Issue:8, pp.no: 1314- 1321, 2016. DOI: 10.3923/ajit.2016.1314.1321
6.	K.Kalyani, S.Rajaram, Implementation of Approximate Matrix Inversion for Multi – User Cellular Systems,Asian Journal of Social science and humanities, ISSN 2249-7315,Vol6, No 11, PP.600-609, November 2016.
7.	Gracia Nirmala Rani.D & Rajaram.S 2016 , ‘Analysis and Design Of VLSI Floorplanning Algorithms For Nano-Circuits’ International Journal of Advanced Engineering Technology, Vol. VII/Issue I/Jan.- March., 2016/527-532.
8.	K.Kalyani, S.Rajaram, S.N.V.Saipratap, “A novel QR factorization approach to Multi-robot localization and mapping” International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.2 (2015) pp. 1356-1360.
9.	Gracia Nirmala Rani, D & Rajaram, S, ‘A Novel Differential Evolution based optimization algorithm for Non-Sliceable VLSI Floorplanning’ 2015, Iranian Journal of Science and Technology Transaction A, Article 5, Volume 39, Issue 3.1, Autumn 2015, Page 375-382.

10. V. R. Venkatasubramani, N. Premkumar, K. Vignesh S. Rajaram.'A High-performance Elliptic Curve Cryptographic Processor for FPGA Platform', Intelligent Computing and Applications Advances in Intelligent Systems and Computing Volume 343, 2015, pp 307-312.
11. Chinna thambi M, Bharani dharan N, Rajaram S, ' ASIC Implementation of Fast and Area Efficient Numerically Controlled Oscillator Based on CORDIC', International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.2 (2015) pp. 1365-1369.
12. Bharani dharan N, Chinna thambi M, Rajaram S, ' FPGA Implementation of Novel Discrete Phase-Locked Loop', Intelligent Computing and Applications Advances in Intelligent Systems and Computing Volume 343, 2015, pp 223-229.
13. D.Gracia Nirmala Rani, Amalin Marina, V.Dharshana & S.Rajaram,"Testing of Reversible Logic Circuits using automatic Test pattern generation techniques" 2015, International journal of Applied Engineering Research, Vol.27, No.4,2015,pp 553 – 558.
CONFERENCES
1. P.Steffi Shakila, V.Vinoth Thyagarajan, S.Rajaram, FPGA Implementation of filtering algorithm for Multispectral satellite image, International Conference on Inventive systems and control(ICISC-2018)l, JCT College of engineering and technology, Coimbatore,19-20, January 2018.
2. A.Andrew Roobert, D.Gracia Nirmala Rani, M.Divya, S.Rajaram, Design of CMOS Based LNA for 5G Application, International Conference on Communication and Broadband Networking, Nanyang Technical University, Singapore,24-26 e 2018.
3. I.Stefigraf, S.Rajaram, Design and Analysis of Low Noise Amplifier for Satellite Transponder, International Conferences on Circuits and Systems(ICCS 2017),Mar Baselios College of Engineering and Technology,Trivandrum,Kerala,20-21 Dec 2017.
4. K.Kalyani,S.Rajaram,FPGA implementation of efficient FFT architecture for OFDM applications,SCESM 2017(2nd International Conference on sustainable Computing Techniques in Engineering,Science and Management,27 & 28 Jan 2017.
5. K.Manimekalai,S.Rajaram,FPGA Implementation of Auto-correlation based spectrum sensing for cognitive radio,2nd International conference on Recent Trends in Engineering and technology, St.Joseph's Institute of technology,Chennai,5-6 May 2017.
6. I.Swathi,S.Rajaram, FPGA Implementation of Viterbi Decoder for Software Defined Radio Applications, IEEE WiSPNET 2017.

7. B.Kowsalya, S.Rajaram, Design and Analysis of High Gain CMOS Transconductance Amplifier for Low Frequency Applications,International Conferences on Circuits and Systems(ICCS 2017),Mar Baseliol College of Engineering and Technology,Trivandrum,Kerala,20-21 Dec 2017.
8. K.Kalyani,S.Rajaram,High speed and low complexity XOR free technique based data encoder architecture,2nd International Conference on Recent Trends in Engineering and Technology(ICRTET-17),5th and 6th may 2017
9. R.Suganya,S.Rajaram,Embedded visual cryptography for secure transmission of bank cheque,International Conference on Circuits and Systems 2017.
10. Venkatasubramani. VR, Rajaram.S, Deepika.V, Shanmugapriya. M,FPGA Implementation of Delay Based Physically Unclonable Function,Proceedings of International Conference on Electrical, Electronics and Communication (ICEEC'16),31st Mar & 1st Apr 2016
11. R.Suganya,S.Rajaram,J.Kesava rajkumar,T.SenthilKumar,Role of Teacher in Outcome Based Education:Facts and Trends,Fourth IEEE International Conference on MOOCS,Innovation and Technology in Education -MITE 2016,TCE,9-10 Dec 2016.
12. R.Suganya,S.Rajaram,A.Sheik Abdullah,V.Rajendran,Classification of Cardiac diseases using Hybrid Support Vector Machine,International Conference on Business Analytics and Intelligence(ICBAI 2016),Indian Institute of science,IISC,Bangalore,19-20 Dec 2016.
13. R.Suganya,K.Angaleeswari,A.Sheik Abdullah,S.Rajaram,T.Senthil kumar,Application of Big Data in intelligent Transportation System by using Modified Kruskal's Algorithm,International Conference on Business Analytics and Intelligence(ICBAI-2016),Indian Institute of Science ,IISC,Bangalore,19-21 Dec 2016.
14. G.Divya Preethi, Dr.S.Rajaram,FPGA Implementation of Cryptographic Algorithm for Internet of Things (IoT),International Conference On International Conference On Computing, Communication, Nanophotonics, Nanoscience, Nanomaterials And Nanotechnology(i2c4n-2k16),31st Mar & 1st Apr 2016
15. K.Kalyani, S.Rajaram, G.Sujith Simon, V.Ralph Aloysius,FCT implementation based on MARS,International conference on Electrical, Electronics and communicationICEEC 2016,31st March &1st April 2016
16. Rajaram.S, Vignesh.S & Yogeswaran.P ,FPGA Design and Implementation of MIMO Decoder for Wireless LAN,Proceedings of International Conference on Electrical, Electronics and Communication (ICEEC'16),31st Mar & 1st Apr 2016

Dr. S. Rajaram,

LIST OF PUBLICATIONS (LAST 5 YEARS)

Head of the ECE Department,

Thiagarajar college of Engineering.

17. Suganya R, S.Karthiga, A.Krishna Priya, S.Rajaram, Intelligent Transportation System for Safety by using Wireless Sensor Network, First International Conference on Electrical Electronics and Communication – ICEEC'16, Karaikudi, 31st Mar & 1st Apr 2016
18. Venkatasubramani. VR, Rajaram.S, Balasubramaniam. S, VLSI Implementation of Itoh-Tsujii Inversion Algorithm Over Binary Field, Proceedings of International Conference on Electrical, Electronics and Communication (ICEEC'16), 31st Mar & 1st Apr 2016
19. Rajaram.S, Vinoth Thyagarajan.V & Silambarasan.N, Analysis of Substrate Induced Noise in Annular and Co-axial Through Silicon Via used in 3D Ics, Proceedings of International Conference on Electrical, Electronics and Communication (ICEEC'16), 31st Mar & 1st Apr 2016
20. K.Kalyani, S.Rajaram, T.Suja, M.Harsha Vardhini, VLSI implementation of LMS adaptive filter using Distributed arithmetic algorithm, International conference on Electronics and communication systems (ICECS), Karpagam college, 25th & 26th Feb 2016
21. Dr.S.Sridevi, G.Mahalakshmi, Dr.S.Rajaram, "A Survey on Forecasting of time series data" in the IEEE International Conference on ICCTIDE'16 organized by NEC, Kovilpatti during 7 and 9th January'2016.
22. Suganya R, Rajaram.S, Vishalini.S, Meena.R, Senthil kumar.T, Dental Image Retrieval using Fused local binary pattern & Scale invariant feature transform, Second International Symposium on signal Processing and Intelligent Recognition Systems (SIRS'15) conducted by IIITM-Kerala, Technopark, 16 to 19 Dec 2015
23. Dr.S.Sridevi, G.Mahalakshmi, Dr.S.Rajaram, Forecasting of time series data using improved Particle Swarm Optimization, Third International Conference on BAI conducted by IIM, Bangalore, 17th & 19th dec 2015
24. K.Kalyani, S.Rajaram, S.N.V.Saipratap, A novel QR factorization approach to Multi-robot localization and mapping, International conference on Emerging trends in Engineering and Technology (ICET 2K15), K.N.S.K. CE, Nagarcoil, Tamilnadu, 26th & 27th March 2015.
25. Chinna thambi M, Bharani dharan N, Rajaram S, FPGA Implementation of Low Cost Matrix Inverse Computation for MIMO Systems, IEEE Sponsored 2nd International Conference on Innovations in Information, Embedded and Communication systems (ICIIECS) 19th and 20th March, 2015.
26. Bharani dharan N, Chinna thambi M, Rajaram S, Low Complexity Arithmetic Mean Decomposition Based Pre-coding for MIMO Systems and its FPGA Implementation, IEEE Sponsored 2nd International Conference on Innovations in Information, Embedded and Communication systems (ICIIECS) 2015.
27. K.Kalyani, P.P.Omnath, K.Priyadharshini, S.Srinath, S.Rajaram, FPGA Implementation of 2D parallel distributed arithmetic based DCT architecture, IEEE Sponsored 2nd International Conference on

Innovations in Information, Embedded and Communication systems (ICIIECS) 2015, 19th and 20th March, 2015
28. V.Vinoth Thyagarajan, S.Preetham kumar, V.surya Gurumoorthy, P.Nantha Kumar, S.Rajaram, 'Delay analysis in cylindrical and coaxial TSV for 3D IC', IEEE Sponsored 2nd International Conference on Innovations in Information, Embedded and Communication systems (ICIIECS) 2015, 19th and 20th March, 2015
29. Suganya, R, Rajaram, S, Meena, R, Vishalini, Sornalatha 2015, "Selection of Speckle reduction filter for Medical Image Retrieval", in National conference on Innovations in IT, Management & Education- Digital India Initiative (IIMEDII 2015) at Maharaj Surajmal Institute, Newdelhi.
30. D. Gracia Nirmala Rani, M. Saranya, T. Sivashankari, N. Meenakshi, R. Meena, S. Rajaram, Design Of Static Random Access Memory Using QCA Technology Third International Conference on Devices, Circuits and Systems (ICDCS'16), 3rd – 5th, Mar 2015
31. D. Gracia Nirmala Rani, C. Mathumitha, R. Priyadarshini and S. Rajaram, Design and Implementation Of Configurable Logic Block Of An Fpga Using Quantum Dot Cellular Automate, Third International Conference on Devices, Circuits and Systems (ICDCS'16), 3rd – 5th, Mar 2015
32. Chinna thambi M, Bharani dharan N, Rajaram S, 'FPGA Implementation of Low Cost Matrix Inverse Computation for MIMO Systems', IEEE Sponsored 2nd International Conference on Innovations in Information, Embedded and Communication systems (ICIIECS) 2015, 19th and 20th March, 2015
33. Bharani dharan N, Chinna thambi M, Rajaram S, 'Low Complexity Arithmetic Mean Decomposition Based Pre-coding for MIMO Systems and its FPGA Implementation', IEEE Sponsored 2nd International Conference on Innovations in Information, Embedded and Communication systems (ICIIECS) 2015, 19th and 20th March, 2015
BOOKS
1. "Big Data in Medical Image Processing", CRC Press, Taylor & Francis Group, 2018, ISBN :9781138557246.
2. "Linear Integrated Circuits and its Applications-TEXAS INSTRUMENTS, 2017(SSQU016).
3. Bookchapter " Applying Big Data Analytics in Bioinformatics and Medicine", IGI Global Publishers, 2017(011216-120742).
4. "Handbook of Research on Advanced Data Mining Techniques and Applications for Business Intelligence", IGI Global Publishers, 2016