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List of Publications:

Last 5 years

1. Design of artificial neuron network with synapse utilizing hybrid CMOS transistors with memristor for low power applications
   Keerthy Rai, V., Sakthivel, R.Journal of Circuits, Systems and Computers, 2020, 29(12), 2050187

2. An efficient hardware implementation of the elliptic curve cryptographic processor over prime field, Fp
   Kudithi, T., Sakthivel, R.International Journal of Circuit Theory and Applications, 2020, 48(8), pp. 1256-1273

3. Low power area optimized and high speed carry select adder using optimized half sum and carry generation unit for FIR filter
   Sakthivel, R., Ragunath, G.Journal of Ambient Intelligence and Humanized Computing, 2020

4. Superior Implementation of Accelerated QR Decomposition for Ultrasound Imaging
   Sreejeesh, S.G., Sakthivel, R., Kidav, J.U.IEEE Access, 2020, 8, pp. 156244-156260, 9170633

5. Nonlinear System Modelling Using Programmable Hardware for Soft Computing Applications Vanitha, M., Sakthivel, R., Mangayarkarasi, R., Sharma, S.Advances in Intelligent Systems and Computing, 2020, 1057, pp. 293-306

6. Long-lifetime and low latency data aggregation scheduling for wireless sensor network
   Gayathri, E., Vanitha, M., Mangayarkarasi, R., Sakthivel, R.Journal of Testing and Evaluation, 2019, 47(6), JTE20180511

7. Ultra-low-voltage GDI-based hybrid full adder design for area and energy-efficient computing systems Sanapala, K., Sakthivel, R.IET Circuits, Devices and Systems, 2019, 13(4), pp. 558-564