

Cellphone : 9444412789

e-Mail ID : psv@annauniv.edu

Address : L - 9/6 - Valluvar Block Second Floor, Anna  
University Staff Quarters, Anna University Campus,  
Gandhi Mandapam Road, Chennai - 600 025.



## Present Position

Professor, Department of Electronics and Communication Engineering, College of Engineering Guindy, Anna University, Chennai from April-2014.

## Area of Specialisation

- Computer Science and Engineering
- VLSI Systems and Electronic Design Automation
- VLSI Algorithms
- Data Structures, Design and Analysis of Algorithms
- Modeling and Simulation
- Computer Networks

## Papers Published in Journals

Research Papers Published in International Journals	59
Research Papers Published in National Journals	13

1. D. Sridharan, P. Sakthivel and S. K. Srivatsa, "Intelligent Document Categorization for Information Retrieval based on Inter-connected Neurons", Iranian International Journal of Science, published by Iranian Academy of Science. Vol. 4, Issue 1, pp. 87-96 (2003).
2. P. Sakthivel and P. Narayanasamy, " Genetic Algorithm Based Design and Optimization of SOC Test Solutions", WSEAS Transactions on Circuits and Systems, published by WSEAS. Vol. 5, Issue 8, pp. 1212-1219 (2006).
3. P. Sakthivel and P. Narayanasamy, "Test Design and Optimization for Multiple Core Systems-On-a-Chip Using Genetic Algorithm", International Journal of Computer Science and Network Security, published by IJCSNS. Vol. 6, Issue 10, pp. 121-129 (2006).
4. P. Sakthivel and P. Narayanasamy, "An Approach to the Design of Optimal Test Scheduling for System-On-Chip Based on Genetic Algorithm", International Journal of Innovations and Advanced Techniques in Computer and Information Sciences and Engineering, published by Springer. Vol. 4, Issue 1, pp. 25-28 (2007).

5. P. Sakthivel, R. Delhi Babu and P. Narayanasamy, "Optimization of Test Scheduling and Test Access for ITC-02 SOC Benchmark Circuits", Journal of Computer Science, published by Science Publications. Vol. 5, Issue 4, pp. 290-296 (2009).
6. T Kalavathidevi, V AnishKumar, P Sakthivel, " An Efficient Low Power VLSI Architecture for Viterbi Decoder using Null Convention Logic", International Conference on VLSI, Communication & Instrumentation, ICVCI-2011 , (2011).
7. P Sakthivel, AN Kumar, "Low transition-generalized linear feedback shift register based test pattern generator architecture for built-in-self-test", Journal of Computer Science , Vol. 8, Issue 6, (2012).
8. P Sakthivel, KN Kumar, T Mayilsamy, " LOW TRANSITION TEST PATTERN GENERATOR ARCHITECTURE FOR MIXED MODE BUILT-IN-SELF-TEST (BIST)", Vol. 9, Issue 9, pp. 1396 (2012).
9. S Navaneethan, PK Sakthivel, " Design of Hard-Decision Viterbi Decoding Algorithm and VLSI Implementation", Journal of Advanced Technology in Engineering , Vol. 1, Issue 1, pp. 32-34 (2012).
10. V. Nirmalrani and P. Sakthivel, "Implementation Strategies for Multifactor Authentication for E-Governance Applicatins through Restful Webservices", International Journal of Computer Applications, published by Foundations of Computer Science, USA. Vol. 3, Issue 1, pp. 41-49 (2012).
11. P Sakthivel, A Nirmalkumar, T Mayilsamy, "Low transition test pattern generator architecture for built-in-self-test", American Journal of Applied Sciences, published by Science Publications. Vol. 9, Issue 9, pp. 1396 (2012).
12. P. Sudhakar and P. Sakthivel, "Automatic Specification Evaluator for Effective Migration", Journal of Computer Science, published by Science Publications. Vol. 8, Issue 6, pp. 822-827 (2012).

13. P. Sudhakar and P. Sakthivel, "Predicting Source Code Irregularities in Automated Code Conversion Systems using SRASG", *European Journal of Scientific Research*, published by EuroJournals Publishing. Vol. 67, Issue 3, pp. 486-491 (2012).
14. P. Sudhakar and P. Sakthivel, "Reengineering Legacy to Modern System with One Time Checker for Information System Evolution", *American Journal of Applied Sciences*, published by Science Publications. Vol. 9, Issue 6, pp. 832-841 (2012).
15. Sanjay Baisakhiya, Ramprabhu Sivasamy, Malathi Kanagasabai and Sakthivel Periyaswamy, "Novel Compact UWB Frequency Selective Surface for Angular and Polarization Independent Operation", *Progress in Electromagnetics Research Letters*, published by EMW Publishing. Vol. 40, pp. 71-79 (2013).
16. T. R. Saravanan and P. Sakthivel, "Location Based Hierarchical Key Management System for Secure Group Communications in MANET Based on Number of Receivers", *International Journal of Research in Engineering & Advanced Technology*, published by Pioneer Research & Development Group. Vol. 1, Issue 4, pp. 1-7 (2013).
17. Jayaram Kizhekke Pakkathillam, Malathi Kanagasabai, Chitra Varadhan and Periyasamy Sakthivel, "A Novel Fractal Antenna for UHF Near-Field RFID Readers", *IEEE Antennas and Wireless Propagation Letters*, published by IEEE. Vol. 12, pp. 1141-1144 (2013).
18. V. Nirmalrani and P. Sakthivel, "Protection of Resources Using Role Based Access Control with Multilevel Authentication", *International Review on Computers and Software*, published by Praise Worthy Prize. Vol. 9, Issue 11, pp. 1867-1874 (2014).
19. V. Nirmalrani and P. Sakthivel, "A Dynamic Access Control Framework for Database Security", *Journal of Computer Science and Applications*, published by International Research Publication House. Vol. 6, Issue 1, pp. 11-24 (2014).
20. Senduru Srinivasulu, P. Sakthivel and T. Amala, "Ontology Learning for Semantic Web using Lexical-Semantic Method", *International Journal of Engineering Trends and Technology*, published by Seventh Sense Research Group, USA. Vol. 9, Issue 1, pp. 25-31 (2014).
21. Senduru Srinivasulu, P. Sakthivel and E. Balamurugan, "Measuring the Ontology Level and Class Level Complexity Metrics in the Semantic Web", *International Journal of Advanced Computational Engineering and Networking*, published by IRAJ, India. Vol. 2, Issue 5, pp. 68-74 (2014).
22. V. Nirmalrani and P. Sakthivel, "Context Based Access Control Model for Protecting Pervasive Environment", *Indian Streams Research Journal*, published by Indian Streams Research Journal. Vol. 4, Issue 3, pp. 1-14 (2014).
23. Senduru Srinivasulu and P. Sakthivel, "Exploring the Full Potentials of Contemporary Semantic Web", *Indian Journal of Computer Science and Engineering*, published by Engineering Journals Publications. Vol. 4, Issue 6, pp. 494-499 (2014).
24. P. Sakthivel, "Certain investigations on low Transition test pattern generator Architecture for built in self test BIST", published by Chennai. (2014).

25. T. R. Saravanan and P. Sakthivel, "Zone Based Secure Multicast Architecture for Intra-Cluster Communication in MANET", International Review on Computers and Software, published by Praise Worthy Prize. Vol. 9, Issue 11, pp. 1834-1842 (2014).
26. Senduru Srinivasulu, P. Sakthivel, M. Arun and S. Abinaya, "The Paradigm Shift of Contemporary Semantic Web: Web3.0", Image Processing and Networking, published by Noorul Islam Center for Higher Education. Vol. 8, Issue 3, pp. 40-45 (2014).
27. S. R. Malathi and P. Sakthivel, " A New Preprocessing Method for Efficient Construction of Decision Diagrams", IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, published by IEICE Engineering Sciences Society. Issue 2, pp. 624-631 (2014).
28. S. R. Malathi and P. Sakthivel, "A New Optimization Method for Reversible Circuits using BDD", International Journal of Applied Engineering Research, published by Research India Publications. Vol. 9, Issue 21, pp. 4889-4894 (2014).
29. T. R. Saravanan and P. Sakthivel, "Statistical Analysis of Inter and Intra Cluster Communication in Location Based Hierarchical Key Management System", Indian Journal of Advances in Computer Science & Technology, published by GBS Publishers & Distributors. Vol. 3, Issue 1, pp. 1-9 (2015).
30. T. R. Saravanan and P. Sakthivel, " Impact of Attackers in Location Privacy Scheme for Secure Multicasting in MANET", Indian Journal of Engineering, published by Discovery Publication. Vol. 12, Issue 28, pp. 16-22 (2015).
31. V. Nirmalrani and P. Sakthivel, "A Hybrid Access Control Model with Multilevel Authentication and Delegation to Protect the Distributed Resources", Journal of Pure and Applied Microbiology, published by Journal of Pure and Applied Microbiology. Vol. 9, Issue 2, pp. 595-609 (2015).
32. V. Nirmalrani and P. Sakthivel, "Framework for Providing Access to Web Data Bases Using Budget Aware Role Based Access Control", Journal of Theoretical and Applied Information Technology, published by Little Lion Scientific. Vol. 76, Issue 3, pp. 296-308 (2015).
33. Senduru Srinivasulu and P. Sakthivel, "MMT: Memory Mapping Technique and NA: Neural Analysis for Semantic Web Search", Kasmera, published by Universidad del Zulia, Venezuela. Vol. 43, Issue 2, pp. 556-563 (2015).
34. P Sakthivel, R Hariharan, C Mahesh, P Prasenna, " Geographical Image Retrieval Using Spatial Filtering For Image Enhancement Techniques", International Journal of Applied Engineering Research , Vol. 10, Issue 6, pp. 15869-15881 (2015).
35. Senduru Srinivasulu and P. Sakthivel, "Contemporary Semantic Web: The Primary Social and Technical Challenges", Journal of Theoretical and Applied Information Technology, published by Little Lion Scientific. Vol. 72, Issue 3, pp. 337-346 (2015).
36. Senduru Srinivasulu and P. Sakthivel, "A Concise Method for Modeling Profiles Using Semantic Approach", Contemporary Engineering Sciences, published by Crossref, Bulgaria. Vol. 8, Issue 5, pp. 197-207 (2015).

37. Senduru Srinivasulu, P. Sakthivel and V. Ramya, "A Novel Semantic Cloud Computing Interoperability Model for Platform as a Service System", International Review on Computers and Software, published by Praise Worthy Prize. Vol. 10, Issue 4, pp. 415-423 (2015).
38. P Sakthivel, R Hariharan, " Economic Conditions of Migrant Workers Engaged in Knitwear Industry: A Study in Tiruppur District, Tamilnadu", International Journal of Management and Development Studies , Vol. 4, Issue 4, pp. 366-374 (2015).
39. S. R. Malathi and P. Sakthivel, "An Efficient Method for Computing Total Autocorrelation Over Shared Binary Decision Diagram", International Journal of Applied Engineering Research, published by Research India Publications. Vol. 10, Issue 11, pp. 29773-29782 (2015).
40. B. Gomathi and P. Sakthivel, "An Intelligent Web-Fusion using Mash up Applications Based Web-Scale Probabilistic Knowledge for Complex Operations", Advances in Natural and Applied Sciences, published by AENSI Journals. Vol. 9, Issue 6, pp. 239-243 (2015).
41. D. Sivasankar and P. Sakthivel, "A Performance Evaluation of Network on Chip Providing Quality of Service", International Journal of Applied Engineering Research, published by Research India Publications. Vol. 10, Issue 44, pp. 30923-30927 (2015).
42. S. Sridevi and P. Sakthivel, "Data Modeling & Analysis in Real-Time Process Control", Advances in Natural and Applied Sciences, published by AENSI Journals. Vol. 9, Issue 6, pp. 66-74 (2015).
43. S. Bhanumathi and P. Sakthivel, "Indian Medicinal Plants for Diabetes: Text Data Mining the Literature of Different Electronic Databases for Future Therapeutics", Biomedical Research, published by Biomedical Research. pp. 430-436 (2016).
44. T Kalavathidevi, P Sakthivel, " Design and FPGA Implementation of Bit Level Pipelined Digit Serial", Asian Journal of Information Technology , Vol. 15, Issue 7, pp. 1232-1242 (2016).
45. S. Lokesh, O. Uma Maheswari and P. Sakthivel, "Three State Skip Logic Built-In Self-Test Scheme for Combinational Circuits", Asian Journal of Information Technology, published by Medwell Journals. Vol. 15, Issue 15, pp. 2693-2698 (2016).
46. B Gomathi, P Sakthivel, " Implementing Fusion to Improve the Efficiency of Information Retrieval Using Clustering and Map Reduction", Artificial Intelligence and Evolutionary Computations in Engineering Systems, pp. 879-884 (2016).
47. A. Akilandeswari and P. Sakthivel, "Fast Architecture Multiplier Less Based DWT", Asian Journal of Information Technology, published by Medwell Journals. Vol. 15, Issue 17, pp. 3196-3204 (2016).
48. R. Benschwartz and P. Sakthivel, "A Process Variation Tolerant OTA Design for Low Power ASIC Design", Circuits and Systems, published by Scientific Research Publishing. Vol. 7, pp. 956-970 (2016).
49. K. S. Thivya, P. Sakthivel and P. M. Venkata Sai, "Analysis of Framelets for Breast Cancer Diagnosis", Technology and Health Care, published by IOS Press. Vol. 24, pp. 21-29 (2016).

50. S. Bhanumathi and P. Sakthivel, "A Review of Recent Advances in Privacy Preservation in Health Care Data Publishing", *International Journal of Pharma and Bio Sciences*, Vol. 7, Issue 4, pp. 33-41 (2016).
51. S. Bhanumathi and P. Sakthivel, "A Framework of Protein-Drug Association for Malaria by Text Data Mining of Biomedical Literature", *Research Journal of Pharmaceutical, Biological and Chemical Sciences*, Vol. 7, Issue 4, pp. 1493-1499 (2016).
52. S. Bhanumathi and P. Sakthivel, "Preservation of Private Information using Secure Multi-party Computation", *Indian Journal of Science and Technology*, Vol. 9, Issue 14, pp. 1-6 (2016).
53. B. Gomathi and P. Sakthivel, "Enhancing Information Retrieval Process Using Data Fusion by ABC Weighted Based Fuzzy Retrieval in Health Care Analytic Software", *Journal of Medical Imaging and Health Informatics*, published by American Scientific Publishers. Vol. 6, pp. 1-6 (2016).
54. KS Thivya, P Sakthivel, " Analysis of Framelets for the Microcalcification", *Proceedings of International Conference on Computer Vision and Image*, published by Springer, Singapore. pp. 11-21 (2017).
55. S. Sridevi and P. Sakthivel, "Design Considerations of Modeling and Simulation Framework and Validation of Continuous Process Training Model in a Rotary System", *Advances in Mechanical Engineering*, published by SAGE. Vol. 9, Issue 10, pp. 1-15 (2017).
56. S. Lokesh, O. Uma Maheswari and P. Sakthivel, "Test Data Compression for Digital Circuits Using Tetrad State Skip Scheme", *Design Automation of Embedded Systems*, published by Springer. Vol. 21, pp. 197-211 (2017).
57. D. Sivasankar and P. Sakthivel, "Enhancing Quality of Service in Real Time Communication Services for Network on Chip", *International Journal of Electronics Engineering Research*, Vol. 9, Issue 2, pp. 233-240 (2017).
58. S. Sridevi and P. Sakthivel, "Real-Time Simulation Design for Continuous Process Industries", *Lecture Notes in Electrical Engineering*, published by Springer. Vol. 403, Issue 7, pp. 83-98 (2017).
59. P. R. Karthikeyan, P. Sakthivel and T.S. Karthik, " Comparative Study of Illumination-Invariant Foreground Detection", *The Journal of Supercomputing*, published by Springer Nature. (2018).
60. C. Gokul, P. Sakthivel and P. R. Karthikeyan, " Firefly and Modified Ant Colony Optimization Technique for Test Scheduling and Reduction of Testing Time for Core based System-on-Chip (SoC)", *Taga Journal*, published by SWANSEA PRINTING TECHNOLOGY LTD. Vol. 14, pp. 743-751 (2018).
61. S. Uma and P. Sakthivel, "Hardware Evaluation and Software Framework Construction for Performance Measurement of Embedded Processor", *Journal of Computational and Theoretical Nanoscience*, published by American Scientific Publishers. Vol. 15, pp. 1-9 (2018).
62. B. Arunsundar, P. Sakthivel and E. Natarajan, "Analysis of Energy Consumption and Latency in Advanced Wireless Networks Through DRX Mechanism", *The Journal of Supercomputing*, published by Springer. pp. 1-23 (2018).

63. S. Skylakha, P. Sakthivel and K. S. Arunselvan, "Empirical Study on Application of Machine Learning Techniques for Resource Allocation in Health Care Using KPI", The Journal of Supercomputing, published by Springer. pp. 1-9 (2018).
64. B Gomathi, P Sakthivel, " Fusion of Hybrid Optimization Algorithm and Fuzzy set for enhancing Information Retrieval using clustering", International Journal of Advanced Research in Computer Science Special, published by I C T Academy. Vol. 9, Issue 1, pp. 104-106 (2018).
65. S. Lokesh, P. Sakthivel and O. Uma Maheswari, "Power Transition X Filling Based Elective Huffman Encoding Technique for Test-Data Compression and Scan Power Reduction for SOCs", Microprocessors and Microsystems, published by Elsevier. (2019).
66. S. Lekashri and P. Sakthivel, " Design and evaluation of dynamic partial reconfiguration using fault tolerance in asynchronous FPGA", Microprocessors and Microsystems, published by Elsevier. Vol. 71, pp. 1-11 (2019).
67. S. Lekashri and P. Sakthivel, "Lossless Image Compression for Low Memory Using Dynamic Partial Reconfiguration in Asynchronous FPGA", Journal of Advanced Research in Dynamical & Control Systems, Vol. 11, Issue 2, pp. 298-307 (2019).
68. P. R. Karthikeyan and P. Sakthivel, "Entropy Based Illumination-Invariant Foreground Detection", IEICE Transactions on Information Systems, published by IEICE Information Systems Society. Issue 7, pp. 1434-1437 (2019).
69. C. Gokul, P. Sakthivel and P. R. Karthikeyan, "Minimization of Test Time in System On Chip Using Artificial Intelligence-Based Test Scheduling Techniques", Neural Computing and Applications, published by Springer. (2019).
70. C. Vijayalakshmi, P. Sakthivel and Anand Vinekar, "Automated Detection and Classification of Telemedical Retinopathy of Prematurity Images", Telemedicine and e-Health, published by MARY ANN LIEBERT, INC. . pp. 1-5 (2019).
71. C. Gokul, P. Sakthivel and P. R. Karthikeyan, "Test Scheduling for System On Chip Using Modified Firefy and Modified ABC Algorithms", SN Applied Sciences, published by Springer Nature. (2019).
72. K. N. Madhusudhan and P. Sakthivel, "Combining Digital Signature with Local Binary Pattern-Least Significant Bit Steganography Techniques for Securing Medical Images", Journal of Medical Imaging and Health Informatics, published by American Scientific Publishers. Vol. 10, pp. 1288-1293 (2020).

## Papers Presented in Programmes

Research Papers Presented in International Programmes	26
Research Papers Presented in National Programmes	15

1. V. G. Chandramohan and P. Sakthivel, "Computer-Assisted Language Learning Problems and Prospects" presented in a International level conference on Educational Computing, organised by National Institute of Technical Teachers Training and Research, Chandigarh, India from 19-Mar-1996 to 21-Mar-1996.

2. V. G. Chandramohan and P. Sakthivel, "Action! As a Multimedia Developmental Tool and a Survival Business Communication Presentation" presented in a International level conference on Educational Computing, organised by Rajamangala Institute of Technology, Bangkok, Thailand from 20-May-1997 to 23-May-1997.
3. P. Sakthivel and P. Narayanasamy, "VLSI Expert: An Interactive Intelligent Knowledge Based System for VLSI Design" presented in a International level conference on Cognitive Systems, organised by National Institute of Information Technology, New Delhi, India from 15-Dec-1999 to 18-Dec-1999.
4. P. Sakthivel, D. Sridharan and P. Narayanasamy, "Testing of Embedded Core Based System On a Chip : Challenges and Directions" presented in a National level seminar on Global Prosperity Through Information Technology, organised by Sriji College, Maddiralapadu, India from 24-Feb-2002.
5. D. Sridharan, P. Sakthivel and S. K. Srivatsa, "Intelligent Information Retrieval based on VSM with Relevance Feedback using Connectionist Methods" presented in a International level conference on Artificial Intelligence in Engineering and Technology, organised by Univeriti Malaysia Sabah, Kota Kinabalu, Malaysia from 17-Jun-2002 to 18-Jun-2002.
6. P. Sakthivel, D. Sridharan and P. Narayanasamy, "ATPG Algorithm for Synchronous Sequential Circuits Based on HGA and Simulation" presented in a International level conference on VLSI, organised by The University of Georgia, United States of America from 24-Jun-2002 to 27-Jun-2002.
7. D. Sridharan, P. Sakthivel and S. K. Srivatsa, "An Intelligent Accelerator Mechanism for Cooperative Internet Caching Systems" presented in a International level conference on Internet Computing, organised by The University of Georgia, United States of America from 24-Jun-2002 to 27-Jun-2002.
8. T. Jayakar Charles Tobin, K. Satish and P. Sakthivel, "Design for Random Pattern Testability of Asynchronous Circuits" presented in a International level workshop on VLSI Design and Test, organised by Indian Institute of Science, Bangalore, India from 29-Aug-2002 to 31-Aug-2002.
9. D. Sridharan, P. Sakthivel and S. K. Srivatsa, "Intelligent Document Classification for Web Document Retrieval" presented in a International level conference on Knowledge-Based Intelligent Information Engineering Systems and Allied Technologies, organised by University of Milan, Crema, Milan, Italy from 16-Sep-2002 to 18-Sep-2002.
10. D. Sridharan, P. Sakthivel and S. K. Srivatsa, "An Intelligent Approach for Web Information Management Based on Clustering of Users and URL References" presented in a International level conference on LabAutomation, organised by Association for Laboratory Automation, Palm Springs, California, United States of America from 02-Feb-2003 to 05-Feb-2003.
11. P. Sakthivel, D. Sridharan and P. Narayanasamy, "Design of Biomimetic Sensor for Neurochemical Effects using VLSI Circuits" presented in a International level conference on smallTalk, organised by Association for Laboratory Automation, San Jose, California, United States of America from 13-Jul-2003 to 16-Jul-2003.



12. J. Bharathi and P. Sakthivel, "Design of High Performance Synchronous Digital Circuits for Evolvable Hardware (EHW) Application Using Genetic Algorithm" presented in a National level conference on Signals, Systems and Communication, organised by Anna University, Chennai - 600 025, India from 01-Jun-2005 to 02-Jun-2005.
13. P. Sakthivel, "Optimization of Test Vector using Genetic Algorithm for Digital Circuits and System On Chips" presented in a National level conference on Signals, Systems and Communication, organised by Anna University, Chennai - 600 025, India from 01-Jun-2006 to 02-Jun-2006.
14. P. Sakthivel and P. Narayanasamy, "Test Scheduling Optimization for Globally Asynchronous Locally Synchronous System-On-Chip Using Genetic Algorithm" presented in a International level conference on WSEAS International Conference on Circuits, organised by WSEAS, Athens, Greece from 10-Jul-2006 to 12-Jul-2006.
15. P. Sakthivel and P. Narayanasamy, "An Approach to the Design of Optimal Test Scheduling for System-On-Chip Based on Genetic Algorithm" presented in a International level conference on Industrial Electronics, Technology and Automation, organised by University of Bridgeport, Bridgeport, United States of America from 13-Nov-2006 to 15-Nov-2006.
16. P. Sakthivel and P. Narayanasamy, "Test Design and Optimization for Multiple Core Systems-On-a-Chip Using Genetic Algorithm" presented in a National level seminar on Advances in Soft Computing, organised by Institution of Engineers (India), Orissa State Centre, Bhubaneswar, India from 10-Feb-2007 to 11-Feb-2007.
17. S. Sridevi and P. Sakthivel, "Real-Time Simulation Design for Continuous Process Industries" presented in a International level conference on Nano-electronics, Circuits and Communication Systems, organised by Velammal Engineering College, Chennai, India from 01-Jun-2007 to 03-Jun-2007.
18. P. Sakthivel and P. Narayanasamy, "Design and Optimization of Test Scheduling for Core-based System-On-Chip Benchmark Circuits Using Genetic Algorithm" presented in a International level conference on Computer Science and Engineering, World Congress on Engineering, organised by Imperial College of Science and Technology, London, United Kingdom from 02-Jul-2007 to 04-Jul-2007.
19. Senduru Srinivasulu and P. Sakthivel, "Extracting Spatial Semantics in Association Rules for Weather Forecasting Image" presented in a International level conference on Trendz in Information Sciences and Computing, organised by Sathyabama University, Chennai, India from 17-Dec-2010 to 19-Dec-2010.
20. V. Nirmalrani and P. Sakthivel, "Delegation and Revocation Using Role Based Access Control" presented in a National level conference on Computing Concepts in Current Trends, organised by Sathyabama University, Chennai, India from 11-Aug-2011 to 12-Aug-2011.
21. S. Bhanumathi and P. Sakthivel, "A Comprehensive Survey on Classification of Privacy Preserving Data Mining Approaches" presented in a National level conference on Recent Trends in Data Mining and Distributed Systems, organised by Sathyabama University, Chennai, India from 15-Sep-2011 to 16-Sep-2011.

22. S. Bhanumathi and P. Sakthivel, "Available Protocols for Multi-party Computation during Privacy Preserving Data Mining" presented in a International level conference on Communication and Computer Engineering, organised by M.G.R University, Chennai, India from 12-Apr-2012 to 13-Apr-2012.
23. V. Nirmalrani and P. Sakthivel, "Design and Implementation of A-RBAC Model for Services in Distributed SOA" presented in a National level conference on Emerging Trends in Information and Communication Technologies, organised by SRM University, Chennai, India from 13-Sep-2012 to 14-Sep-2012.
24. S. Bhanumathi and P. Sakthivel, "Privacy Preserving Multiparty Collaborative Mining using Integer Programming Model" presented in a National level conference on Recent Trends in Computer and Networking Technologies, organised by BHAVINI, Kalpakkam, India from 21-Dec-2012 to 23-Dec-2012.
25. S. Bhanumathi and P. Sakthivel, "A New Model for Privacy Preserving Multiparty Collaborative Data Mining" presented in a International level conference on Circuit, Power and Computing Technologies, organised by Noorul Islam University, Nagercoil, India from 21-Mar-2013 to 22-Mar-2013.
26. V. Nirmalrani and P. Sakthivel, "Implementation Strategies for Biometric Fingerprint Authentication for E-Governance Application" presented in a International level conference on Computer Networks & Information Technology, organised by International Innovative Scientific & Research Organisation, Thailand from 29-Jun-2013 to 30-Jun-2013.
27. V. Nirmalrani and P. Sakthivel, "An Efficient Access Control Framework to Secure Web Services using Dynamic Duty Rules" presented in a International level conference on Human Computer Interactions, organised by Saveetha University, Chennai, India from 23-Aug-2013 to 24-Aug-2013.
28. Senduru Srinivasulu, P. Sakthivel, M. Arun and M. Anandan, "Enhanced Framework of Semantic Web Model" presented in a National level conference on Recent Trends in Mathematical Computing, organised by VIT University, Chennai Campus, Chennai, India from 23-Aug-2013 to 24-Aug-2013.
29. S. Bhanumathi and P. Sakthivel, "Preserving Sensitive Data in Multiparty Collaborative Mining" presented in a National level conference on Recent Trends in Mathematical Computing, organised by VIT University, Chennai Campus, Chennai, India from 23-Aug-2013 to 24-Aug-2013.
30. Senduru Srinivasulu, P. Sakthivel and Thendral Senthil, "A Syntax and Semantic Approach for Data Conversion using Soundex Algorithm" presented in a National level conference on Computing Concepts in Current Trends, organised by Sathyabama University, Chennai, India from 29-Aug-2013 to 29-Aug-2013.
31. T. R. Saravanan and P. Sakthivel, "Nodes Mobility based Secure Group Communications in Mobile Adhoc Networks using Location based Hierarchical Key Management System" presented in a International level conference on Mathematical Computer Engineering, organised by Vellore Institute of Technology, Chennai Campus, India from 29-Nov-2013 to 30-Nov-2013.

32. Senduru Srinivasulu, P. Sakthivel and S. Abinaya, "A Novel Approach of Semantic Web Model for Heterogeneous Environments" presented in a International level conference on Mathematical Computer Engineering, organised by VIT University, Chennai Campus, Chennai, India from 29-Nov-2013 to 30-Nov-2013.
33. Senduru Srinivasulu, P. Sakthivel, M. Arun and S. Abinaya, "A Concise Method to Choose Appropriate Ontology in the Context of Problem Solving" presented in a International level conference on Advanced Computing, organised by Madras Institute of Technology, Chennai, India from 18-Dec-2013 to 20-Apr-2013.
34. T. R. Saravanan and P. Sakthivel, "Secure Multicast Architecture for Intra-cluster Communication in MANET" presented in a National level conference on Current Academic Revolution in Communication & Networking, organised by Sathyabama Institute of Science and Technology, Chennai, India from 06-Mar-2014 to 07-Mar-2014.
35. V. Nirmalrani, P. Sakthivel and A. Uma, "A Privacy Model for Protection of Personal Health Records in Cloud Computing using Attribute Based Encryption" presented in a National level conference on Recent Advances in Computing and Communications, organised by Computer Society of India, Chennai, India from 21-Mar-2014 to 22-Mar-2014.
36. Senduru Srinivasulu, P. Sakthivel and Annie Jones, "An Enhanced Semantic Web Data Model for Heterogeneous Data Store" presented in a National level conference on Recent Advances in Computing and Communications, organised by Computer Society of India, Chennai, india from 21-Mar-2014 to 22-Mar-2014.
37. T. R. Saravanan and P. Sakthivel, "Location Privacy Protection for Secure Multicasting in MANET" presented in a National level conference on Recent Advances in Electronics & Computer Engineering, organised by Indian Institute of Technology Roorkee, Roorkee, India from 13-Feb-2015 to 15-Feb-2015.
38. T. R. Saravanan and P. Sakthivel, "Effectual Locality Privacy Guard for Secure Multicasting in MANET for Speediest Nodes" presented in a International level conference on Computational Intelligence, organised by Anna University, Trichy, India from 07-Apr-2015 to 08-Apr-2015.
39. B. Gomathi and P. Sakthivel, "Implementing Fusion to Improve the Efficiency of Information Retrieval Using Clustering and Map Reduction" presented in a International level conference on Artificial Intelligence and Evolutionary Computations in Engineering Systems, organised by Velammal Engineering College, Chennai, India from 22-Apr-2015 to 23-Apr-2015.
40. K. S. Thivya and P. Sakthivel, "Analysis of Framelets for the Microcalcification" presented in a International level conference on Computer Vision and Image Processing, organised by Indian Institute of Technology Roorkee, Roorkee, India from 09-Sep-2017 to 12-Sep-2017.
41. S. Lekashri and P. Sakthivel, "FPTA Based Active Reconfigurable System with Configurable Margin Examine Circuit - Applications" presented in a International level conference on Recent Trends in Computing, Communication and Networking Technologies, organised by Kings Engineering College, Chennai, India from 18-Oct-2019 to 19-Oct-2019.

## Sponsored Projects Completed

1. "Fund for Improvement of S&T Infrastructure in Universities and other Higher Educational Institutions (FIST)", funded by Department of Science and Technology, Government of India (April-2010 - March-2015). Project Cost: 15000000.00.
2. "Optimum Design of Reflector-Array Antennas for Mobile Satellite Station", funded by Department of Science and Technology, Government of India (January-2012 - January-2014). Project Cost: 608000.00.

### Programme Chaired

1. Chairman, International level conference titled "Advances in Computing, Communications and Informatics" conducted by R.M.K. Engineering College, Chennai, India from 03-Aug-2012 to 05-Aug-2012.

### Programme Organized

1. Coordinator, National level Short Course on "AICTE QIP Short Term Course on Network Programming, Security and Management" from 02-Jun-2003 to 14-Jun-2003.
2. Coordinator, National level Short Course on "Testing of VLSI Circuits" from 02-Dec-2008 to 08-Dec-2008.
3. Coordinator, National level workshop on "Digital Custom IC Design using CADENCE EDA Tools" from 12-Mar-2015 to 14-Mar-2015.
4. Organizing Committee Member, International level conference on "Urban Data Science" from 20-Jan-2020 to 21-Jan-2020.

### Programme Attended

1. Attended a National level Short Course on "ISTE Winter School on Executive Qualities for Industrial/Institutional Management" organized by Sathyabama Engineering College, Chennai, India from 22-Nov-1994 to 28-Nov-1994.
2. Attended a National level Short Course on "ISTE Winter School on Artificial Intelligence and Robotics" organized by Anna University, Chennai, India from 19-Dec-1994 to 31-Dec-1994.
3. Attended a National level Short Course on "AICTE Induction Training Programme" organized by Aligarh Muslim University, Aligarh, India from 06-May-1996 to 25-May-1996.
4. Attended a National level Short Course on "Instruction Enhancement Programme on Advanced Topics in Database Systems" organized by Indian Institute of Technology Bombay, Mumbai, India from 22-Jun-1998 to 26-Jun-1998.
5. Attended a National level Short Course on "Industry Oriented Software Engineering" organized by Anna University, Chennai, India from 22-Oct-1998 to 24-Oct-1998.
6. Attended a National level Short Course on "Instruction Enhancement Programme on Object Oriented Analysis and Design" organized by Jadavpur University, Kolkata, India from 07-Dec-1998 to 18-Dec-1998.

7. Attended a National level Short Course on "QIP Short Term Course on Internet and Web Page Design" organized by Anna University, Chennai, India from 31-May-1999 to 11-Jun-1999.
8. Attended a National level Short Course on "AICTE-ISTE Summer School Hands-on Course on VLSI Design" organized by Sri Jayachamarajendra College of Engineering, Mysore, India from 16-Aug-1999 to 28-Aug-1999.
9. Participated in a International level conference on "Cognitive Systems" organized by NIIT, New Delhi, India from 15-Dec-1999 to 18-Dec-1999.
10. Attended a National level Short Course on "Digital VLSI Designing Using VHDL" organized by Tamil Nadu Institute of Information Technology, Chennai, India from 03-Jul-2000 to 07-Jul-2000.
11. Participated in a National level workshop on "VLSI Design and Test Workshop" organized by Indian Institute of Technology Delhi, New Delhi, India from 25-Aug-2000 to 27-Aug-2000.
12. Participated in a National level workshop on "VLSI Design and Test Workshop" organized by Indian Institute of Science, Bangalore, India from 16-Aug-2001 to 18-Aug-2001.
13. Attended a National level seminar on "Global Prosperity Through Information Technology" organized by Sriji College, Maddiralapadu, Ongole, India from 24-Feb-2002 to 25-Feb-2002.
14. Participated in a International level conference on "VLSI" organized by The University of Georgia, United States of America from 24-Jun-2002 to 27-Jun-2002.
15. Participated in a International level workshop on "VLSI Design and Test Workshop" organized by Indian Institute of Science, Bangalore, India from 29-Aug-2002 to 31-Aug-2002.
16. Attended a International level Short Course on "ICTP Microprocessor Laboratory Asian Course on Advanced VLSI Design Techniques using a Hardware Description Language" organized by Ateneo de Manila University, Philippines from 25-Nov-2002 to 13-Dec-2002.
17. Participated in a International level conference on "smallTalk2003" organized by Association for Laboratory Automation, United States of America from 13-Jul-2003 to 16-Jul-2003.
18. Attended a International level Short Course on "School on Digital Radio Communications for Research and Training in Developing Countries" organized by The Abdus Salam International Centre for Theoretical Physics, Italy from 09-Feb-2004 to 27-Feb-2004.
19. Attended a National level Short Course on "ISTE-Short Term Training Programme on Numerical Linear Algebra" organized by Indian Institute of Technology Madras, India from 29-Nov-2004 to 10-Dec-2004.
20. Participated in a International level conference on "VLSI Design" organized by Indian Statistical Institute, Kolkata, India from 03-Jan-2005 to 07-Jan-2005.
21. Attended a International level Short Course on "CIMPA-UNESCO-INDIA School" organized by Indian Institute of Science, Bangalore, India from 25-Jan-2005 to 04-Feb-2005.
22. Attended a National level Short Course on "Ubiquitous Computing" organized by Indian Institute of Science, Bangalore, India from 07-Mar-2005 to 11-Mar-2005.

23. Participated in a National level workshop on "Challenges in VLSI" organized by Dhirubhai Ambani Institute of Information and Communication Technology, Gandhinagar, India from 13-May-2005 to 14-May-2005.
24. Attended a National level Short Course on "QIP Short Term Training Programme on Semiconductor Manufacturing Processes" organized by Indian Institute of Technology Madras, Chennai, India from 23-May-2005 to 27-May-2005.
25. Participated in a National level conference on "Signals, Systems and Communication" organized by Anna University, Chennai, India from 01-Jun-2005 to 02-Jun-2005.
26. Attended a National level Short Course on "Design for Testability - Theory and Practice" organized by Indian Institute of Science, Bangalore, India from 27-Jul-2005 to 30-Jul-2005.
27. Participated in a National level workshop on "Research Methodology for Engineering Teachers" organized by Sri Ramakrishna Engineering College, Coimbatore, India from 03-Sep-2005 to 03-Sep-2005.
28. Attended a National level Short Course on "UGC Sponsored Refresher Course on VLSI Design and Testing" organized by Jadavpur University, Kolkata, India from 17-Oct-2005 to 05-Nov-2005.
29. Participated in a National level conference on "Signals, Systems and Communication" organized by Anna University, Chennai, India from 01-Jun-2006 to 02-Jun-2006.
30. Attended a National level Short Course on "Digital Circuit Testing and Design for Testability" organized by VLSI Society of India, Bangalore, India from 17-Aug-2006 to 19-Aug-2006.
31. Participated in a National level workshop on "Effective Teaching and Research Methodology for College Teachers" organized by Sri Ramakrishna Engineering College, Coimbatore, India from 23-Sep-2006 to 23-Sep-2006.
32. Attended a International level Short Course on "ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis" organized by The Abdus Salam International Centre for Theoretical Physics, Italy from 27-Nov-2006 to 22-Dec-2006.
33. Attended a National level seminar on "Advances in Soft Computing" organized by The Institution of Engineers (India) Orissa State Centre, Bhubaneswar, India from 10-Feb-2007 to 11-Feb-2007.
34. Participated in a International level conference on "World Congress on Engineering" organized by Imperial College of Science and Technology, United Kingdom from 02-Mar-2007 to 04-Jul-2007.
35. Participated in a National level workshop on "Emerging Trends in VLSI and ASIC Designs" organized by Anna University, Chennai, India from 07-Apr-2007 to 07-Apr-2007.
36. Participated in a National level workshop on "Mentoring Youth and Adolescents" organized by Anna University, Chennai, India from 16-May-2007 to 16-May-2007.
37. Participated in a National level workshop on "Partial Reconfiguration" organized by Indian Institute of Technology Madras, Chennai, India from 03-Jan-2008 to 04-Jan-2008.

38. Attended a National level Short Course on "Second Indian Winter School on Logic" organized by Indian Institute of Technology, Kanpur, India from 14-Jan-2008 to 26-Jan-2008.
39. Participated in a National level workshop on "Design Verification Methodologies" organized by VLSI Society of India, Bangalore, India from 25-Apr-2008 to 26-Apr-2008.
40. Participated in a National level workshop on "FPGA Based Embedded Processor Overview - NIOS II" organized by Anna University, Chennai, India from 07-May-2008 to 07-May-2008.
41. Attended a National level Short Course on "AICTE-ISTE STTP on Formal Languages and Model Based Computing" organized by R.M.D. Engineering College, Kavaraipettai, India from 17-Jun-2008 to 21-Jun-2008.
42. Participated in a National level workshop on "Embedded System Design" organized by Anna University, Chennai, India from 11-Jul-2008 to 12-Jul-2008.
43. Participated in a National level workshop on "Embedded Systems" organized by Anna University, Chennai, India from 20-Feb-2009 to 20-Feb-2009.
44. Participated in a National level workshop on "Design Automation Algorithms for FPGAs" organized by VLSI Society of India, Bangalore, India from 31-Mar-2009 to 31-Mar-2009.
45. Attended a National level Short Course on "MHRD-AICTE Faculty Development Programme on Analog IC Design" organized by National Institute of Technology Calicut, Kozhikode, India from 01-Jun-2009 to 12-Jun-2009.
46. Attended a National level Short Course on "NCC Pre-Commission Course" organized by National Cadet Corps, Seamanship School, Naval Base, Kochi, India from 10-Aug-2009 to 10-Oct-2009.
47. Participated in a National level workshop on "Research Promotion Workshop on Introduction to Graph and Geometric Algorithms" organized by National Institute of Technology Tiruchirappalli, India from 07-Jan-2010 to 09-Jan-2010.
48. Attended a National level Short Course on "AICTE-STTP on Wireless Ad-Hoc and Sensor Networks" organized by Indian Institute of Technology Kharagpur, Kharagpur, India from 02-Feb-2010 to 15-Feb-2010.
49. Participated in a International level conference on "IFIP-Networking-2010" organized by Indian Institute of Technology Madras, Chennai, India from 10-May-2010 to 14-May-2010.
50. Attended a National level Short Course on "NCC Naval Technical Attachment Camp" organized by INS Valsura, Jamnagar, Gujarat & Naval Dockyard, Visakhapatnam, Andhra Pradesh, India from 23-May-2010 to 08-Jun-2010.
51. Participated in a National level workshop on "National Programme on Technology Enhanced Learning" organized by Indian Institute of Technology Madras, Chennai, India from 03-Sep-2010 to 03-Sep-2010.
52. Participated in a National level workshop on "Cryptography" organized by Computer Society of India, Chennai, India from 08-Oct-2010 to 09-Oct-2010.

53. Participated in a National level workshop on "Database Management Systems" organized by Indian Institute of Technology Bombay, Mumbai, India from 13-Dec-2010 to 23-Dec-2010.
54. Attended a National level Short Course on "NCC Combined Annual Training Camp" organized by Jeppiaar Engineering College, Chennai, India from 22-Dec-2010 to 31-Dec-2010.
55. Participated in a International level conference on "An Overview of Modern-day VLSI Design and Test Practices (International Conference on VLSI Design)" organized by Indian Institute of Technology Madras, Chennai, India from 02-Jan-2011 to 07-Jan-2011.
56. Participated in a International level workshop on "Biomedical Sciences and Technologies" organized by Anna University, Chennai, India from 02-Mar-2011 to 04-Mar-2011.
57. Participated in a National level workshop on "Research Methodologies" organized by Kumaraguru College of Technology, Coimbatore, India from 15-Apr-2011 to 16-Apr-2011.
58. Participated in a National level workshop on "Low Power Embedded Systems Using MSP430 Microcontroller" organized by Anna University, Chennai, India from 19-Apr-2011 to 20-Apr-2011.
59. Participated in a National level workshop on "Basic Electronics" organized by Indian Institute of Technology Bombay, Mumbai, India from 28-Jun-2011 to 08-Jul-2011.
60. Attended a National level seminar on "Formal Methods for Specification and Verification" organized by Chennai Mathematical Institute, Chennai, India from 09-Jul-2011 to 09-Jul-2011.
61. Participated in a National level workshop on "Research and Writing" organized by Anna University, Chennai, India from 14-Sep-2011 to 15-Sep-2011.
62. Participated in a National level conference on "Architecture, Programming & Applications of Stellaris Microcontroller" organized by Texas Instruments India, Bangalore, India from 19-Nov-2011 to 20-Nov-2011.
63. Attended a National level seminar on "Recent Issues in Computer Science" organized by R.M.D. Engineering College, Kavaraipettai, India from 22-Nov-2011 to 22-Nov-2011.
64. Participated in a National level conference on "CSI Annual National Convention" organized by Computer Society of India, Ahmedabad, India from 01-Dec-2011 to 03-Dec-2011.
65. Attended a National level Short Course on "NCC Sea Training Camp" organized by Mumbai to Visakhapatnam via Arabian Sea, Indian Ocean and Bay of Bengal - INS Gharial, India from 22-Dec-2011 to 04-Jan-2012.
66. Participated in a National level workshop on "Renesas 16 Bit RL78 Microcontrollers" organized by Anna University, Chennai, India from 31-Jan-2012 to 31-Jan-2012.
67. Attended a National level seminar on "Issues in Preparing Research Thesis and Project Proposal" organized by Anna University, Chennai, India from 16-Mar-2012 to 17-Mar-2012.
68. Participated in a National level workshop on "How to Do a Good Ph.D Thesis" organized by Anna University, Chennai, India from 10-Apr-2012 to 12-Apr-2012.



69. Participated in a National level workshop on "Adding WIFI Capability to Microcontroller-based Sensor and Control Applications" organized by Anna University, Chennai, India from 03-May-2012 to 03-May-2012.
70. Attended a National level Short Course on "NCC Naval Technical Attachment Camp" organized by INS Shivaji, Lonavla, Maharashtra & Naval Dockyard, Visakhapatnam, Andhra Pradesh, India from 30-May-2012 to 13-Jun-2012.
71. Participated in a National level workshop on "Planning and Preparing the Best PhD Thesis of an International Quality" organized by CSIR-Central Leather Research Institute, Chennai, India from 14-Jun-2012 to 16-Jun-2012.
72. Attended a National level Short Course on "SKY Yoga Course for Holistic Health" organized by The World Community Service Centre, Chennai, India from 30-Jul-2012 to 10-Aug-2012.
73. Attended a National level Short Course on "Real-Time and Embedded Systems" organized by Indian Institute of Technology Kharagpur, Kharagpur, India from 19-Sep-2012 to 25-Sep-2012.
74. Participated in a National level workshop on "Semiconductor Solutions for Wireless Communication" organized by Anna University, Chennai, India from 05-Oct-2012 to 06-Oct-2012.
75. Attended a National level Short Course on "National Cadet Corps National Integration Camp" organized by NCC, Bhuj, Gujarat, India from 03-Jan-2013 to 14-Jan-2013.
76. Participated in a National level workshop on "Multi-Criteria Decision Making" organized by Anna University, Chennai, India from 26-Mar-2013 to 27-Mar-2013.
77. Participated in a National level workshop on "Database Management Systems" organized by Indian Institute of Technology Bombay, Mumbai, India from 21-May-2013 to 31-May-2013.
78. Participated in a National level workshop on "Self-Awareness and Higher Goals in Education" organized by Indian Institute of Technology Madras, Chennai, India from 17-Jun-2013 to 21-Jun-2013.
79. Attended a National level Short Course on "AICTE FDP On Hardware Verification Techniques" organized by K C G College of Technology, Chennai, India from 25-Nov-2013 to 08-Dec-2013.
80. Attended a National level Short Course on "NCC Combined Annual Training Camp" organized by Sathyabama University, Chennai, India from 22-Dec-2013 to 31-Dec-2013.
81. Attended a National level Short Course on "Applied Game Theory for Engineers and Managers" organized by Indian Institute of Technology Kanpur, Kanpur, India from 20-Jun-2014 to 24-Jun-2014.
82. Participated in a National level workshop on "ISTE Workshop on Computer Networking" organized by Indian Institute of Technology Bombay, Mumbai, India from 30-Jun-2014 to 05-Jul-2014.
83. Participated in a National level workshop on "Author Workshop on Book/Journal Publishing" organized by Anna University, Chennai, India from 24-Sep-2014 to 24-Sep-2014.

84. Attended a National level Short Course on "QIP STC on Advances in Databases and Data Mining" organized by Anna University, Chennai, India from 16-Feb-2015 to 01-Mar-2015.
85. Attended a National level Short Course on "Algorithm and Game Theory" organized by Indian Institute of Technology Guwahati, Guwahati, India from 10-Mar-2015 to 14-Mar-2015.
86. Attended a National level Short Course on "ISTE STTP on Introduction to Design of Algorithms" organized by Indian Institute of Technology Kharagpur, Kharagpur, India from 25-May-2015 to 30-May-2015.
87. Attended a National level seminar on "Indian Engineering Dean's Council Global Summit on Engineering Education - Current Scenario & Future Prospects" organized by Indian Society for Technical Education, Chennai, India from 31-Jul-2015 to 01-Aug-2015.
88. Participated in a National level workshop on "Urkund - An Anti Plagiarism Detection Tool" organized by Anna University, Chennai, India from 22-Sep-2015 to 22-Sep-2015.
89. Participated in a National level conference on "Recent Advances in Computing and Communications" organized by Shri Shankarlal Sundarbai Shasun Jain College for Women, Chennai, India from 14-Oct-2015 to 14-Oct-2015.
90. Attended a National level Short Course on "Modeling, Simulation and Characterization of Nano-Transistors" organized by Indian Institute of Technology Kanpur, Kanpur, India from 26-Oct-2015 to 30-Oct-2015.
91. Attended a National level Short Course on "Evolving Synergy of Statistical Analysis" organized by National Institute of Technology Hamirpur, Hamirpur, India from 21-Dec-2015 to 25-Dec-2015.
92. Participated in a International level conference on "Challenges & Opportunities in Technical Education in the Era of Sustainable Development" organized by Shree Hanuman Vyayam Prasarak Mandal's College of Engineering, Amravati, India from 09-Jan-2016 to 11-Jan-2016.
93. Attended a National level Short Course on "Prototyping Tools and Testbed Creation for 5G Wireless Communication" organized by Anna University, Chennai, India from 02-Mar-2016 to 06-Mar-2016.
94. Attended a National level Short Course on "AICTE - QIP STC on Biology for Engineers" organized by Indian Institute of Technology Bombay, Mumbai, India from 06-Jun-2016 to 11-Jun-2016.
95. Participated in a National level workshop on "Automotive Embedded Systems and Communication Protocols" organized by P S G College of Technology, Coimbatore, India from 09-Jul-2016 to 09-Jul-2016.
96. Attended a National level Short Course on "AICTE-STC on ICT Solutions for Issues and Challenges in Smart Grid Technology" organized by Pondicherry Engineering College, Pondicherry, India from 28-Nov-2016 to 02-Dec-2016.
97. Attended a National level Short Course on "AICTE-STC on Deep Learning and Applications" organized by Indian Institute of Technology Kanpur, Kanpur, India from 12-Jan-2017 to 16-Jan-2017.

98. Attended a National level Short Course on "AICTE-QIP STC on Algorithmic Game Theory and Mechanism Design" organized by Indian Institute of Science, Bangalore, India from 10-Apr-2017 to 14-Apr-2017.
99. Attended a National level Short Course on "AICTE-QIP STC on Tools for Cyber Security: A Laboratory Course" organized by Indian Institute of Technology Bombay, Mumbai, India from 22-May-2017 to 26-May-2017.
100. Attended a National level Short Course on "NCC-National Integration Camp" organized by NCC, Dhanbad, Jharkhand, India from 04-Sep-2017 to 15-Sep-2017.
101. Attended a National level Short Course on "GIAN Course on Cloud Data Center Provisioning: Theoretical and Practical Approaches" organized by Jawaharlal Nehru University, New Delhi, India from 29-Jan-2018 to 09-Feb-2018.
102. Attended a National level Short Course on "NCC Refresher Course" organized by NCC Officers Training Academy, Kamptee, India from 01-Jun-2018 to 21-Jun-2018.

### Special Representations

1. International Student Coordinator in Computer Society of India from 01-Apr-2012 to 31-Mar-2014.
2. National Executive Council Member in Indian Society for Technical Education from 01-Jan-2012
3. Member, ISTE National Awards Committee in Indian Society for Technical Education from 27-Nov-2013 to 27-Nov-2013.
4. Chairman - Division 2 (Software) in Computer Society of India from 01-Apr-2020
5. Member, ISTE National Awards Committee in Indian Society for Technical Education from 13-Oct-2015 to 13-Oct-2015.
6. Vice Chairman in Computer Society of India Chennai Chapter from 01-Apr-2009 to 31-Mar-2010.
7. Tamilnadu & Puducherry State Student Coordinator in Computer Society of India from 01-Apr-2011 to 31-Mar-2012.
8. Executive Committee Member in IETE Chennai Centre from 01-Apr-2008 to 31-Mar-2012.
9. Treasurer in ACM Chennai Professional Chapter from 01-Jan-2017 to 31-Dec-2018.
10. State Committee Member in Institution of Engineers (India) Tamilnadu State Centre from 01-Oct-2012 to 30-Sep-2018.
11. Chairman in IEEE Computer Society Madras Chapter from 01-Jun-2016
12. Treasurer in IEEE Madras Section from 01-Jan-2016 to 31-Dec-2017.

13. Managing Committee Member in Computer Society of India Chennai Chapter from 01-Apr-2008 to 31-Mar-2009.
14. Chairman in Computer Society of India Chennai Chapter from 01-Apr-2010 to 31-Mar-2011.
15. Secretary in IEEE Madras Section from 01-Jan-2016 to 31-Dec-2016.

## Honours

1. "Academic Grant" given by Association for Laboratory Automation from United States of America (2003).
2. "Certificate of Appreciation" given by International Association of Engineers from United Kingdom (2007).
3. "Fellow" given by The Institution of Engineers (India) from India (2012).
4. "Certificate of Appreciation" given by Computer Society of India from India (2013).
5. "Chapter Patron Award" given by Computer Society of India from India (2013).
6. "Fellow" given by The Institution of Electronics and Telecommunication Engineers from India (2014).
7. "Certificate of Appreciation" given by Indian Society for Technical Education from India (2014).

## Experience Abroad

1. Visited Rajamangala Institute of Technology, Thailand from 18-May-1997 to 25-May-1997. Purpose of visit :To Attend International Conference on Educational Computing.
2. Visited Linkoping University, Sweden from 19-May-2001 to 27-May-2001. Purpose of visit :To Visit VLSI Laboratory.
3. Visited University of Nevada, United States of America from 23-Jun-2002 to 30-Jun-2002. Purpose of visit :To Attend International Conference on VLSI.
4. Visited Ateneo de Manila University, Philippines from 24-Nov-2002 to 14-Dec-2002. Purpose of visit :To Attend UNESCO Sponsored Training Programme on VLSI.
5. Visited Stanford University, United States of America from 12-Jul-2003 to 20-Jul-2003. Purpose of visit :To Present a Research Paper in the International Conference.
6. Visited International Centre for Theoretical Physics, Italy from 09-Feb-2004 to 27-Feb-2004. Purpose of visit :To Attend UNESCO Sponsored Training Programme on Digital Radio Communications.
7. Visited International Centre for Theoretical Physics, Italy from 27-Nov-2006 to 22-Dec-2006. Purpose of visit :To Attend UNESCO Sponsored Training Programme on FPGA.

8. Visited Imperial College of Science and Technology, United Kingdom from 01-Jul-2007 to 07-Jul-2007. Purpose of visit :To Attend International Conference on CSE.
9. Visited Hanoi University of Science and Technology, Vietnam from 13-Dec-2012 to 25-Dec-2012. Purpose of visit :Joint Project Work.
10. Visited University of Bologna, Italy from 05-Dec-2019 to 10-Dec-2019. Purpose of visit :To Attend IEEE CS Chapter Chairpersons Meeting.

### Invited Lectures

1. Delivered a Lecture on "Application of Photoshop" in QIP Short Term Course on Internet and Web Page Design organized by Anna University, Chennai (10-Jun-1999).
2. Delivered a Lecture on "Computer Programming" in Refresher Course on Water Supply Distribution Network Analysis using Computers organized by Centre for Environmental Studies, Anna University, Chennai (15-Feb-2001).
3. Delivered a Lecture on "Network Programming and Security" in AICTE QIP Short Term Course on Network Programming, Security and Management organized by Anna University, Chennai (02-Jun-2003).

### Extension & Outreach Programme

1. Coordinator, AICTE QIP Short Term Course on Network Programming, Security and Management, participated by Faculty Members of Engineering Colleges and funded by AICTE at Ramanujan Computing Centre, Anna University, Chennai - 600 025 during 02-Jun-2003 and 14-Jun-2003. No. of participants: 50.
2. Coordinator, Faculty Development Training Programme on Testing of VLSI Circuits, participated by Faculty Members of Engineering Colleges and funded by Centre for Faculty Development, Anna University, Chennai - 600 025 at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 02-Dec-2008 and 08-Dec-2008. No. of participants: 25.
3. Coordinator, International Workshop on Recent Advances in Antenna Design, participated by Faculty Members of Engineering Colleges and funded by DST-PURSE at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 20-Feb-2013 and 20-Feb-2013. No. of participants: 30.
4. Coordinator, Workshop on Digital Custom IC Design using CADENCE EDA Tools, participated by Faculty Members of Engineering Colleges and funded by Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 12-Mar-2015 and 14-Mar-2015. No. of participants: 60.
5. Coordinator, Two Week Faculty Development Programme on Foundation Programme in ICT for Education, participated by Faculty Members of Engineering Colleges and funded by Indian Institute of Technology Bombay at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 19-Aug-2017 and 20-Aug-2017. No. of participants: 50.

6. Coordinator, Two Week Faculty Development Programme on Pedagogy for Online and Blended Teaching-Learning Process, participated by Faculty Members of Engineering Colleges and funded by Indian Institute of Technology Bombay at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 07-Oct-2017 and 08-Oct-2017. No. of participants: 50.
7. Coordinator, Two Week Faculty Development Programme on Foundation Programme in ICT for Education, participated by Faculty Members of Engineering Colleges and funded by Indian Institute of Technology Bombay at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 24-Mar-2018 and 25-Mar-2018. No. of participants: 50.
8. Coordinator, Two Week Faculty Development Programme on Pedagogy for Online and Blended Teaching-Learning Process, participated by Faculty Members of Engineering Colleges and funded by Indian Institute of Technology Bombay at Department of Electronics and Communication Engineering, Anna University, Chennai - 600 025 during 26-May-2018 and 27-May-2018. No. of participants: 50.