

Dr. B. Sargunam M.E, Ph.D
 Associate Professor and Head Qualification
 Specialization : VLSI Design
 Email : sargunam_ece@avinuty.ac.in
 Contact number : 98430 19740

Teaching experience:

Level	No. of Years
PG	8 years
UG	20 years
Diploma	5 Months
Total	20 years 5 Months

Industry experience:

Name of the Industry	Position held	Nature of work done
Sri Easwari's Agencies Pvt. Ltd.	Service Engineer– Customer Support (1 year 10 months from 21.04.1995 to 21.02.1997)	Servicing of computers and accessories

Research experience excluding years spent for pursuing M.Phil/ Ph.D.

Degree(s): 4 years

PUBLICATIONS:

National and International Journals:

1. B.Sargunam and Dr.R.Dhanasekaran , “Error detection Schemes for finite field multipliers” American Journal of Applied Sciences, Vol. 11, no. 1, pp. 137-144, 2014
2. B.Sargunam and Dr.R.Dhanasekaran,” Fault tolerant techniques for finite field multipliers”, Applied Mechanics and Materials, Vol. 573, pp. 209-214, 2014
3. B.Sargunam and Dr.R.Dhanasekaran, “Word level finite field multipliers using normal basis”, Journal of Theoretical and Applied Information Technology, Vol. 62, no.3, pp. 805-811, 2014.
4. B.Sargunam, P.Priya and Dr.R.Dhanasekaran, “Efficient word based Montgomery multiplication for cryptography”, International Journal of Electrical & Electronics Engineering Advanced Research, Vol. 2, SP-1, pp. 186-189, 2014

5. B.Sargunam, J.Mary Kanimozhi and Dr.R.Dhanasekaran,” Graph-based low complexity bit parallel multipliers for trinomials over Galois field”, International Journal of Electronic Communication Engineering Advanced Research, Vol. 2, SP-1, pp. 117-120, 2014.
6. B.Sargunam and Dr.R.Dhanasekaran,” Error Detection for Graph Based Finite Field Multiplier Using Swapping Technique”, International Journal of Applied Engineering Research, Vol.9, no.23, pp. 19037-19046, 2014
7. B.Sargunam and S.Srinitha,” Area Efficient Sorting Unit using Scalable Digital CMOS Comparator”, International Journal of Engineering Trends and Technology, Vol.9, no.13, pp. 677-682, 2014.
8. Niviya.S, Poorani.A, Induja.R and B.Sargunam, “Error Control Methods for All-One Polynomial Based Galois Field Multiplier”, International Journal of Engineering Research & Technology, Vol. 3, Issue 4, pp. 928-931, 2014.
- 9.. B.Sargunam and S.Srinitha,” FPGA Implementation of Fused Add-Multiply Unit Using Efficient Adder Topologies “,International Journal of Applied Engineering and Research, Vol. 10,No. 6 ,pp. 14235-14246, 2015.
10. R.Miruthu Bashini, K.Thaijalnayaki, S.Priyadharshini, A.Swethapushpa and Dr.B.Sargunam,” Wireless Patient Monitoring System with Electronic Notice Board”, International Journal for Trends In Engineering & Technology, Vol. 14, Issue 1, pp. 95-98, 2016.
11. Dr. B. Sargunam, S. Anusha,” Real Time Tracking System using Bluetooth”, Journal of Applied Science and Computations, Volume VI, Issue V, pp.3847-3850, 2019.
12. S. Gousalya, K. Jayasurya, M. Kiruthika, Dr.B.Sargunam , “Voice Based Notice Board using Android Application”, Journal of Applied Science and Computations, Volume VI, Issue V, pp.3877-3879,
13. N.Kirthika, Dr.B.Sargunam,” Detecting Multi-Class Artifacts in Endoscopic Images using YOLOv3”, International Journal of Recent Technology and Engineering (IJRTE), Volume-9, Issue-1, pp.2578-2583, May 2020
- 14.. Meenakshi Subramanian, Dr.B.Sargunam, Akalya.A, Deshika.P,” Fifth Gen Control for Industrial Wet Grinders”, International Journal of Engineering Research & Technology (IJERT), Vol. 9 Issue 08, pp.585-589 August-2020

Book Published:,

1. “Finite Field Multipliers and Error Detection Methods”, Lambert Publication, March 2018, 978-613-7-32727-2

International Conferences:

1. "High Speed Bit-Parallel Systolic Multiplier over $GF(2^m)$ for Cryptographic Application", **IEEE** International conference on advanced communication control and computing technologies, 2012.
2. "Efficient Bit-Parallel Systolic Multiplier for Special Class of $GF(2^m)$ ", International conference on electrical, electronics and computer engineering, 2012.
3. "Efficient word based Montgomery multiplication for cryptography", International conference on advanced communication control and computing technologies, 2014.
4. "Fault tolerant techniques for finite field multipliers", International conference on Advancements in Automation and Control, 2014.
5. "Duplication Based Error Detection for Montgomery Multiplier Over Galois Field", **IEEE** International conference on advanced communication control and computing technologies, 2014.
6. "Area Effective and Speed Optimized Fused Add-Multiply Unit", **IEEE** Sponsored 2nd International Conference on Innovations in Information Embedded and Communication Systems (ICIIECS'15), 2015.
7. "LPG monitoring and Security System", International Conference on Trends in Engineering & Technology, 2016.
8. "Wireless Patient Monitoring System with electronic Notice Board", International Conference on Trends in Engineering & Technology, 2016.
9. "Domestic LPG level Monitoring and Safety System", International Conference on Latest Trends in Science, Engineering and Technology, 2018.
10. "IoT Based Air Compressor Monitor System", International Conference on Latest Trends in Science, Engineering and Technology, 2018.
11. "IOT Based Mobile Medical Application for Smart Insulin Regulation", **IEEE** International Conference on Electrical, Computer & Communication Technologies (IEEE ICECCT 2019), 2019.
12. "A Survey on Low Power Multiplier Design using Efficient Full Adder", International Conference on Emerging Trends in Wireless Communications, Signal Processing and Networking, (ICETWSN'19), 2019.

National Conferences:

1. "FPGA Implementation & Comparison of LUT & PTB based Embedded PLC Architecture", Communication Control and Computing Technologies-CCST '09, Nehru Institute of Engineering and Technology, 2009.
2. "On Chip Single Error Correcting Finite field Multiplier", National Conference on Intelligent Computing-NCIC, Faculty of Engineering, Avinashilingam University for Women, 2010.
3. "Improving the Floating Point Performance in FPGA by Architectural Modifications", Recent Trends in Electrical and Computer Technology-RTECT '10, Syed Ammal Engineering College, 2010.
4. "Implementation and Testing of Single Error Correcting Multiplier over GF (2^m)", Recent Trends in Electrical and Computer Technology-RTECT '10, Syed Ammal Engineering College, 2010.
5. "Improved Fault Detection Schemes for Advanced Encryption Standard Using Finite Fields", Emerging Trends in Instrumentation Systems, Sri Ramakrishna Engineering College, 2012.
6. "Wireless Monitoring System for Alzheimer's Patients using Electronic Notice Board", Challenging Issues and Technological Approaches in Medicine, Faculty of Engineering, Avinashilingam University, 2016.
7. "Brain Computer Interface to Control Home Appliances", Recent Innovations in Healthcare Technologies, Faculty of Engineering, Avinashilingam University, 2017.
8. "Internet of Things aided Mobile Medical Application for Monitoring Blood Glucose Level using Non-invasive Method", Recent Trends in Embedded System, Faculty of Engineering, Avinashilingam University, 2017.
9. "பாரதியாரின் நாட்டு பற்று", National Conference on Influence of Sri Subramania Bharathiyar's compositions during pre-independence, Faculty of Engineering and Faculty of Humanities, Presented Avinashilingam University, 2017.
10. "RFID Intelligent Book Conveyer", National Conference on Contemporary Dimensions in Library and Information Science, Avinashilingam University, 2017.
11. "Survey on Patient Monitoring System using IOT", ICMR sponsored national seminar on Technological Advances in Smart Healthcare Monitoring System using IOT, Department of ECE, Faculty of Engineering, Avinashilingam University, 2017.
12. "A Wireless Health Monitoring System Using Mobile Phone Accessories", ICMR sponsored national seminar on Technological Advances in Smart Healthcare Monitoring

System using IOT, Department of ECE, Faculty of Engineering, Avinashilingam University, 2017.