

Name : **Vigneswaran T**

Publications (last 5 years)

1. Ramanathan, Pandian & Vigneswaran, T. & Kumari, Lalitha. (2016). Characterization of CT cancer lung image using image compression algorithms and feature extraction. *Journal of Scientific and Industrial Research*. 75. 747-751.
2. Chitra, E. & Vigneswaran, T.. (2016). An Efficient Low Power and High Speed Distributed Arithmetic Design for FIR Filter. *Indian Journal of Science and Technology*. 9. 1-5. 10.17485/ijst/2016/v9i4/79055.
3. Raj, Joseph & Vigneswaran, T.. (2016). A paradigm of distributed arithmetic (DA) approaches for digital FIR filter. 4668-4672. 10.1109/ICEEOT.2016.7755606.
4. Ponraj, Abraham & Vigneswaran, T.. (2019). Daily evapotranspiration prediction using gradient boost regression model for irrigation planning. *The Journal of Supercomputing*. 76. 10.1007/s11227-019-02965-9.
5. Chitra, E. & Vigneswaran, T. & Subramani, Malarvizhi. (2018). Analysis and Implementation of High Performance Reconfigurable Finite Impulse Response Filter Using Distributed Arithmetic. *Wireless Personal Communications*. 102. 10.1007/s11277-018-5375-4.
6. N., Devi & T., Vigneswaran. (2016). A 128-point FFT/IFFT processor for MIMO-OFDM transceivers - A broader survey. *International Journal of Engineering and Technology*. 8. 2154-2160. 10.21817/ijet/2016/v8i5/160805435.
7. Sarada, V. & Vigneswaran, T. & Jayakumar, Selvakumar. (2019). Low-power and high-throughput 128-point feedforward FFT processor. *Cluster Computing*. 22. 10.1007/s10586-018-1918-4.
8. Jossy, Maria & Vigneswaran, T. & Subramani, Malarvizhi & Nagarajan, K. K.. (2018). Characterization and modeling of dual material double gate tunnel field effect transistor using superposition approximation method. *Concurrency and Computation: Practice and Experience*. 31. e4860. 10.1002/cpe.4860.
9. Jossy, Maria & Vigneswaran, T. & Subramani, Malarvizhi & Nagarajan, K. K.. (2018). Characterization and modeling of dual material double gate tunnel field effect transistor using superposition approximation method. *Concurrency and Computation: Practice and Experience*. 31. e4860. 10.1002/cpe.4860.

10. Kalaierasi, M. & Vigneswaran, T.. (2016). VLSI Architectures for Lifting Based Discrete Wavelet Transform – A Survey. Indonesian Journal of Electrical Engineering and Computer Science. 3. 323. 10.11591/ijeecs.v3.i2.pp323-330.
11. Nirmalraj, S. & Vigneswaran, T.. (2016). A Novel Approach to Compress an Image Using Cascaded Transform and Compressive Sensing. 10.1007/978-81-322-2671-0_70.
12. Sushmitha, R. & Nisha, O. & V, Ravi & Vigneswaran, T.. (2020). Defect Detection and Defect-Tolerant Design of a Multi-port SRAM. 10.1007/978-981-15-7245-6_39.
13. Gracin, D. & Omkar, Kichchannagari & V, Ravi & Vigneswaran, T.. (2020). Variation-Tolerant In-Memory Digital Computations Using SRAM. 10.1007/978-981-15-7245-6_38.
14. Umadevi, S. & Vigneswaran, T.. (2019). Reliability improved, high performance FIR filter design using new computation sharing multiplier: suitable for signal processing applications. Cluster Computing. 22. 10.1007/s10586-018-2067-5.
15. Radhika, P. & Vigneswaran, T. & Jayakumar, Selvakumar. (2019). Design of high performance filter bank multi-carrier transmitter. Cluster Computing. 22. 10.1007/s10586-017-1679-5.
16. Hemalatha, S. & Vigneswaran, T.. (2019). Reconfigurable communication wrapper for QoS demand for network on chip. International Journal of Advanced Intelligence Paradigms. 12. 24. 10.1504/IJAIP.2019.096949.