LIST OF PUBLICATIONS

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Associate Professor, School of Electrical Engineering (SELECT), VIT University, Vellore.

- 1. Kola Muralikumar and P. Ponnambalam "Analysis of Cascaded Multilevel Inverter with a Reduced Number of Switches for Reduction of Total Harmonic Distortion" IETE Journal of Research Taylor and Francis, 2020
- 2. Murthy Priya, PathipooranamPonnambalam , Kola Muralikumar,"Modular-multilevel converter topologies and applications a review", IET Power Electronics, vol. 12, no. 2, pp. 170-183, 20/2/2019
- 3. ChallaBabu, P Ponnambalam," The theoretical performance evaluation of hybrid PV-TEG system", Energy Conversion and Management 173 (2018) Elsevier, 450-460
- 4. ChallaBabu, P Ponnambalam, "The role of thermoelectric generators in the hybrid PV/T systems: A review", Energy Conversion and Management 151 (2017) Elsevier, 368-385
- 5. P Ponnambalam, Muralikumar.K, Vasundhara.P, Sreejith.S, Challa. Babu, "Fuzzy Controlled Switched Capacitor Boost Inverter" Energy Procedia 117 (2017) Elsevier, 909–916
- 6. P.Ponnambalam, M. Praveenkumar, ChallaBabu, P. Dhambi Raj, "Analysis of Stacked Multicell Converter with Fuzzy Controller", Advances in Intelligent Systems and Computing (Springer), Vol 546, pp 318-330, 2017
- 7. P.Ponnambalam, K. Aroul, P.Prasadreddy, K. Muralikumar, "Analysis of Fuzzy Controller for H-bridge Flying Capacitor Multilevel Converter", Advances in Intelligent Systems and Computing (Springer), Vol 546, pp 307-317, 2017
- 8. P.Ponnambalam, M. Praveenkumar, V. Surendar, G. Gokulakrishnan, "Fuzzy Controller for flying capacitor multicell inverter", Advances in Intelligent Systems and Computing (Springer), Vol 436, pp 521-535, 2016
- 9. P.Ponnambalam, B. Shyamsekhar, M. Praveenkumar, V. Surendar, P. Raviteja, "Fuzzy Controller for Reversing Voltage Topology MLI", Advances in Intelligent Systems and Computing (Springer), Vol 436, pp 617-632, 2016
- 10. Sreejith. S, Velamuri Suresh, P.Ponnambalam, "Static Economic Dispatch Incorporating UPFC using Artificial Bee Colony Algorithm", Advances in Intelligent Systems and Computing (Springer), Vol 436, pp 757-769, 2016
- 11. Sreejith. S, Velamuri Suresh, P.Ponnambalam, "Static economic dispatch incorporating windfarm using Flower pollination algorithm", Perspectives in Science (Elsevier), Vol 8, pp 260-262, 2016
- 12. K. Yamini, B. Vasudha, Avinash Sharma, P.Ponnambalam "Implementation of Fuzzy Logic Controller for Cascaded Multilevel Inverter with Reduced Number of Components", Indian Journal of Science and Technology, Vol 8(S2), pp 278–283, 2015