

G LAKSHMI NARAYANAN

LIST OF PUBLICATION

- [1]. Nithish Kumar Venkatachalam ; Lakshminarayanan Gopalakrishnan ; Mathini Sellathurai, “Low complexity and area efficient reconfigurable multimode interleaver address generator for multistandard radios”, IET Computers & Digital Techniques, DOI: 10.1049/iet-cdt.2015.0070, Vol. 10, Issue 2, pp. 56-68, March 2016
- [2]. Antony Xavier Glittas, Mathini Sellathurai, and G Lakshminarayanan, “A Normal I/O Order Radix-2 FFT Architecture to Process Twin Data Streams for MIMO” IEEE Trans. On VLSI , 2016 (10.1109/TVLSI.2015.2504391)
- [3]. Antony Xavier Glittas, Mathini Sellathurai, and G Lakshminarayanan “Two-parallel Pipelined FFT Processors for Real-valued Signals” IET Circuits, Devices & Systems, Vol. 10, Issue 4, pp. 330-336, July 2016.
- [4]. S, Lakshminarayanan. G, “Reconfigurable address generator for multi-standard interleaver”, Microprocessors and Microsystems, Vol. 65, pp. 47-56, Mar. 2019.
- [5]. S, Lakshmi Renuka M, Lakshminarayanan. G, Mathini Sellathurai, “Low-complex processing element architecture for successive cancellation decoder”, Integration the VLSI Journal, Elsevier, 2019, DOI: 10.1016/j.vlsi.2019.01.005.
- [6]. Aravindhan Alagarsamy, Lakshminarayanan Gopalakrishnan, Seok Bum-Ko, “KBMA: A Knowledge Based Multi-objective Application Mapping Approach for 3D NoC”, IET Computers and Digital Techniques, 2019, DOI: 10.1049/iet-cdt.2018.5055.
- [7]. A, Shalini S, Lakshminarayanan. G, “Cluster Based Application Mapping Strategy for 2D NoC”, Journal of Procedia Technology, Elsevier Publications, Vol. 25, pp 505-512, Sep 2016, DOI:10.1016/j.protcy.2016.08.138
- [8]. Shalini S, Aravindhan. A, Lakshminarayanan. G, “A Case Study on Cluster Based Power Aware Mapping Strategy for 2D NoC” ICTCAT Journal of Microelectronics, pp. 315-322, Vol. 2 (4), Jan 2017, DOI:10.21917/ijme.2017.0055
- [9]. Meenu Anna George, Aravindhan. A, Lakshminarayanan. G, “Design of Five Port Priority Based Router with Port Selection Logic for NoC” ICTCAT Journal of Microelectronics, pp. 293-299, Vol.2 (4), Jan 2017, 10.21917/ijme.2017.0051
- [10]. Ishwerya, P., V. Nithish Kumar, and G. Lakshminarayanan (2016), An efficient digital baseband encoder for short range wireless communication applications. International Conference on Electrical, Electronics, and Optimization Techniques (ICEEOT), pp. 2775-2779, 3-5 March 2016.
- [11]. Aravindhan Alagarsamy, Lakshminarayanan G, “ SAT: A New Application Mapping Method for Power Optimization in 2D-NoC”, 20th International Symposium on VLSI Design and Test (VDATE), 24-27 May 2016.
- [12]. Ishwerya, P., S. Geethu, and G. Lakshminarayanan (2016), Autocorrelation based spectrum sensing architecture on FPGA with dynamic offset compensation. 2016 IEEE Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER), pp. 153-157, 13-14 Aug. 2016.
- [13]. Ishwerya, P., S. Geethu, and G. Lakshminarayanan, An efficient hybrid spectrum sensing architecture on FPGA. 2017 International Conference on Wireless Communications, Signal Processing and Networking (WiSPNET), 22-24 March 2017.

- [14]. Kartikeya Dubey, Marshal Raj, G. Lakshminarayanan, " QCA Code converters using Novel Fault and Area efficient Exclusive-OR gate", International Conference on Nanotechnology: Ideas, Innovations and Initiatives, Roorkee, Dec 2017.
- [15]. Kartikeya Dubey, Marshal Raj, G. Lakshminarayanan, "Design Of Majority Logic Based Comparator", IEEE 9th International Conference on Computing Communication and Networking Technologies (ICCCNT), Bengaluru, July 2018.
- [16]. Rejil Raj E P, Bibin Sam Paul, G Lakshmi Narayanan, "Simplified SIFT Histogram Of Oriented Gradients Bin Locator In FPGA," IEEE 9th International Conference on Computing, Communication and Networking Technologies (ICCCNT), Bengaluru, July 2018.
- [17]. S, Lakshminarayanan. G, "Low-Complexity Successive Cancellation Decoder with Scan Chain", IEEE International Conference on Circuits and Systems in Digital Enterprise Technology (ICCSDET), Kerala, Dec 21 - 22, 2018.
- [18]. R, Lakshminarayanan. G, "Design and defect analysis of novel NAND/NOR gate in Quantum-dot cellular automata", IEEE International Conference on Circuits and Systems in Digital Enterprise Technology (ICCSDET), Kerala, Dec 21 - 22, 2018.
- [19]. Aravindhan Alagarsamy and Lakshminarayanan Gopalakrishnan, "MBA: A New Cluster Based Bandwidth and Power Aware Mapping for 2D NoC", IEEE International Conference on Circuits and Systems in Digital Enterprise Technology (ICCSDET), Kerala, Dec 21 - 22, 2018.