

### Anna University DC Member 1 Profile

**Name** : Dr. P. Sakthivel

**Designation** : Professor

**Department** : Electronics and Communication Engineering

**Name of the Organization / Institution** : College of Engineering, Guindy, Anna University, Chennai

**Place** : Chennai

**Pin code** : 600 025

**Mobile** : 9444412789

**E-mail** : psv@annauniv.edu

**Area of Specialization** : Computer Science and Engineering, Data Structures, Design and Analysis of algorithms, Modelling and Simulations, Computer Networks, VLSI Systems and Electronic Design Automation, VLSI Algorithms

#### **List of Publications:**

1. K. N. Madhusudhan and **P. Sakthivel**, "Combining Digital Signature with Local Binary Pattern-Least Significant Bit Steganography Techniques for Securing Medical Images", Journal of Medical Imaging and Health Informatics, published by American Scientific Publishers. Vol. 10, pp. 1288-1293 (2020).
2. C. Gokul, **P. Sakthivel** and P. R. Karthikeyan, "Test Scheduling for System On Chip Using Modified Firefy and Modified ABC Algorithms", SN Applied Sciences, published by Springer Nature (2019).
3. C. Vijayalakshmi, **P. Sakthivel** and Anand Vinekar, "Automated Detection and Classification of Telemedical Retinopathy of Prematurity Images", Telemedicine and e-Health, published by MARY ANN LIEBERT, INC. . pp. 1-5 (2019).
4. C. Gokul, **P. Sakthivel** and P. R. Karthikeyan, "Minimization of Test Time in System On Chip Using Artificial Intelligence-Based Test Scheduling Techniques", Neural Computing and Applications, published by Springer. (2019).
5. P. R. Karthikeyan and **P. Sakthivel**, "Entropy Based Illumination-Invariant Foreground Detection", IEICE Transactions on Information Systems, published by IEICE Information Systems Society. Issue 7, pp. 1434-1437 (2019).
6. S. Lekashri and **P. Sakthivel**, "Lossless Image Compression for Low Memory Using Dynamic Partial Reconfiguration in Asynchronous FPGA", Journal of Advanced Research in Dynamical & Control Systems, Vol. 11, Issue 2, pp. 298-307 (2019).
7. S. Lekashri and **P. Sakthivel**, " Design and evaluation of dynamic partial reconfiguration using fault tolerance in asynchronous FPGA", Microprocessors and Microsystems, published by Elsevier. Vol. 71, pp. 1-11 (2019).

8. S. Lokesh, **P. Sakthivel** and O. Uma Maheswari, "Power Transition X Filling Based Elective Huffman Encoding Technique for Test-Data Compression and Scan Power Reduction for SOCs", *Microprocessors and Microsystems*, published by Elsevier. (2019).
9. B. Gomathi, **P. Sakthivel**, " Fusion of Hybrid Optimization Algorithm and Fuzzy set for enhancing Information Retrieval using clustering", *International Journal of Advanced Research in Computer Science Special*, published by I C T Academy. Vol. 9, Issue 1, pp. 104-106 (2018).
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11. B. Arunsundar, **P. Sakthivel** and E. Natarajan, "Analysis of Energy Consumption and Latency in Advanced Wireless Networks Through DRX Mechanism", *The Journal of Supercomputing*, published by Springer. pp. 1-23 (2018).
12. S. Uma and **P. Sakthivel**, "Hardware Evaluation and Software Framework Construction for Performance Measurement of Embedded Processor", *Journal of Computational and Theoretical Nanoscience*, published by American Scientific Publishers. Vol. 15, pp. 1-9 (2018).
13. C. Gokul, **P. Sakthivel** and P. R. Karthikeyan, " Firefly and Modified Ant Colony Optimization Technique for Test Scheduling and Reduction of Testing Time for Core based System-on-Chip (SoC)", *Taga Journal*, published by SWANSEA PRINTING TECHNOLOGY LTD. Vol. 14, pp. 743-751 (2018).
14. P. R. Karthikeyan, **P. Sakthivel** and T.S. Karthik, " Comparative Study of Illumination-Invariant Foreground Detection", *The Journal of Supercomputing*, published by Springer Nature. (2018).
15. S. Sridevi and **P. Sakthivel**, "Real-Time Simulation Design for Continuous Process Industries", *Lecture Notes in Electrical Engineering*, published by Springer. Vol. 403, Issue 7, pp. 83-98 (2017).
16. D. Sivasankar and **P. Sakthivel**, "Enhancing Quality of Service in Real Time Communication Services for Network on Chip", *International Journal of Electronics Engineering Research*, Vol. 9, Issue 2, pp. 233-240 (2017).
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19. KS Thivya, **P. Sakthivel**, " Analysis of Framelets for the Microcalcification", *Proceedings of International Conference on Computer Vision and Image*, published by Springer, Singapore. pp. 11-21 (2017).
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21. S. Bhanumathi and **P. Sakthivel**, "Preservation of Private Information using Secure Multi-party Computation", Indian Journal of Science and Technology, Vol. 9, Issue 14, pp. 1-6 (2016).
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24. K. S. Thivya, **P. Sakthivel** and P. M. Venkata Sai, "Analysis of Framelets for Breast Cancer Diagnosis", Technology and Health Care, published by IOS Press. Vol. 24, pp. 21-29 (2016).
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29. T. Kalavathidevi, **P. Sakthivel**, "Design and FPGA Implementation of Bit Level Pipelined Digit Serial", Asian Journal of Information Technology, Vol. 15, Issue 7, pp. 1232-1242 (2016).
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44. T. R. Saravanan and **P. Sakthivel**, "Statistical Analysis of Inter and Intra Cluster Communication in Location Based Hierarchical Key Management System", Indian Journal of Advances in Computer Science & Technology, published by GBS Publishers & Distributors. Vol. 3, Issue 1, pp. 1-9 (2015).