K. Udhayakumar

Professor, Dept. of EEE,
Anna University

MEMS
Control and Instrumentation Engg.

GET MY OWN PROFILE

	All	Since 2015
Citations	131	97
h-index	7	6
i10-index	4	3

TITLE	CITED BY	YEAR
Path Tracking Controller Design of Differentially Driven Wheeled Mobile Robots using Cascaded Adaptive PID Controller SCSK Udhayakumar Interciencia Journal 45 (1), 1-8		2020
An Assessment of Onshore and Offshore Wind Energy Potential in India Using Moth Flame Optimization LMP Krishnamoorthy R , Udhayakumar K, Kannadasan Raju, Rajvikram Madurai ... Energies 13 (3063), 1-41	7 *	2020
Energy Efficient Reference Tracking Modified Enhanced H-Bridge Multilevel Inverter T Annamalai, K Udhayakumar, S Anitha, D Pachaikani, P Sasikumar, ... 2019 IEEE 1st International Conference on Energy, Systems and Information …	1	2019
Fuzzy based MPPT Technique for Photovoltaic System with Boost and Super lift Boost Converters MP Udhayakumar K Journal of Electrical Engineering 19 (8), 80-87		2019
A new multi-level inverter with reduced number of switches based on modified H-bridge TAK Udhayakumar International Journal of Power Electronics 10 (s), 49 - 64	4 *	2019
A New 23 Level Cascaded Multi-Level Inverter with Optimum Structure T Sunitha, K Udhayakumar, T Annamalai, C Gopinath 2018 4th International Conference on Electrical Energy Systems (ICEES), 320-327		2018
An Enhanced H-Bridge Multilevel Inverter with Reduced THD, Conduction, and Switching Losses Using Sinusoidal Tracking Algorithm A Thiruvengadam, K Udhayakumar Energies 12 (1), 1-22	1	2018
Simulation and Analysis of Various H-Bridge Inverter Topologies Employed in Cascaded Multi-level Inverter KU T Annamalai International Conference on Emerging Trends and Innovations in Engineering …		2018
Performance Analysis Comparison of 4-2 Compressors in 180nm CMOS Technology M Kumar, J Nath MS&E 225 (1), 012138		2017
A Single Phase Modified H-Bridge Seven Level Inverter with Reduced Power Electronic Switches TR T. Sreedhar, K. Udhayakumar, E. Aswini International Journal of Control Theory and Applications 10 (29), 273 - 283		2017

TITLE	CITED BY	YEAR
Negative Harmonics Injection Cuddled Equal Area Criteria Based Selective Harmonic Elimination Pulse Width Modulation Techniques for Multilevel Cascaded Inverters T Sreedhar, K Udhayakumar Journal of Computational and Theoretical Nanoscience 13 (10), 7564-7573		2016
Genetic Algorithm Based Selective Harmonic Elimination Technique For Multilevel Inverters With Unequal Voltage Sources S T, K Udhayakumar Association For The Advancement Of Modelling & Simulation Techniques In …		2016
SUPPLY VOLTAGE FLUCTUATION IMMUNE SHEPWM FOR MULTILEVEL INVERTERS WORKING WITH FLUCTUATING DC SOURCES T Sreedhar, K Udhayakumar Int J Adv Engg Tech/Vol. VII/Issue I/Jan.-March 622, 631		2016
GENETIC ALGORITHM BASED SELECTIVE HARMONIC ELIMINATION TECHNIQUE FOR MULTILEVEL INVERTERS WITH UNEQUAL VOLTAGE SOURCES KU T.Sreedhar International Journal of Innovative Works in Engineering and Technology …		2015
IMPLEMENTATION OF PHOTOVOLTAIC MPPT WITH REGULATED LOAD POWER CONTROLLER FOR POSITIVE OUTPUT ELEMENTARY OUTPUT SUPER LIFT LUO CONVERTER KU P MANIKANNAN INTERNATIONAL JOURNAL OF APPLIED ENGINEERING RESEARCH 10, 1 - 13		2015
FPGA IMPLEMENTATION OF NEWTON-RAPHSON METHOD CLASPED SHE-PWM FOR SINGLE PHASE VOLTAGE SOURCE INVERTER KU T SREEDHAR INTERNATIONAL JOURNAL OF APPLIED ENGINEERING RESEARCH 10 (51), 495 - 501		2015
Comparison Of Genetic Algorithm And Newton-Raphson Algorithm Based Selective Harmonic Elimination Techniques For Multilevel VSI S T, U K International Conference on Advances in Control and Computing of Analog and …		2014
Ternary Flip-Flops Based on Emerging Sub-32 nm Technology Nodes PA Sankar, K Udhayakumar Journal of Low Power Electronics 10 (4), 602-616		2014
MOSFET-like CNFET based logic gate library for low-power application: a comparative study PAG Sankar, K Udhayakumar Journal of Semiconductors 35 (7), 075001	21	2014
A novel carbon nanotube field effect transistor based arithmetic computing circuit for low-power analog signal processing application PA Gowrisankar, U K Procedia Technology 12, 154-162	4	2014