

Dr.G.Suchitra

Assistant Professor

Department of Electronics and communication Engineering

Government College of Technology, Coimbatore- 641013.

Email id:, suchitra.g.ece@gct.ac.in, susisivsai@gmail.com

Ph.No: 9952404623

Area of interest: Communication and Signal Processing, Cognitive Radio, Deep learning, Netwroking

PUBLICATIONS**International Journals:**

1. Anitha, G & Vijayakumari, V, Suchitra G, 2020, 'Smart Monitoring Of Electrical Parameters Using Clustering Mechanism Based On Battery Threshold And Fuzzy Logic', Journal of Electrical Engineering, ISSN 1582-4594
2. "PERFORMANCE ANALYSIS OF A CONVOLUTIONAL ENCODER USING DIGITAL MODULATION TECHNIQUE", S.Madumitha, G.Suchitra, ICTACT Journal on Communication Technology, Volume: 11 , Issue: 1, ISSN 2229-6948(Online), 2153-2158, March 2020. DOI: 10.21917/ijct.2020.0317.
3. "PERFORMANCE ANALYSIS OF REED-SOLOMON CODES IN DIGITAL COMMUNICATION SYSTEM USING LABVIEW", G.Durga Priyadharshini, G.Suchitra, ICTACT Journal on Communication Technology, Volume: 11 , Issue: 1, ISSN 2229-6948(Online), 2121-2125, March 2020. DOI: 10.21917/ijct.2020.0313.

International Conference:

1. "Performance Investigation of Interference Alignment Techniques for Underlay MIMO Cognitive Radio Networks", Reba P., Umamaheswari G., Suchitra G., 15th IEEE India Council International Conference (INDICON 2018) held at Amrita Viswa Vidyapeetham, Coimbatore during December 16-18, 2018. (978-1-5386-8235-7/18/\$31.00 ©2018 IEEE).
2. G.Durga Priyadharshini, G.Suchitra, "VLSI Implementation of Reed Solomon Codes using Xilinx ISE design suite 14.7", International Web Conference on Smart Engineering Technologies -2020, Ramco Institute of Tech, Rajapalayam , 26.06.2020 & 27.06.2020.

National Conference:

1. Recognition of Target using Otsu's algorithm and Target Classification using Convolutional Neural Network, Vithya.R, Dr.G.Suchitra, 11th National Conference on Signal Processing, Communication and VLSI Design held on 4th and 5th April 2019.
2. Cellular Multiplication for 2's complement Binary Multiplier using 4-2 Compressor and Implementation in FIR and Lattice FIR filter, R.Mahalakshmi, Dr.G.Suchitra, 11th

National Conference on Signal Processing, Communication and VLSI Design held on 4th and 5th April 2019.

3. Implementation of FFT computation using Booth-Dadda multiplier , A.Priyadharshini, Dr.G.Suchitra, 11th National Conference on Signal Processing, Communication and VLSI Design held on 4th and 5th April 2019.
4. P.Dhivya Lakshmi, G.Suchitra, “Performance Analysis of LDPC codes in Digital Communication system using LabVIEW”, Innovative Technologies in Computer Science, at Hindustan Institute of Technology, Coimbatore, March 2020.
5. S.Sreelekha, G.Suchitra “BER Performance Analysis of Block Interleaver and Convolutional Interleaver In QAM Modulation Using LabVIEW”, Innovative Technologies in Computer Science, at Hindustan Institute of Technology, Coimbatore, March 2020.
6. S.Madumitha, G.Suchitra, “BER performance analysis of convolutional encoder using digital modulation technique in NI LabVIEW”, 9th National Conference on Computing and Communication - 2020(NCCCC20), at Dr.Mahalingam College of Engg and Tech,Pollachi, March 2020.
7. G.Durga Priyadharshini, G.Suchitra,” Performance Analysis of Reed Solomon codes in Digital Communication system using LabVIEW”, 9th National Conference on Computing and Communication - 2020(NCCCC20), at Dr.Mahalingam College of Engg and Tech,Pollachi, March 2020.