PUBLICATIONS OF Dr.V.KUMAR CHINNAIYAN

Google Scholar Citation Index (as on 19/09/2020):



Book Chapter

- 1. **Dr.V.Kumar Chinnaiyan** and Dr.R.Uthirasamy, "Stepped DC Link Hybrid Power Converters for Solar PV Systems" Scholars' Press United Kingdom
- 2. **Dr.V.Kumar Chinnaiyan,** "Intelligent Computing techniques for Smart Energy Systems" Springer

International Journal

- Dr. V. Kumar Chinnaiyan "A Recommended Cascaded Structure for Multilevel Inverter with Minimum Power Electronic Components Electric Power Component and Systems", Taylor & Francis, Vol.46, Issue.13, pp.1432-1447. 2019
- Dr. V. Kumar Chinnaiyan "Cascaded Structure Multilevel Inverter with Reduced Power Electronic Components Electric" Power Components and Systems, Taylor & Francis, 2018
- 3. **Dr. V. Kumar Chinnaiyan** "Design and Testing of Two Phase Array Reconfiguration Procedure for Maximizing Power in Solar PV Systems under Partial Shade Conditions (PSC)" Energy Conversion and Management, Elsevier, Vol.178, pp.92-110, 2018
- 4. **Dr.V. Kumar Chinnaiyan** "Investigation on Three-Phase Seven-Level Cascaded DC-link Converter using Carrier Level Shifted Modulation Schemes for Solar PV System Applications" Renewable Power Generation, IET, Vol.12, Issuse.4, pp.439-449, 2018

- 5. **Dr.V.Kumar Chinnaiyan,** DS Pillai, N Rajasekar, JP Ram, "Design and testing of two phase array reconfiguration procedure for maximizing power in solar PV systems under partial shade conditions (PSC)"Energy conversion and management 178, 92-110, 2018
- Dr.V.Kumar Chinnaiyan, M Pradish, SP Karthikeyan, "Simulated annealing based selective harmonic elimination for multi-level inverter" Energy Procedia 117, 855-86, 2017
- 7. **Dr.V. Kumar Chinnaiyan** "Design and Analysis of Stepped DC Link Hybrid Converter for Solar PV Systems" Journal of Electrical Systems, Vol.13, Issue.3, pp.558-567. 2017
- 8. **Dr.V. Kumar Chinnaiyan** "Power Quality Mitigation on New DC Link Cascaded Multilevel Inverter using Space Vector Width Modulation Technique" International Journal of Printing, packaging & Allied Sciences, Vol.4, pp.2518-2534. 2016
- 9. **Dr.V. Kumar Chinnaiyan** "Performance Analysis of High Power Converters for Renewable Energy Resources" International conference on Green Energy and Healthcare Technologies, pp. 1-5,2016
- Dr.V. Kumar Chinnaiyan "Multilevel Inverter Topology using Single Source and Double Source Module with Reduced Power Electronic Components" Journal of Engineering, IET, Vol (5), pp.139-148, 2017
- 11. **Dr.V. Kumar Chinnaiyan** "Simulated Annealing Based Selective Harmonic Elimination for Multi-Level Inverter" Elsevier Energy Procedia, Vol-117, pp.855-861. 2017
- Dr. V. Kumar Chinnaiyan "Single-Ended Resonant Inverter for Photovoltaic Applications Power Research", A Journal of CPRI, Vol.12, Issuse3, pp.555-560. 2017
- 13. Dr.V. Kumar Chinnaiyan "An Analysis of Cuckoo Search Algorithm based Selective Harmonic Elimination for Multilevel Inverter" International Journal of Circuit Theory and Applications, Vol.9, pp. 6631-6637. 2017
- 14. **Dr. V. Kumar Chinnaiyan** "A Review on Multilevel Inverter: Topologies, Control Strategies and Application Power Research", A Journal of CPRI, Vol.11, Issue.4, pp.715-724. 2017

- Dr.V. Kumar Chinnaiyan "Interleaved Flyback Micro Inverter based Solar Photovoltaic Power Generation System Power Research", A Journal of CPRI, vol.12, Issue.2, pp.241-244. 2017
- Dr.V. Kumar Chinnaiyan "Modeling and Simulation of High-Efficiency Interleaved Flyback Micro-inverter For Photovoltaic Applications" Materials Today, Elsevier, Vol.4, Issuse.9, pp.10417-10421. 2017.
- 17. Mr. N. Vinothkumar, **Dr.V.KumarChinnaiyan** "A Novel MLI Topology with Reduced Power Switches Power Electronics and Renewable Energy Systems, Lecture Notes in Electrical Engineering, springer link Volume 326,pp 341-350, Nov 2014.
- 18. **Kumar Chinnaiyan, V.,**R Uthirasamy, US Ragupathy, "Structure of boost DC-link cascaded multilevel inverter for uninterrupted power supply applications", **IET Power Electronics** 8 (11), 2085-2096
- 19. **Kumar Chinnaiyan, V.,**N.VinothKumar,"A Novel MLI Topology with Reduced Power Switches Power Electronics and Renewable Energy Systems", Lecture Notes in Electrical Engineering, Springer Link Volume 326, pp 341-350, Nov 2014.
- 20. **Kumar ChinnaiyanV.,**J. Karpagam and R. Uthirasamy "Control Techniques and Reduction of Torque Pulsations for Hybrid Electric Vehicle Using Cascaded Multilevel Inverter" International Review on Modelling and Simulations, Vol. 7, N. 4, August 2014, pp.590-597,Impact Factor 0.92.
- 21. **Kumar Chinnaiyan, V.,**R.Uthirasamy, U.S.Ragupathy, and C.Mega"Experimentation of Boost Chopper Interfaced Cascaded Multilevel Inverter Topology for Photovoltaic Applications" International Review of Electrical Engineering, Vol.9, No.1, Feb 2014, pp.16-25.
- 22. **Kumar Chinnaiyan, V.,**Karpagam, J., Nirmal Kumar, A. "An Experimental Analysis of a Single Phase Matrix Converter for Adjustable Speed Drives", International Review on Modelling and Simulations, Vol.6, No.4, 2013 pp.1071-1078, (Impact Factor 0.92)
- 23. **Kumar Chinnaiyan, V**. and Jovitha Jerome "An Experimental Investigation on a Multilevel Inverter for Solar Energy Applications" International Journal of Electrical Power and Energy Systems, **Elsevier Publications**, Vol. 47, May 2013,PP 157-167.Impact Factor 3.67.

- 24. **Kumar Chinnaiyan, V.,**Jovitha Jerome and Karpagam, J. "Design and Realization of a Three Phase Cascaded Multilevel Inverter for Industrial Drives with Reduced Power Quality Issues", International Review of Electrical Engineering, Vol. 5, No. 4, pp.1364-369, 2010. Impact Factor 1.07.
- 25. **Kumar Chinnaiyan, V.**, Jovitha Jerome and Karpagam, J. "FPGA Control of Cascaded Multilevel Inverters Implementation with Experiments", Journal of Electrical Engineering, Vol. 10, No. 4, pp. 49-54, 2010.
- 26. **Kumar Chinnaiyan, V.**, Jovitha Jerome and Karpagam, J. "An FPGA Based Hardware Algorithm Implementation for Cascaded Multilevel Inverters", The Pacific Journal of Science and Technology(PJST), Vol. 11, No. 2, pp.101-106,2010.
- 27. **Kumar Chinnaiyan, V.**, Jovitha Jerome and Karpagam, J. "Design Simulation Experimentation and Analysis of Three-phase Cascaded Multilevel Inverter with Reduced Power Quality Issues", AMSE Journal of Modeling, Measurement and Control Series A: General Physics and Electrical Applications, Vol. 83,Issue 2, pp.34-46,2010.
- 28. **Kumar Chinnaiyan, V.,** Jovitha Jerome and Karpagam, J. "An FPGA Based Control Algorithm for Cascaded Multilevel Inverters",IACSIT International Journal of Engineering and Technology Vol. 1, No. 5, pp.430-434, 2009.
- 29. **Kumar Chinnaiyan, V**., Jovitha Jerome and Karpagam, J. "A Novel Implementation of Inverter for Induction Motor Drives with Reduced Losses", International Journal of Academic Research Vol. 1. No. 1, pp.114-122, 2009.