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Area of Interest: VLSI Design

List of Publications

1. R.Kiran Kumar, S.Shiyamala, "Design and Performance of Charge-Plasma-Based Schottky –FET CMOS Circuit Ring Oscillator for High Density ICs", Springer - Silicon, July 2020 (SCI – Indexed) , DOI: 10.1007/s12633-020-00581-4

2. A.Sai Suneel and S.Shiyamala , "Peak detection based energy detection of a spectrum under Rayleigh fading noise environment" Springer-Journal of Ambient Intelligence and Humanized Computing, Feb 2020

3. R.Kiran Kumar , S.Shiyamala, "A 2-D Analytical Modeling of Dual Work Function Metal Gate MOSFET using High-K Gate Dielectric with enhanced RF/Analog Performance for Low Power Applications", Springer - Silicon , Jan 2020

4. Priyanka halle , S.Shiyamala, "Trust and Cryptography centered Privileged Routing providing Reliability for WSN considering DoS attack designed for AMI of Smart Grid ", International Journal of Innovative Technology and Exploring Engineering (IJITEE), Volume: 9,Issue :3, PP 1794 - 1800, Jan 2020

5. S.Shiyamala , Sai Ganesh, "Design of High Stability Factor Lna using Cascade Mosfet at 850MHz Rf Frequency, International Journal of Recent Technology and Engineering, Volume: 8,Issue :3, PP 8925-8928,2019

6. Priyanka halle , S.Shiyamala Secure Routing through Refining Reliability for WSN Against DoS Attacks using AODSD2V2 Algorithm for AMI , International Journal of Innovative Technology and Exploring Engineering (IJITEE), Volume: 8,Issue :10, PP 282-289,2019

7. S.Shiyamala , Reicelin Rani, " Design of High Speed H.265 Sample Adaptive Offset Estimation for Ultra-Hd Tv Encoding using Clock Synchranization Code, International Journal of Recent Technology and Engineering, Volume: 8,Issue :2, PP 719-724,2019