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## Publications

TITLE	CITED BY	YEAR
An approach towards selective harmonic elimination switching pattern of cascade switched capacitor twenty nine-level inverter using artificial bee colony algorithm  A Ramesh, HH Sait  Microprocessors and Microsystems 79, 103292	1	2020
Design of a Novel Topology of Transformer-Less Cuk Inverter for Isolated Load Applications  AP Narmadha, H Habeebullah Sait  Journal of Circuits, Systems and Computers, 2150060		2020
A comprehensive review of fault ride-through capability of wind turbines with grid-connected doubly fed induction generator  G Angala Parameswari, H Habeebullah Sait  International Transactions on Electrical Energy Systems 30 (8), e12395	3	2020
Design and control of shunt active power filter for power quality improvement of utility powered brushless DC motor drives  S Kumaresan, H Habeebullah Sait  Automatika 61 (3), 507-521		2020
A Communication Model Framework for Electric Transmission Line Monitoring Using Artificial Bee Colony Algorithm  C Jeyanthi, HH Sait, K Chandrasekaran, CC Columbus  Journal of Circuits, Systems and Computers 29 (04), 2050063		2020
Embedded System Implementation of Shunt Active Power Filter with Direct Compensation Component Generation Using Linear Operational Amplifiers  S Kumaresan, H Habeebullah Sait		2020

Journal of Circuits, Systems and Computers, 2050166		
Design of a Single Chip PWM Driver Circuit for Inverter Welding Power Source JD Jabavathi, H Sait IEEE Transactions on Circuits and Systems II: Express Briefs 67 (4), 720-724	1	2019
Servo Inverter Design for High Performance Multi-axis CNC Tube Bending Machine JD Jabavathi, H Sait, K Rajkumar, RM Ehsan, S Vinod 2019 Fifth International Conference on Electrical Energy Systems (ICEES), 1-5	1	2019
Development of a lab module for multi-interface solar photovoltaic inverter for three-phase loads DJ Jabavathi, H Sait, M Ehsan The International Journal of Electrical Engineering & Education ...		2019
Habeebullah Sait H. Comparative studies on switched capacitor based single phase multilevel inverter fed from PV systems for isolated applications S Yasmin Taj, M Yamuna, V Revathi Indian Journal of Engineering 16, 300-312		2019
A Modified PWM Scheme for Harmonic Reduction in Hybrid System without Filter P Vijayarajan, A Shunmugalatha, HH Sait International Journal of Mathematical and Computational Methods 2		2017
Optimal placement of cellular transceiver for transmission line monitoring using genetic algorithm C Jeyanthi, HH Sait, K Chandrasekaran, CC Columbus Asian Journal of Research in Social Sciences and Humanities 7 (1), 653-672	3	2017
FPGA Based Implementation of the Push-Pull Configuration of a Single Phase Multilevel Inverter with a Novel PWM Technique RG Raj, S Palani, HH Sait		2017

Appl. Math 11 (3), 827-835		
<p>Hybrid Technique for Locating and Sizing of Renewable Energy Resources in Power System</p> <p>M Durairasan, A Kalaiselvan, HH Sait</p> <p>Journal of Electrical Engineering &amp; Technology 12 (1), 161-172</p>	3	2017
<p>Analysis of dynamic characteristics of flash butt welder with bidirectional power flow controller</p> <p>JD Jabavathi, H Sait, R Chaurasia, M Ehsan</p> <p>2016 IEEE 1st International Conference on Power Electronics, Intelligent ...</p>		2016
<p>State Space Modeling and Implementation of a New Transformer Based Multilevel Inverter Topology with Reduced Switch Count</p> <p>RG Raj, S Palani, HH Sait</p> <p>Circuits and Systems 7 (4), 446-463</p>		2016
<p>Development of switched capacitor based single phase multilevel inverter for isolated applications</p> <p>SY Taj, M Yamuna, HH Sait</p> <p>Development 3, 152-160</p>		2016
<p>Development of modified carrier based PWM scheme for single phase H-bridge inverter fed isolated wind-PV systems</p> <p>P Vijayarajan, A Shunmugalatha, HH Sait</p> <p>Solar Energy 126, 208-219</p>	5	2016
<p>Intelligent-Based Maximum Power Extraction on Grid-Integrated Multilevel Inverter-Fed Wind-Driven Induction Generators</p> <p>BM Sundaram, BV Manikandan, HH Sait</p> <p>Circuits and Systems 7 (09), 2551</p>		2016