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## LIST OF PUBLICATIONS

- 1.V. Thiyagarajan & P. Somasundaram, "New Asymmetric 21-Level Inverter with Reduced Number of Switches", The Journal of Engineering Research, published by Sultan Qaboos University. Vol. 16, Issue 1, pp. 18-27 (2019).
- 2.V. Thiyagarajan & P. Somasundaram, "DESIGN OF NEW SYMMETRICAL NINE LEVEL INVERTER WITH REDUCED NUMBER OF SWITCHES", Revue roumaine des sciences techniques, published by Romanian Academy. Vol. 63, Issue 2, pp. 196-201 (2018)
- 3.V. Thiyagarajan & P. Somasundaram, "A New Seven Level Symmetrical Inverter with Reduced Switch Count", International Journal of Power Electronics and Drive Systems, published by Institute of Advanced Engineering and Science. Vol. 9, Issue 2, pp. 921-925 (2018). page 6/10
- 4.V. Thiyagarajan & P. Somasundaram, "Multilevel Inverter Topology with Modified Pulse Width Modulation and Reduced Switch Count", Acta Polytechnica Hungarica, published by Óbuda University. Vol. 15, Issue 2, pp. 141-167 (2018)
- 5.Thiyagarajan V. and Somasundaram P., "Modified Nine Level Inverter with Reduced Number of Switches", International Journal of Control Theory and Applications, published by International Science Press. Vol. 10, Issue 2, pp. 217-225 (2017). —
- 6.S.V. Anbuselvi, P. Somasundaram, and R.P. Kumudini Devi, "Loss minimization in wind farm integrated AC/DC system by optimal injections and droop settings of VSC-MTDC systems", Turkish Journal of Electrical Engineering and Computer Sciences, published by The Scientific and Technological Research Council . Vol. 25, Issue 3, pp. 1693-1711 (2017)
- 7.V. Thiyagarajan & P. Somasundaram, "Modified Seven Level Symmetric Inverter with Reduced Switch Count", Advances in Natural and Applied Sciences, published by BYAENSI Publication. Vol. 11, Issue 7, pp. 264-271 (2017)
- 8. V. Thiyagarajan & P. Somasundaram, "Modeling and Analysis of Novel Multilevel Inverter Topology with Minimum Number of Switching Components", Computer Modeling in Engineering & Sciences, published by Tech Science Press. Vol. 113, Issue 4, pp. 461-473 (2017)
- 9. V. Thiyagarajan & P. Somasundaram, "New Asymmetric Seven Level Inverter with Minimum Number of Voltage Sources and Switches", Journal of Electrical Engineering, published by Politehnica Publishing House. Vol. 17, Issue 3, pp. 354-359 (2017)
- 10. V. Thiyagarajan & P. Somasundaram, "Analysis of Multicarrier PWM techniques for Photovoltaic fed Cascaded H-Bridge Multilevel Inverter", Journal of Electrical & Electronics Engineering, published by University of Oradea. Vol. 10, Issue 1, pp. 85-90 (2017)
- 11. S.V. Anbuselvi, P. Somasundaram, and R.P. Kumudini Devi, "Impact of current controller dynamics in small signal stability analysis of two terminal VSC-HVDC system employing grid voltage vector orientation control", INTERNATIONAL TRANSACTIONS ON

- ELECTRICAL ENERGY SYSTEMS, published by John Wiley & Sons, Ltd.. Vol. 26, pp. 730-749 (2016)
- 12. Thiyagarajan V. and Somasundaram P., "Performance Analysis of Photovoltaic Array with H5 Inverter under Partial Shading Conditions", International Journal of Innovation and Scientific Research, Vol. 22, Issue 1, pp. 164-177 (2016)
- 13. Karthikeyan A., Thiyagarajan V.,Somasundaram P., "Comparative analysis of PWM techniques for Photovoltaic application with HERIC inverter", Journal of Advances in Chemistry, Vol. 12, Issue 16, pp. 4950-4955 (2016)
- 14.S. Abdul Rahman, P.A. Janakiraman and P. Somasundaram, "Voltage Sag and Swell Mitigation based on Modulated Carrier PWM", International Journal of Electrical Power and Energy Systems, published by Elsevier. Vol. 66, pp. 78-85 (2015)
- 15. Thiyagarajan V. and Somasundaram P., "Optimization of Grid Connected Solar Array Using P & O Based MPPT Algorithm with Current Reference Converter", International Journal of Applied Engineering Research, Vol. 10, Issue 4, pp. 3782-3786 (2015).
- 16. S. Abdul Rahman, P. Somasundaram & P.A. Janakiraman, "Mitigation of Voltage Sag and Swell Using Direct Converters with Minimum Switch Count", Journal of Power Electronics, published by The Korean Institute of Power Electronics . Vol. 14, Issue 6, pp. 1314-1321 (2014).
- 17. M. Chandran , S. Ravindra kumar and P. Somasundaram, "Smart and Adaptive Over Current Protection for Distribution System with Distributed Generation", Advanced Research in Electrical, Electronics and Instrumentation Engineering , published by IJAREEIE. Vol. 3, Issue 2, pp. 102-111 (2014)
- 18.NEKKANTI S S S CHANDRA and P.SOMASUNDARAM, "Fast Control Strategy For Stabilizing Fixed Speed Wind Turbines in an Islanded Distributed System", Advanced Research in Electrical, Electronics and Instrumentation Engineering, published by IJAREEIE. Vol. 3, Issue 2, pp. 344-354 (2014).
- 19.S.Vimalraj and P.Somasundaram, "Fault Detection, Isolation And Identification Of Fault Location In Low-Voltage Dc Ring Bus Microgrid System", Advanced Research in Electrical, Electronics and Instrumentation Engineering, published by IJAREEIE. Vol. 3, Issue 2, pp. 570-582 (2014).
- 20.M. Devesh Raj, N. B. Muthuselvan & P. Somasundaram, "Swarm-Inspired Artificial Bee Colony Algorithm for Solving Optimal Power Flow with Wind Farm", Arabian Journal for Science and Engineering, published by Springer. Vol. 39, Issue 6, pp. 4775-4787 (2014).
- 21.S. Abdul Rahman and P. Somasundaram, "Voltage sag and swell compensation using AC/DC converters", Australian Journal of Electrical and Electronics Engineering, published by Engineers Australia. Vol. 11, Issue 2, pp. 186-194 (2014)