

Profile



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LIST OF PUBLICATIONS:

ACADEMIC YEAR	TITLE OF THE PAPER	NAME OF THE CONFERENCE/JOURNAL	DATE OF PUBLICATION/ PAGE NUMBER
2019-20	Self - Sensing Performance of Graphene Reinforced Geopolymer Concrete"	KSCE Journal of Civil Engineering	Submitted
2018-19	A Comprehensive Survey On Strategies In Multi-Core Architectures, Design Considerations, And Challenges	International Conference on Artificial Intelligence, Smart Grid and Smart City Applications – AISGSC 2019	Published as Chapter 74 in Springer
	Automatic Attendance Marking using Face Recognition and SMS Alert using IoT	International Journal for Research in Applied Science & Engineering Technology	Volume 7 Issue III, Mar 2019-
	Improving Power Efficiency in the Implementation of Discrete Cosine Transform in Multicore Architecture	Journal of Computational and Theoretical Nanoscience	Vol. 15, No.8 pp.2597-2603
	An intelligent Content Recommendation System for E-Learning using Social Network Analysis	Journal of Computational and Theoretical Nanoscience	Vol. 15, No.8 pp.2589-2596

2016-17	Design of Reconfigurable Discrete Cosine Transform in Multicore Architecture	Asian Journal of Information Technology	Vol. 15, no. 22, pp. 4522-4527.
	Improving Energy Efficiency in Multi-Core Processing Using Dynamically Re-Focusing on Processors	International Journal of Advanced Engineering Technology	Vol. VII, no. II, pp. 830-839.
2015-16	Effective Use of Cache Memory in Multi-Core Processor	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering , Vol 5, Issue 4	April 2016
	Detection and Prediction of Lung Tumour Survival by using SWFCM Clustering	International Journal of Advanced Research in Electronics and Communication Engineering , Vol 5, Issue 4	April 2016
	Design of Reconfigurable Multicore DCT Architecture on FPGA	ICGPC-2016, St.Peters University, Chennai.	23-03-16
	An Efficient FPGA Architecture For RDCT With Reduced Components	Australian Journal of Basic and Applied Sciences	December 2015
	Effective use of Mixed cell Cache Memory in Multi-core Architecture	EMTIC '15, SAEC	16-12-15
	Effective use of Mixed cell Cache Memory in Multi-Core Architecture	International Journal of Applied Engineering Research, Vol No 10, Issue 87	16-12-15
2014-15	Design of Discrete Cosine Transform on FPGA	ICIRET , Park College of Engineering and Technology	11.4.2015
	Speech Enhancement of the Vowels based on formant and Pitch	ICIRET , Park College of Engineering and Technology	11.4.2015
	Effective Architecture of DCT using sum select algorithm	International Conference on Emerging Technology, ICET 2K15	26.3.2015
	Design Of Discrete Cosine Transform Circuit in FPGA	SPECTRUM' 15, SAEC	20.3.15
	Enhancement Of Speech Signal Based On Vowel Sound	SPECTRUM' 15, SAEC	20.3.15
	A Comparison Study Of Different	NT-SIPCOM '14- TEQIP-II	27.11.2014

	Methods Of Tracking A Remote Mobile UGV From A Static Base Vehicle	sponsored National Conference on signal processing & communication	
	Effective Architecture of DCT using sum select algorithm	International Journal of Applied Engineering Research	ISSN 0973-4562 Vol. 10 No.2 (2015) pp. 1852-1856

APPRECIATION/AWARD

- ❖ Resource Person for Anna University sponsored Faculty Development Program on the Topic Filter Design at Saveetha Engineering College
- ❖ NBA Co-ordinator at Jeppiaar Institute of Technology
- ❖ NBA Criteria 1 coordinator at S.A.Engineering College and was successful in receiving the accreditation
- ❖ Received cash award for producing 95 - 100% results in the University Examination in the subjects Digital Image processing, Advanced Digital System Design, Digital Electronics, Microprocessors and Microcontrollers, Digital signal Processing, Embedded and Real Time Systems and VLSI Design.
- ❖ Resource Person in Anna University sponsored FDTP training programme in Signals & Systems, VLSI design in S.A. Engineering college
- ❖ Resource Person for Guest Lecture on the Topic Digital Image Processing at VelTech Ranga Sanku Arts College
- ❖ Session jury in International conferences EMTIC' 17, EMTIC'18 and ICICES '2015
- ❖ Received Best Teacher Award at Panimalar Engineering College
- ❖ Participated in a Mission10X workshop
- ❖ Participated in 18th International Symposium on VLSI Design and Test 2014
- ❖ Successfully completed a workshop on High Impact Teaching Skills attested by Dale Carnegie & Associates

FUNDED FDP/SEMINAR/WORKSHOP – AS CO-ORDINATOR

- ❖ Co-ordinator for the 3 days Hands on training on RF circuits and antenna design using ADS, CST & HFSS.
- ❖ Co-ordinator for the Workshop on VLSI and Nanotechnology in S.A. Engineering college
- ❖ Co-ordinator for the Faculty development programme on Computer Networks in S.A. Engineering college
- ❖ Organizing Committee members for the First International Conference ICICES'15 in S.A. Engineering college
- ❖ Co-ordinator for the International Conference EMTIC 2017 in S.A. Engineering college

- ❖ Co-ordinator for the student Symposium EXIMIUS'17 in S.A. Engineering college
- ❖ Co-ordinator for the National Conference SPECTRUM'11 and SPECTRUM'14.
- ❖ Exam-cell Co-ordinator for Anna University Examination at S.A. Engineering College

ADMINISTRATIVE EXPERIENCE

- ❖ Head of the Department in St.Peter's College of Engineering and Technology
- ❖ NBA Co-ordinator - For Older Version And Also For TIER II Application – Responsible For PEO/Vision/Mission/PO Definition, Assessment Of PEO And PO Through Alumni Survey, Employer Survey , Student Exit Survey And Course End Survey
- ❖ Co-ordinator For Set Up Various Special Labs Like Embedded laboratory and VLSI laboratory
- ❖ ISO Co-ordinator
- ❖ Project Co-ordinator
- ❖ Project Design Contest Incharge
- ❖ Organizer And Co-ordinator For various Symposiums/Workshops/Conferences/Faculty Development Programme
- ❖ Department Budget Co-Ordinator
- ❖ Internal Auditor for ISO Audit at S.A.Engineering College
- ❖ Appointed as auditor for Inter Departmental Stock verification at the semester End in S.A.Engineering College
- ❖ Appointed as Chief invigilator for Group IV Exams
- ❖ Appointed as Chief Superintendent for Anna University Theory Examination at S.A. Engineering College
- ❖ Appointed as Chief Anna University Representative for Anna University Theory Examination at Sri Ram Engineering College, Chennai institute of technology, AMS College of Engineering and Jaya Institute of Technology.
- ❖ Inter Departmental Auditor for NBA Accreditation at S.A.Engineering College
- ❖ Appointed as squad member for various colleges in Zone 19 Engineering colleges by Anna University
- ❖ Appointed as Governing Council member in St.Peter's College of Engineering and Technology
- ❖ Appointed as AICTE Co-ordinator in St.Peter's College of Engineering and Technology
- ❖ Appointed as ISO Co-ordinator in St.Peter's College of Engineering and Technology

PROFESSIONAL ACTIVITIES

- ❖ Establishment of various labs both at UG and PG level.

- ❖ Served as a coordinator for Anna University sponsored Seven Days Faculty Development Programme on “VLSI DESIGN” at S.A. Engineering College

PROFESSIONAL MEMBERSHIP

Life Member of “The Indian Society for Technical Education (ISTE)”, India

Monographs prepared

- ❖ Digital Signal Processing
- ❖ Control System Engineering
- ❖ Advanced Digital System Design
- ❖ VLSI Design
- ❖ Digital Image Processing
- ❖ Microprocessors and Microcontrollers

Theory Subjects Handled

- ❖ Embedded Systems
- ❖ Signals & System
- ❖ Digital Signal Processing
- ❖ Microprocessors and Microcontrollers
- ❖ Digital Electronics
- ❖ Advanced Digital System Design
- ❖ Transmission Lines and Waveguides
- ❖ Data Converters
- ❖ Advanced Computer Architecture

Lab Courses Handled

- ❖ VLSI Design Lab
- ❖ Linear Integrated Circuits Lab
- ❖ Embedded System Lab
- ❖ Microprocessors and Microcontrollers lab
- ❖ VLSI Design Lab
- ❖ Digital Electronics Lab
- ❖ Digital Signal Processing Lab

Personal Information

Date of Birth : June 1,1980
Gender : Female
Marital Status : Married
Spouse's Name :Er.V.Ramakrishnan
Nationality : Indian

Languages Known

Tamil and English : Read, Write & Speak

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I hereby declare that the details given above are correct and true to the best of my knowledge

Place: Avadi

Date:

Dr.R.Radhika