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Designation : Assistant professor/ECE

No. of journal Publications : 6

Area of Specialization: Low power VLSI design, VLSI architecture, Digital system design

Journals publications:

1. Nair, Binoy B., **DS Harish Ram**, Manoj Kumar Panda, A. Jayanth Balaji, T. Gireesh Kumar, and Vivek Mohan. "Future Engineering Curricula: Balancing Domain Competence with CPS Readiness." IEEE Design & Test (2020).

2. Balaji, A. Jayanth, **D. S. Harish Ram**, and Binoy B. Nair. "A deep learning approach to electric energy consumption modeling." Journal of Intelligent & Fuzzy Systems 36, no. 5 (2019): 4049-4055.

3. Thomas, Jeshmon K., and **Harish Ram**. "A Survey on Real Time Processing With Spiking Neural Networks." (2019).

4. Balaji, A. Jayanth, **DS Harish Ram**, and Binoy B. Nair. "Applicability of deep learning models for stock price forecasting an empirical study on BANKEX data." Procedia computer science 143 : 947-953, (2018).

5. Rajan, Sruthi, and **Harish Ram DS**. "FPGA Based Honeypot With Stateful TCP Emulation For SMTP Malware Collection." (2016).

6. Balaji, A. Jayanth, and **DS Harish Ram**. "FPGA based system for denial of service detection in smart grid." Journal of Engineering and Applied Sciences 10, no. 7 (2015): 2903-2906.

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1. Surej, I. Harun, S. Karthic, G. Vigneshwara, T. Jeyashri, R. Thiruvengadathan, R. Gandhiraj, KA Pradeep Kumar, B. N. Binoy, GA Shanmugha Sundaram, and **DS Harish Ram.** "Evidence of Scatter in C-band Spatio-temporal Signals using Machine Learning Models." In 2020 International Conference on Communication and Signal Processing (ICCSP), pp. 1288-1292. IEEE, 2020.

2. Natarajan, S. Surya, R. Ateesh Varun, G. Shivasubramanian, D. Thamayandran, M. Dharani, R. Gandhiraj, GA Shanmugha Sundaram **DS Harish Ram.** et al. "Detection of Interference in C-Band Signals using K-Means Clustering." In 2020 International Conference on Communication and Signal Processing (ICCSP), pp. 1521-1526. IEEE, 2020.

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5. Balaji, A. Jayanth, G. Thiru Balaji, M. S. Dinesh, Binoy B. Nair, and **DS Harish Ram**. "A Machine Learning Based Approach to Crack Detection in Asphalt Pavements." In 2018 15th IEEE India Council International Conference (INDICON), pp. 1-4. IEEE, 2018.

6. Balaji, A. Jayanth, **DS Harish Ram**, and Binoy B. Nair. "Machine learning approaches to electricity consumption forecasting in automated metering infrastructure (ami) systems: An empirical study." In Computer Science On-line Conference, pp. 254-263. Springer, Cham, 2017.

7. Balaji, A. Jayanth, **DS Harish Ram**, and Binoy B. Nair. "Modeling of consumption data for forecasting in automated metering infrastructure (AMI) systems." In Computer Science On-line Conference, pp. 165-173. Springer, Cham, 2016.

Book Chapter

1. Bhuvaneswari, M. C., **DS Harish Ram.**, and R. Neelaveni. "Design space exploration for scheduling and allocation in high level synthesis of Datapaths." In Application of Evolutionary Algorithms for Multi-objective Optimization in VLSI and Embedded Systems, pp. 69-92. Springer, New Delhi, 2015.