Details of DC Member 2

Name : Dr J Ramesh

Designation: Professor

Department: Electronics and Communication Engineering

University : PSG College of Technology

Address : Avinashi Rd, Peelamedu, Coimbatore - 641004

Phone : 0422 257 2177

Email : jkr.eee@psgtech.ac.in

List of publications:

- Michaelraj Kingston Roberts, Ramesh Jayabalan "A modified optimally quantized offset min-sum decoding algorithm for low-complexity LDPC decoder", published by Wireless Personal Communications, vol.80, Issue 2(2015).
- 2. Govindaraj Vellingiri, Ramesh Jayabalan "Adaptive neuro fuzzy inference system-based power estimation method for CMOS VLSI circuits",published by International Journal of Electronics,vol.105, Issue 3(2018).
- 3. Sanjana Prasad, J Ramesh, "Partial transmit sequence based PAPR reduction with GA and PSO optimization techniques" published by 2017 International Conference on Innovations in Information, Embedded and Communication Systems (ICIIECS) (2017).
- 4. Krishnan Thiruvenkadam, Jayabalan Ramesh, Anjali S Pillai, "Areaefficient dual-mode fused floating-point three-term adder" published by Circuits, Systems, and Signal Processing vol.38,Issue 1(2019).
- 5. Sanjana Prasad, Research Scholar, Ramesh Jayabalan, "Scaled offset PSO based PTS for PAPR reduction in OFDM systems" published by 2017 IEEE 8th Annual Ubiquitous Computing, Electronics and Mobile Communication Conference (UEMCON),pp.53-57 (2017).
- 6. Michaelraj Kingston Roberts, Ramesh Jayabalan, "A Power-and Area-Efficient Multirate Quasi-Cyclic LDPC Decoder" published by Circuits, Systems, and Signal Processing, col. 34, Issue 6(2015).
- 7. Michaelraj Kingston Roberts, Ramesh Jayabalan, "An improved low complex hybrid weighted bit-flipping algorithm for LDPC codes" published by Wireless Personal Communications, vol.82, Issue 1(2015).
- 8. J. Ramesh, "An area efficient multi-mode quadruple precision floating point adder" published by Microprocessors and Microsystems Journal, vol.45,pp.310-323 (2016)."

- 9. T Mythili, J Ramesh, P Ramanathan,"Innovative Localization Algorithm Using the Line of Intersection Technique in Wireless Sensor Networks" published by Journal of Internet Technology, vol.21, Issue 2(2020).
- 10.C Arunkumar Madhuvappan, J Ramesh, "Enhanced Test Zone Search Algorithm for High Efficiency Video Coding Encoders" published by Journal of Computational and Theoretical Nanoscience, vol.14, Issue 2(2017).