

DC Member 1 /Anna University:

Dr. P. SOMASUNDARAM

Professor, Department of Electrical and Electronics Engineering

List of publication

- 1.** Thiagarajan V. and Somasundaram P., "Optimization of Grid – Connected Solar Array Using P & O Based MPPT Algorithm with Current Reference Converter", International Journal of Applied Engineering Research, Vol. 10, Issue 4, pp. 3782-3786 (2015).
- 2.** S. Abdul Rahman, P.A. Janakiraman and P. Somasundaram, " Voltage Sag and Swell Mitigation based on Modulated Carrier PWM", International Journal of Electrical Power and Energy Systems, published by Elsevier. Vol. 66, pp. 78-85 (2015).
- 3.** Karthikeyan A., Thiagarajan V.,Somasundaram P., " Comparative analysis of PWM techniques for Photovoltaic application with HERIC inverter", Journal of Advances in Chemistry, Vol. 12, Issue 16, pp. 4950-4955 (2016).
- 4.** Thiagarajan V. and Somasundaram P., "Performance Analysis of Photovoltaic Array with H5 Inverter under Partial Shading Conditions", International Journal of Innovation and Scientific Research, Vol. 22, Issue 1, pp. 164-177 (2016).
- 5.** S.V. Anbuselvi, P. Somasundaram, and R.P. Kumudini Devi, "Impact of current controller dynamics in small signal stability analysis of two terminal VSC-HVDC system employing grid voltage vector orientation control", INTERNATIONAL TRANSACTIONS ON ELECTRICAL ENERGY SYSTEMS, published by John Wiley & Sons, Ltd.. Vol. 26, pp.

730-749 (2016).

6. . V. Thiagarajan & P. Somasundaram, "Analysis of Multicarrier PWM techniques for Photovoltaic fed Cascaded H-Bridge Multilevel Inverter", Journal of Electrical & Electronics Engineering , published by University of Oradea. Vol. 10, Issue 1, pp. 85-90 (2017).

7. . V. Thiagarajan & P. Somasundaram, " New Asymmetric Seven Level Inverter with Minimum Number of Voltage Sources and Switches", Journal of Electrical Engineering , published by Politehnica Publishing House. Vol. 17, Issue 3, pp. 354-359 (2017).

8. V. Thiagarajan & P. Somasundaram, "Modeling and Analysis of Novel Multilevel Inverter Topology with Minimum Number of Switching Components", Computer Modeling in Engineering & Sciences, published by Tech Science Press. Vol. 113, Issue 4, pp. 461-473 (2017).

9. V. Thiagarajan & P. Somasundaram, "Modified Seven Level Symmetric Inverter with Reduced Switch Count", Advances in Natural and Applied Sciences, published by BYAENSI Publication. Vol. 11, Issue 7, pp. 264-271 (2017).

10. S.V. Anbuselvi, P. Somasundaram, and R.P. Kumudini Devi, "Loss minimization in wind farm integrated AC/DC system by optimal injections and droop settings of VSC-MTDC systems", Turkish Journal of Electrical Engineering and Computer Sciences, published by The Scientific and Technological Research Council . Vol. 25, Issue 3, pp. 1693-1711 (2017).

11. Thiagarajan V. and Somasundaram P., "Modified Nine Level Inverter with Reduced

Number of Switches", International Journal of Control Theory and Applications, published by International Science Press. Vol. 10, Issue 2, pp. 217-225 (2017).

12. V. Thiagarajan & P. Somasundaram, "Multilevel Inverter Topology with Modified Pulse Width Modulation and Reduced Switch Count", Acta Polytechnica Hungarica, published by Óbuda University. Vol. 15, Issue 2, pp. 141-167 (2018).

13. V. Thiagarajan & P. Somasundaram, "A New Seven Level Symmetrical Inverter with Reduced Switch Count", International Journal of Power Electronics and Drive Systems, published by Institute of Advanced Engineering and Science. Vol. 9, Issue 2, pp. 921-925 (2018).

14. V. Thiagarajan & P. Somasundaram, "DESIGN OF NEW SYMMETRICAL NINE LEVEL INVERTER WITH REDUCED NUMBER OF SWITCHES ", Revue roumaine des sciences techniques, published by Romanian Academy. Vol. 63, Issue 2, pp. 196-201 (2018).

15. V. Thiagarajan & P. Somasundaram, "New Asymmetric 21-Level Inverter with Reduced Number of Switches", The Journal of Engineering Research, published by Sultan Qaboos University. Vol. 16, Issue 1, pp. 18-27 (2019).

16. CMK Dr. P. Somasundaram, Dr. C. Jegadheesan, M. Srinithin, R. Saravanan," Driver Fatigue Reduction Using Touch Enabled Steering Wheel", International Journal of Scientific & Technology Research 9 (2), 648-451(2020)

17. NK Dr. P. Somasundaram, Dr. C. Jegadheesan, S. Vinoth, S.T. Arun Kumar," Headlight

Glare Reduction Using Parallel Beam Technique”, International Journal of Scientific & Technology Research 9 (2), 644-647(2020)