Dr. S.Chenthur Pandian

Professor & Principal

SNS College of Technology,

Coimbatore-641035

Email: chenthur@rediffmail.com

Ph: 9443791005

List of Publications

Last 5 years

- J Beschi Raja, S Chenthur Pandian, PSO-FCM based data mining model to predict diabetic disease, Computer Methods and Programs in Biomedicine Elsevier, Vol 196, PP 105659, 2020/11/1
- T Prabhu, E Suganya, S Chenthur Pandian, Design of an UWB antenna for Microwave C and X Band Applications, 2020 6th International Conference on Advanced Computing and Communication Systems (ICACCS) IEEE, PP 311-316, 2020/3/6
- J Beschi Raja, S Chenthur Pandian, An Optimal Ensemble Classification for Predicting Churn in Telecommunication., Journal of Engineering Science & Technology Review, Vol13, Issue 2, 2020/3/1
- 4. T Prabhu, S Chenthur Pandian, Design and development of planar antenna array for mimo application, Wireless Networks Springer US, PP 1-8, 2020/1/14
- 5. J Beschi Raja, S Chenthur Pandian, Jestr r, Journal of Engineering Science and Technology Review, Vol 13, Issue 2, PP 44-49, 2020
- G Naveen Balaji, S Chenthur Pandian, Design of test pattern generator (TPG) by an optimized low power design for testability (DFT) for scan BIST circuits using transmission gates, Cluster Computing Springer US, Vol 22, Issue 6, PP 15231-15244, 2019/11/1
- N Thillaiarasu, S Chenthur Pandian, V Vijayakumar, S Prabaharan, Logesh Ravi, V Subramaniyaswamy, Designing a trivial information relaying scheme for assuring safety in mobile cloud computing environment, Wireless Networks Springer US, PP 1-14, 2019/8/16
- B Vijayalakshmi, C Ramkumar, S Niveda, S Chenthur Pandian, Smart Pest Control System in Agriculture, 2019 IEEE International Conference on Intelligent Techniques in Control, Optimization and Signal Processing (INCOS) IEEE, PP 1-4, 2019/4/11
- G Naveen Balaji, P Shanmugavadivu, P Malini, S Chenthur Pandian, 16nm CMOS Technology based Fast Ring Oscillator for Fast Computing and Mathematical Applications, 2019 3rd International Conference on Computing Methodologies and Communication (ICCMC) IEEE, PP 1186-1190, 2019/3/27

- 10. M Sivakumar, P Kannan, S Chenthur Pandian, Mitigation of PQ issues using an enhanced UPQC-based ANN approach, Journal of Circuits, Systems and Computers World Scientific Publishing Company, VOI 28, Issue 3, PP 1950046, 2019/3/22
- 11. T Prabhu, S Chenthur Pandian, E Suganya, Contact Feeding Techniques of Rectangular Microstrip Patch Antenna for 5 GHz Wi-Fi, 2019 5th International Conference on Advanced Computing & Communication Systems (ICACCS) IEEE, PP 1123-1127, 2019/3/15
- 12. P Malini, G Naveen Balaji, K Boopathiraja, A Gautami, P Shanmugavadivu, S Chenthur Pandian, Design of Swing Dependent XOR-XNOR Gates based Hybrid Full Adder, 2019 5th International Conference on Advanced Computing & Communication Systems (ICACCS) IEEE, PP 1164-1170, 2019/3/15
- N Thillaiarasu, S ChenthurPandian, A novel scheme for safeguarding confidentiality in public clouds for service users of cloud computing, Cluster Computing Springer US, Vol 22, Issue 1, PP 1179-1188, 2019/1/16
- 14. T Prabhu, S Chenthur Pandian, E Suganya, Boopathi Raja, Tri-Band F Shaped Slot In The Patch Antenna Design For Wireless Communications, communications, Vol 1, PP 5, 2019
- 15. N Thillaiarasu, S Chenthur Pandian, G Naveen Balaji, RM Benitha Shierly, A Divya, G Divya Prabha, Enforcing confidentiality and authentication over public cloud using hybrid cryptosystems, International conference on intelligent data communication technologies and internet of things Springer, Cham, PP 1495-1503, 2018/8/7
- 16. G Naveen Balaji, S Chenthur Pandian, J Geetha Ramani, B Sivasankari, M Srinivasaperumal, 65nm TGMS flipflops for High Speed Design Standards, Advances in Natural and Applied Sciences, VOI 12, Issue 5, PP 1-7, 2018/5/1
- 17. J Beschi Raja, S Chenthur Pandian, J Pamina, Certificate revocation mechanism in mobile ADHOC grid architecture, Int. J. Comput. Sci. Trends Technol, Vol 5, PP 125-130, 2017
- 18. G Naveen Balaji, S Chenthur Pandian, D Rajesh, Fast Test Pattern Generator using ATALANTA M 2.0, Asian Journal of Research in Social Sciences and Humanities Asian Research Consortium, Vol 7, Issue 2, PP 721-729, 2017
- 19. V Parimala, S Chenthur Pandian, D Ganeshkumar, An efficient particle swarm optimization technique for 4-leg shunt active power filter, Circuits and Systems Scientific Research Publishing, VOI 7, Issue 8, PP 1546-1559, 2016/6/2
- 20. G Naveen Balaji, S Chenthur Pandian, D Rajesh, A survey on effective Automatic Test Pattern Generator for self-checking Scan-BIST VLSI circuits, International Research Journal of Engineering and Technology, Vol 3, Issue 5, PP 645-648, 2016/5

- 21. G Naveen Balaji, S Chenthur Pandian, Novel Automatic Test Pattern Generator (ATPG) for degenerated SCAN-BIST VLSI Circuits, International Research Journal of Engineering and Technology, Vol 3, Issue 3, PP 1087-1091, 2016/3
- 22. N Thillaiarasu, S ChenthurPandian, Enforcing security and privacy over multi-cloud framework using assessment techniques, 2016 10th International Conference on Intelligent Systems and Control (ISCO) IEEE, PP 1-5, 2016/1/7
- 23. G Naveen Balaji, S Chenthur Pandian, V Aathira, R Havin, Transmission gate based digital circuits by re-evaluating extraordinary speed design principles, 2016 International Conference on Advanced Communication Control and Computing Technologies (ICACCCT), Syed Ammal Engineering College, Ramanathapuram, Tamilnadu, India, PP 263-268, 2016
- 24. Sengan Sudhakar, S Chenthur Pandian, Hybrid cluster-based geographical routing protocol to mitigate malicious nodes in mobile ad hoc network, International Journal of Ad Hoc and Ubiquitous Computing Inderscience Publishers (IEL), Vol 21, Issue 4, PP 224-236, 2016