

## **LIST OF PUBLICATIONS (2015-2020) by Dr.N.RAMADASS**

1. SB Sivasubramaniyan, R Seshasayanan, **N Ramadass**, “Development of Combinational Circuits by Encoding on the Basis of Developmental Biology”, Computational Intelligence and Neuroscience journal, published by HINDAWI. Volume 2020.
2. Lalin L Laudis, **N Ramadass**, “A Lion’s Pride Inspired Algorithm for VLSI Floorplanning”, Journal of Circuits, Systems and Computers, published by World Scientific Publishing Company Vol.29, Issue 01, pp. 2050003 (2020)
3. Lalin L Laudis, **N Ramadass**, Shilpa Shyam, R Benschwartz, V Suresh, “An Adaptive Symbiosis based Metaheuristics for Combinatorial Optimization in VLSI”, Procedia Computer Science journal, Published by Elsevier, Vol. 167, pp.205-212(2020).
4. P Venugopal, V Ranganathan, **N Ramadass** ,”Design of a High Speed and Low Power Analog to Digital Converter”, Journal of Computational and Theoretical Nanoscience published by American Scientific Publishers Vol.14, Issue 03, pp.1521-1523 (2017).
5. L Joseph Jeyakumar, D, Raja Paul Perinbam,J,**Ramadass.N**, Vinoth, “An Efficient Algorithm With Low Power Implementation For Hearing Aids” International Journal of Applied Engineering Research (IJAER) published by www.ripublication.com Vol.10, Issue 11, pp. 29175-29192 (2015).
6. **Dr.N.Ramadass** ,P.Priyadharshini, “Real Time Face Recognition Based on PCA and Artificial Neural Network” in International Journal of Applied Engineering Research (IJAER) published by www.ripublication.com Vol.10, Issue 22, pp.20686-20691 (2015).