Publications

International Journal

- 1. Jayaraj U Kidav, **N. M Sivamangai**, M. P Pillai, "Design of AWC core using DCD iterations for MVDR beamformer" in journal of Microprocessors and Microsystems Elsevier, 2020, https://doi.org/10.1016/j.micpro.2019.102969. (**IF:1.049**)(**Scopus indexed**)
- 2. Silpa P.A, **Sivamangai**, **N.M** "One step synthesis of graphene" in Inorganic and Nano-Metal Chemistry journal Taylor & Francis, 2019, DOI/10.1080/24701556.2019.1661470 (**IF:0.685**) (**Scopus indexed**)
- 3. Princy P, **Sivamangai**, N.M "An Efficient Wavelet Based Transient Current Test towards Detection of Data Retention Faults in SRAM" in Journal of Electronic Testing Springer, 2019, DOI /10.1007/s10836-019-05819-7 (**IF:0.625**) (**Scopus indexed**)
- 4. P.A. Silpa, **N.M.Sivamangai**, "Review on Fabrication of Graphene Nanoholes", in e-Journal of Surface Science and Nanotechnology, Vol. 17, February 2019, DOI: 10.1380/ejssnt.2019.10. (**Scopus indexed**)
- 5. KIDAV Jayaraj, **Siva Mangai** N M, PERUMAL M. Pillai, "A parallel complex divider architecture based on DCD iterations for computing complex division in MVDR beamformer"in Journal of Systems Engineering and Electronics, Vol.29, N0.6, December 2018. (**IF:0.572**)(**Scopus indexed**)
- 6. U Kidav, **N. M Sivamangai**, M. P Pillai, Subash Raja M, "Architecture and FPGA Prototype of Cycle Stealing DMA array signal processor for ultrasound sector imaging"in journal of Microprocessors and Microsystems Elsevier, 2018, DOI /10.1016/j.micpro.2018.10.005. (**IF:1.049**)(**Scopus indexed**)
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- 8. Silpa P.A, **Siva Mangai N.M.**, "Graphene and its derivatives for Nanoscale Semiconductor Memories A Density Functional Theory Based Approach", materials today: proceedings, Elsevier, 2018. (**Scopus indexed**)
- 9. L.Achsah Dorthy, N.M. Siva Mangai, A.Napolean, "Design and analysis of Dual Oxide TiO₂ based RRAM for High Frequency Applications" in IJEECS, Vol. 6, Issue 12, December 2017, ISSN 2348-117X.
- 10. Jayaraj U Kidav, **N.M Sivamangai**, Nidhi Antony, Dr. M.P Pillai, "Fixed and Floating point Array Signal Processor Architecture Implemented on FPGA and their performance comparisons", International Journal of Electronics, Electrical and Computational System, ISSN 2348-117X, Volume-6, Issue 6, June 2017.
- 11. Jayaraj U Kidav, **N.M Sivamangai**, Prasad Menon, Fathima Nyla," A Low cost and scalable research Platform for Validating Ultrasound Signal Processing Algorithms", International Journal of Engineering Technology, Science and Research IJETSR, ISSN 2394-3386, Volume-4, Issue-6, June 2017.
- 12. S Sridevi Sathya Priya, P. Karthigaikumar, **N M Sivamangai** and V Rejula, "High throughput AES algorithm using parallel subbytes and mixcolumn", in Wireless Personal Communications Springer Science, 2016, DOI 10.1007/s11277-016-3858-8. (**IF:0.951**)(**Scopus indexed**)
- 13. **Sivamangai**, N. M., S.Sridevi Sathya Priya, Karthigai Kumar, P. "An efficient Hardware Architecture for high throughput AES encryptor using MUX based Sub pipelined S-box",

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- 15. **Sivamangai, N. M**., Karthigai Kumar, P. "PSO CO2: An efficient Hardware Architecture for AES algorithm for high throughput", in Wireless Personal Communications Springer Science, 2015, DOI 10.1007/s11277-015-2739-x. (**IF:0.951**)(**Citation index:4**).
- 16. **Sivamangai, N. M.**, Shilu Tresa Vinod, D. Abraham Chandy. "Recognition of Elephants in Infrared images using clustering based Image segmentation", International Journal of Electronic Security and Digital Forensics Inderscience, Vol 7, No.3, 2015. (**scopus Indexed**).