

Cellphone : 9840226692

e-Mail ID : ramadassn@annauniv.edu

Address : No. 18, JEEVANANTHAM 4TH STREET KAMARAJ  
NAGAR AVADI CHENNAI 600071



## Present Position

Professor, Department of Electronics and Communication Engineering, College of Engineering Guindy, Anna University, Chennai from February-2016.

## Previous Positions

- Assistant Professor, Department of Electronics and Communication Engineering, College of Engineering Guindy, Anna University, Chennai.
- Lecturer, Department of Electronics and Communication Engineering, College of Engineering Guindy, Anna University, Chennai.
- Teaching Research Associate, Department of Electronics and Communication Engineering, College of Engineering Guindy, Anna University, Chennai.

## Previous Additional Responsibility

- , , Anna University, Chennai.

## Degree

- ❖ M.E. COLLEGE OF ENGINEERING, ANNA UNIVERSITY (1999 - 2001).
- ❖ B.E. in ELECTRICAL AND ELECTRONICS ENGINEERING , KELAMBAKKAM, MADRAS UNIVERSITY (1993 - 1997).

## Research Degree

- ❖ Ph.D. in EMBEDDED VLSI from Faculty of FACULTY OF INFORMATION AND COMMUNICATION, COLLEGE OF ENGINEERING, ANNA UNIVERSITY (2001 - 2008).  
Title: DYNAMICALLY RECONFIGURABLE BIT-PARALLEL PIPELINED EMBEDDED ARCHITECTURE FOR HIGH-SPEED SIGNAL PROCESSING .

## Area of Specialisation

- EMBEDDED AND VLSI

## Membership in Professional Organization

- THE INSTITUTION OF ELECTRONICS AND TELECOMMUNICATION ENGINEERS

## Research Guidance

Number of Ph.D Scholars Guiding : 7  
Number of M.E./ M.Tech. Projects : 98  
Guided  
Number of M.E./ M.Tech. Projects : 7  
Guiding  
Number of Ph.D Scholars Guided : 1  
as Joint-Supervisor  
Number of Ph.D Scholars Guiding : 1  
as Joint-Supervisor

## Papers Published in Journals

Research Papers Published in International Journals      4  
Research Papers Published in National Journals              0

1. SB Sivasubramaniyan, R Seshasayanan, N Ramadass, "Development of Combinational Circuits by Encoding on the Basis of Developmental Biology", Computational Intelligence and Neuroscience 2020
2. LL Laudis, N Ramadass, "A Lion's Pride Inspired Algorithm for VLSI Floorplanning", Journal of Circuits, Systems and Computers 29 (01), 2050003, 2020
3. LL Laudis, N Ramadass, S Shyam, R Benschwartz, V Suresh , "An Adaptive Symbiosis based Metaheuristics for Combinatorial Optimization in VLSI", Procedia Computer Science 167, 205-212, 2020
4. P Venugopal, V Ranganathan, N Ramadass, "Design of a High Speed and Low Power Analog to Digital Converter", Journal of Computational and Theoretical Nanoscience 14 (3), 1521-1523, 2017
5. L Joseph Jeyakumar, D, Raja Paul Perinbam,J,Ramadass,N, Vinoth , "An Efficient Algorithm With Low Power Implementation For Hearing Aids", International Journal of Applied Engineering Research (IJAER) 10 (11, ISSN),2015
6. Dr.N.Ramadass, P.Priyadharshini, "Real Time Face Recognition Based on PCA and Artificial Neural Network", International Journal of Applied Engineering Research (IJAER) 10 (Number 22, 2015

## **Current Sponsored Projects**

"University for Potential Excellence (UPE) in Biomedical Engineering and Instrumentation Electronic System Design and Manufacturing" ( January-2017 - December-2020 ). Project Cost: 31295000.00.

## **Honours**

1. "Aksharabhyasam on Authichudi Slate - Appreciation " given by Tamilnadu Governor Office from India (2017)

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