## Dr. R.Srinivasan

## SSN College of Engineering

TITLE	CITED BY	YEAR
Power Spectral Density Computation and Dominant Frequencies Identification from the Vibration Sensor Output under Random Vibration Environment R Srinivasan, T Tessy, B Lakshmi Defence Science Journal 70 (6), 692		2020
Reconfigurable silicon nanotube using numerical simulations AN Justeena, R Ambika, P Sadagopan, R Srinivasan JOURNAL OF COMPUTATIONAL ELECTRONICS 19 (3), 966-974		2020
Reconfigurable FET-Based SRAM and its single event upset performance analysis using TCAD simulations  AN Justeena, R Srinivasan Microelectronics Journal, 104815	1	2020
Single-event radiation performance analysis of junction and junctionless FET-based low-noise amplifiers P Rajendiran, R Srinivasan Journal of Computational Electronics 18 (4), 1162-1172	1	2019
A vision-based deep on-device intelligent bus stop recognition system GK Gudur, A Ramesh Adjunct Proceedings of the 2019 ACM International Joint Conference on		2019
Pattern Analysis on Cymatics-based Images for Pronunciation Y Munoth, NV Kumar, V Vishal, LK Pavithra, R Srinivasan 2019 International Conference on Communication and Signal Processing (ICCSP		2019
Eye Blink Detection Using Back Ground Subtraction and Gradient-Based Corner Detection for Preventing CVS TS Sharmila, R Srinivasan, KK Nagarajan, S Athithya Procedia Computer Science 165, 781-789	1	2019
Entropy Analysis on Planar Anamorphic Images SBS Chris, LK Pavithra, R Srinivasan, TS Sharmila Procedia Computer Science 165, 774-780		2019
Heavy ion impact on narrow band cascoded low noise amplifier P Rajendiran, R Srinivasan Microelectronics Reliability 91, 31-37	2	2018
Nanoscale junctionless devices using ringFET structure on bulk silicon substrate SP Scarlet, R Srinivasan, P Sasikala International Journal of Electronics Letters 6 (4), 468-480	1	2018

TITLE	CITED BY	YEAR
Impact of structural process variation on junctionless silicon nanotube FET R Ambika, R Srinivasan Advances in Natural Sciences: Nanoscience and Nanotechnology 9 (3), 035015	3	2018
Optimization of nanometer bulk junctionless Trigate FET using gate and isolation dielectric engineering SP Scarlet, R Srinivasan Materials Science in Semiconductor Processing 84, 107-114	5	2018
Impact of Doping and Spacer on the Performance of Bulk Planar Junctionless devices SP Scarlet, R Srinivasan 2018 4th International Conference on Electrical Energy Systems (ICEES), 267-273	1	2018
SITUATION AWARE TOPIC ANALYSIS FOR KEYNOTE/SPEAKER EVENTS R Srinivasan, T Kundu, CD Venkatesh, S Saluja		2018
Performance optimisation of junctionless FET in nano regime using segmented channel-A 3D numerical simulation study SP Scarlet, B Prasannanjaneyulu, R Srinivasan Superlattices and Microstructures 111, 1233-1243	1	2017
Sensitivity of Silicon Nanotube Field Effect Transistor to Structural Process Parameters R Ambika, R Srinivasan Journal of Nanoelectronics and Optoelectronics 12 (10), 1098-1104	1	2017
SET and SEU performance of single, double, triple and quadruple-gate junctionlessFETs using numerical simulations N Vinodhkumar, R Srinivasan Microelectronics journal 67, 38-42		2017
Numerical modeling of process parameters on RF metrics in FinFETs, junctionless, and gate-all-around devices  B Lakshmi, R Srinivasan International Journal of Numerical Modelling: Electronic Networks, Devices	6	2017
Effect of eccentricity on junction and junctionless based silicon nanowire and silicon nanotube FETs SP Scarlet, R Ambika, R Srinivasan Superlattices and Microstructures 107, 178-188	6	2017
SET analysis of silicon nanotube FET GD Jayakumar, R Srinivasan Journal of Computational Electronics 16 (2), 307-315	8	2017
Silicon nanotube SRAM and its SEU reliability GD Jayakumar, R Srinivasan Superlattices and Microstructures 106, 129-138	3	2017

TITLE	CITED BY	YEAR
Single event transient analyses of conventional planar tunnel FET, planar Tunnel FET with pocket and L shaped tunnel FET RG Manohari, KK Nagarajan, R Srinivasan 2017 International Conference on Nextgen Electronic Technologies: Silicon to	2	2017
Performance optimization of RingFET using LDD implantation VM Shobana, R Srinivasan, V Vaithianathan, KK Nagarajan 2017 International Conference on Nextgen Electronic Technologies: Silicon to		2017
Optimal gate length estimation of iFinFET K Suchitra, KK Nagarajan, R Srinivasan 2017 International Conference on Nextgen Electronic Technologies: Silicon to	1	2017
Investigation of independent gate operation in junctionless silicon nanotube FET using 3D numerical simulations R Ambika, R Srinivasan 2017 International Conference on Wireless Communications, Signal Processing		2017
Performance enhancement of reconfigurable FET using gate workfunction, inter-gate length and inter-gate dielectric permittivity P Sadagopan, V Vaithianathan, R Srinivasan 2017 Fourth International Conference on Signal Processing, Communication and		2017
Single event transient analysis on junctionless silicon nanotube field effect transistor G Durga, V Balamurugan, R Srinivasan 2017 International Conference on Information Communication and Embedded	2	2017
Comparative performance analysis of gate-inside, gate-outside and gate-inside&outside cylindrical junctionless silicon nanotube FET R Ambika, R Srinivasan 2017 International Conference on Information Communication and Embedded	1	2017
Design of exhaust silencer muffler for transmission losses with the performance of a four stroke diesel engine with and without muffler section S Ramu, R Srinivasan, S Somasundaram, T Achamyeleh Technology 8 (1), 139-145	5	2017
Analytical and Experimental Study on Suppression of Electromagnetic Interference on High Speed Printed Circuit Board for Wireless Communication Systems R Packianathan, R Srinivasan Wireless Personal Communications 92 (2), 333-349		2017
Realization of Silicon nanotube tunneling FET on junctionless structure using single and multiple gate workfunction R Ambika, N Keerthana, R Srinivasan Solid-State Electronics 127, 45-50	3	2017

TITLE	CITED BY	YEAR
Optimization of Bulk Planar Junctionless Transistor using Work function, Device layer thickness and Channel doping concentration with OFF Current constraint  I Jenifer, N Vinodhkumar, R Srinivasan 2016 Online International Conference on Green Engineering and Technologies	4	2016
Phase displacement study in MOSFET based ring VCOs due to heavy- ion irradiation using 3D-TCAD and circuit simulation M Ponnambalam, N Vinodhkumar, R Srinivasan, PV Chandramani Microelectronics Reliability 65, 27-34	3	2016
Analysis of independent gate operation in Si nano tube FET and threshold prediction model using 3D numerical simulation R Ambika, R Srinivasan Journal of Computational Electronics 15 (3), 778-786	3	2016
Impact of Spacer Engineering on SOI Junctionless FET Performance Using TCAD Simulation P Sasikala, P Scarlet, KK Nagarajan, R Srinivasan Journal of Materials & Metallurgical Engineering 6 (2), 42-52		2016
Realization of Tunnel FET Operation on Junctionless FET with Single Gate Electrode Work Function  N Keerthana, R Ambika, R Srinivasan Journal of Nanoengineering and Nanomanufacturing 6 (2), 99-102	1	2016
Performance analysis of n-type junctionless silicon nanotube field effect transistor R Ambika, R Srinivasan Journal of Nanoelectronics and Optoelectronics 11 (3), 290-296	17	2016
Radiation performance of planar junctionless devices and junctionless SRAMs N. Vinodhkumar and R.Srinivasan Journal of Computational Electronics 15 (1), 61-66	4	2016
Performance Analysis of Square Cross Section Based Junctionless Silicon Nanotube FET V Pavan, K Reddy, R Ambika, R Srinivasan Middle-East Journal of Scientific Research 24 (4), 1351-1354	2	2016