

## **Profile**

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| 1. Name                | : Dr.M.Nirmala Devi  |
| 2. Present designation | : Vice Chair Person  |
| 3. Email               | : <a href="mailto:m_nirmala@cb.amrita.edu">m_nirmala@cb.amrita.edu</a>             |
| 4. Address             | :Amrita Vishwa Vidhyapreetha, Coimbatore<br>Campus, Amrita Nagar,Coimbatore-641112 |
| 5. Mobile Number       | : 9443692274   |
| 6. Specialization      | : VLSI Design of Artificial Neural Networks  |

### **Qualification:**

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| B.E (ECE)                 | : Government College of Technology,<br>Coimbatore, Bharathiar University in 1990 |
| M.E (Applied Electronics) | : Government College of Technology,<br>Coimbatore, Bharathiar University in 1996 |
| Ph. D Year Awarded        | : 2010   |
| University                | : Anna University, Chennai   |

## **JOURNAL(S) PUBLISHED**

1. Ashok Kumar Mohan, Dr. Nirmala Devi M., Dr. M. Sethumadhavan, and Santhya R, "A Selective Generation of Hybrid Random Numbers via Android Smart Phones", International Journal of Pure and Applied Mathematics, vol. 118, no. 8, pp. 311-317, 2018.
2. R. S. Ranjani and Dr. Nirmala Devi M., "Golden-chip free power metric based hardware trojan detection and diagnosis", Far East Journal of Electronics and Communications, vol. 17, pp. 517-530, 2017.

3. A. Indoria, Varrun, V., Reddy, M. Krishna, Sathyasai, T., Anand, B., and Dr. Nirmala Devi M., "State Variable Filter Design Using Improvised Particle Swarm Optimization Algorithm", *Advances in Intelligent Computing and Systems (AISC) series*, vol. 325, pp. 71–78, 2015.
4. Dr. Lavanya R., Nagarajan, N., and Dr. Nirmala Devi M., "Computer-aided Diagnosis of Breast Cancer by Hybrid Fusion of Ultrasound and Mammogram Features", *Advances in Intelligent Systems and Computing*, vol. 325, pp. 403–409, 2015.
5. Dr. Ramesh Bhakthavatchalu and Dr. Nirmala Devi M., "Crypto Keys Based Secure Access Control for JTAG and Logic BIST Architecture", *International Journal of Engineering and Technology*, vol. 7, no. 3, pp. 973-984, 2015.
6. Dr. Ramesh Bhakthavatchalu, Kannan, S. K., and Dr. Nirmala Devi M., "Verilog design of programmable JTAG controller for digital VLSI IC's", *Indian Journal of Science and Technology*, vol. 8, 2015.
7. Dr. Ramesh Bhakthavatchalu and Dr. Nirmala Devi M., "Deterministic seed selection and pattern reduction in logic BIST", *International Journal of Applied Engineering Research*, vol. 10, pp. 7537-7551, 2015.
8. Karthigha Balamurugan, Dr. Nirmala Devi M., and Jayakumar, M., "Design of V-band low noise amplifier using current reuse topologies", *International Journal of Applied Engineering Research*, vol. 9, pp. 27319-27330, 2014.
9. M. Vijitha and Dr. Nirmala Devi M., "Test data Volume Reduction Using Compressed Scan Mode Architecture", *International Conference on Communication and Computing, ICC 2014*. Elsevier, Bangalore, India, 2014.
- 10.P. Sankar and Dr. Nirmala Devi M., "Particle Swarm Optimization with Different Modifications", *Third International Conference on Recent Trends in Engineering and Technology*. Elsevier Science and Technology, Chanwad, pp. 551-556, 2014.
- 11.Dr. Ramesh Bhakthavatchalu, Dr. Nirmala Devi M., and Krishnan, S., "Reconfigurable Logic Built in Self-Test technique for SoC Applications", *International Conference on Communication and Computing, ICC 2014*, vol. 3. Elsevier, Bangalore, India, pp. 16-23, 2014.

- 12.J. Sirisha, C., N. Vaishnavi, P., N. K., S., S. Vidhya, G., Y., Karthigha Balamurugan, Dr. Nirmala Devi M., and M., J., “Study and Design of CMOS Based Millimeter Wave LNA Including Noise Models”, International Conference on Communication and Computing, ICC 2014. Elsevier, Bangalore, India, pp. 1-8, 2014.
- 13.B. S. Mahalakshmi, Manikantan, S., Bhavana, P., Prem, A. M., SaiEknaath, R. S. S., and Dr. Nirmala Devi M., “Small signal modelling of GaN HEMT at 70GHz”, International Conference on Signal Processing and Integrated Networks (SPIN), 2014 . IEEE, Noida, Delhi-NCR, India, pp. 739 - 743, 2014.