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Last 5 Years Publication

- 1. A N Jayanthi ,D.Prabhu, B.Sibiram 2015,'Low power ECRL logic based parallel self-timed adder 'International Journal of Applied Engineering Research, vol. 10, no. 6, pp.12494-12501,ISSN 0973-4562, Research India Publications.
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- 4. Mr. S. Munaf, Dr. A.N. Jayanthi, Ms. L. Malathi 2017, "Automatic RF Alert system to Avoid Vehicle Accident and Rescue using Wireless Control Techniques", International Advanced Research Journal in Science, Engineering and Technology", Vol. 4, Issue 1, pp:38-41, January 2017, DOI 10.17148/IARJSET.2017.4109.
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