## Dr.M.NIRMALA DEVI

Professor/Dept. of ECE, Amrita School of Engineering, Amrita Vishwa Vidyapeetham, Coimbatore-641 112.

Mobile: 94436 92274 m\_nirmala@cb.amrita.edu

Area of Specialization: VLSI Design and Testing, Circuits, Image Processing

## **List of Publications**

- 1. Priyatharishini M and **Nirmala Devi M**, (2019) "A compressive sensing algorithm for hardware trojan detection", International Journal of Electrical and Computer Engineering, Vol. 9, Issue 5, pp. 4035-4043.
- 2. Ashok Kumar Mohan, **Nirmala Devi M**, Sethumadhavan M, and Santhya R,(2018), "A Selective Generation of Hybrid Random Numbers via Android Smart Phones", International Journal of Pure and Applied Mathematics, Vol. 118, No. 8, pp. 311-317.
- 3. Ranjani R S and **Nirmala Devi M**, (2017), "Golden-chip free power metric based hardware trojan detection and diagnosis", Far East Journal of Electronics and Communications, Vol. 17, pp. 517-530.
- 4. Indoria A, Varrun V, Reddy, Krishna M, Sathyasai T, Anand B and **Nirmala Devi M**, (2015), "State Variable Filter Design Using Improvised Particle Swarm Optimization Algorithm", Advances in Intelligent Computing and Systems (AISC) series, vol. 325, pp. 71–78.
- 5. Lavanya R, Nagarajan, N, and **Nirmala Devi M,** (2015), "Computer-aided Diagnosis of Breast Cancer by Hybrid Fusion of Ultrasound and Mammogram Features", Advances in Intelligent Systems and Computing, Vol. 325, pp. 403–409.
- 6. Ramesh Bhakthavatchalu and **Nirmala Devi M**, (2015), "Crypto Keys Based Secure Access Control for JTAG and Logic BIST Architecture", International Journal of Engineering and Technology, Vol. 7, No. 3, pp. 973-984.
- 7. Ramesh Bhakthavatchalu, Kannan, SK, and **Nirmala Devi M** (2015), "Verilog design of programmable JTAG controller for digital VLSI IC's", Indian Journal of Science and Technology, Vol. 8.
- 8. Ramesh Bhakthavatchalu and **Nirmala Devi M**, (2015), "Deterministic seed selection and pattern reduction in logic BIST", International Journal of Applied Engineering Research, vol. 10, pp. 7537-7551.
- 9. Karthigha Balamurugan, **Nirmala Devi M** and Jayakumar M, (2014), "Design of V-band low noise amplifier using current reuse topologies", International Journal of Applied Engineering Research, vol. 9, pp. 27319-27330.