

Dr. M.Arun

Associate Professor

Department Of Embedded Technology

VIT University

Vellore-632014

Publications (Last 5 years)

- S.Saravanan, S.Sathish Kumar, M.Arun, Xiao ZhiGao, M.Nithyakumar, "Certain Study on Fabrication of Reversible Quantum Dot Photonic Integrated Circuit", Caribbean Journal of Science, ISSN: 0008-6452, Vol. 53, No. 2, 2019.
- A Surendar, M Arun, AM Basha, "FFcPsA: a fast finite conventional state using prefix pattern gene search algorithm for large sequence identification", Soft Computing Vol.23, No.8, pp.2761-2771, 2019.
- Anish Chandran, Arun Manoharan, "Optimization of Tridiagonal Matrix Algorithm [TDMA] on Multicore Architectures: Computational framework and mathematical modelling", International Journal of Grid and High Performance Computing, Volume 11, Issue 4, Article 1, 2019 (140917-052547).
- Jayanthi Chandrashekar and Arun Manoharan, "SAMR: Discovery of Short Distance and Secured Routing in MANET", Journal of Engineering and Applied Sciences, Vol.14, No.18, pp. 6675-6684, 2019.
- Jayanthi Chandrashekar and Arun Manoharan, "An Identity Based Key Management Technique for Secure Routing in MANET", International Journal of Intelligent Engineering and Systems, Vol.11, No.06, pp.33-43, 2018.
- L. N. Sirisha Mrunalini and Arun M , "Dual-Band Reconfigurable Graphene based Patch antenna in Terahertz band for WNoC Applications", IET Microwaves, Antennas Propagation, August 2017.
- Bellamkonda Saidulu, Arun Manoharan , "Energy efficient and low noise OTA with improved NEF for Neural Recording Applications", International Journal of Biomedical Engineering and Technology, Inderscience, 2017
- Bellamkonda Saidulu, Arun Manoharan and Kumaravel Sundaram, "Low Noise Low Power CMOS Telescopic-OTA for Bio-Medical Applications", MDPI Computers, Vol.5, No.4, pp. 1-11, 2016.
- Bellamkonda Saidulu and Arun Manoharan, "Design of Low Power Amplifier for Neural Recording Applications using Inversion Coefficient Methodology", International Journal of Control Theory and applications, No.10 (6) , pp.719-727, 2017.
- Bellamkonda Saidulu, Arun Manoharan, Bhavani B, "An improved CMOS voltage bandgap reference circuit", Advances in Intelligent Systems and Computing, Springer, 2017. (Accepted)
- Bellamkonda Saidulu, Arun Manoharan, Bhavani B, "Power And Area Efficient TFET(20nm) Based Two Stage Opamp For Ultra Low Power Applications, Lecture Notes in Electrical Engineering, Springer, 2017 (Accepted).
- Sureshkumar N, Arun, M., "A Genetic Based Feature Selection For Classification Of Landcover Changes Using Combination Of Lands", International Journal of Bio-Inspired Computation, Inderscience, 2017, 10(3), pp. 172-187.
- Sureshkumar N, Arun, M., "Accuracy Analysis of Various Classification Algorithms for Used Land, Study Area Vellore, India", International journal of Enterprise Network Management, Inderscience, Vol.7 No.2, pp.113-132, 2016.
- Sureshkumar N, Arun, M., "Remote Sensing Image Retrieval Using Object Based,

Semantic, Classifier Techniques”, International Journal of Information and Communication Technology, Inderscience, 13(1), pp. 68-82, 2018.

- Sureshkumar N, Arun, M., "Mapping of Land Cover Changes in Vellore District by Using Remote Sensing Image", International Journal of Enterprise Network Management, Inderscience, Accepted, 2016.
- Sureshkumar N, Arun, M., "Deforestation Identification Model Using Satellite Images by Colour Based Segmentation", International Journal of PharmTech Research, Vol.8, No.4, pp. 776-781, 2015.
- Sureshkumar N, Arun, M., "Enhanced Classification Algorithms for the Satellite Image Processing, Indian Journal of Science and Technology, Vol.8, No.15, 2015.
- Surendar, A., Arun, M., "Efficient DNA sequence analysis for reduced gene selection using frequency analysis", Journal of Chemical and Pharmaceutical Sciences 9(4), pp. 3367-3373, 2016.
- Surendar, A., Arun, M., "FPGA based multi-level architecture for next generation DNA sequencing", Biomedical Research (India), 2016, pp. S75-S79.
- Surendar, A., Arun, M., Basha, A.M., "Micro sequence identification of bioinformatics data using pattern mining techniques in FPGA hardware implementation", Asian Journal of Information Technology, 15(1), 2016, pp. 76-81.
- M. Arun, A. Anudeep Kumar Reddy, V. Thulasiram, "Optimal Resource Allocation and Power Management for Mobile-To-Mobile Communication in Cellular Network", Indian Journal of Science and Technology, Vol.8, S1, pp. 235-241, 2015.
- Rakesh Kumar Achar, M. Swagath Babu, M. Arun, "Border Gateway Protocol Performance and its Protection against Disturbed Denial of Service attack", Indian Journal of Science and Technology, Vol.8, S2, pp. 127-132, 2015.
- M. Arun, P. Saravanan, "Reversible Arithmetic Logic Gate (ALG) for Quantum Computation", International Journal of Intelligent Engineering and Systems, Japan, Vol. 6, No.3, pp.1-10, 2013.
- A. Surendar, M. Arun and Dr.P.S. Periasamy, "Hardware Based Algorithms for Bioinformatics Applications – A Survey", International Journal of Applied Engineering Research, Vol. 8, No. 6, pp.745-754, 2013.
- Arun, M. and Krishnan, A. "High performance Signature Detection Architectures for Network Applications", International Journal of Inovative Computing, Information and Control, Kyushu Tokia University, Japan. (Accepted, September 2011.)
- Arun, M. and Krishnan, A. "Functional Verification of Signature Detection Architectures for High Speed Network Applications", International Journal of Automation and Computing, Springer Vol. 9, No. 4, pp.395-402, 2011.
- Arun, M. and Krishnan, A. "Power Analysis of Multiple Hashing BloomFilter Architecture for Network Applications", International Journal of Computers and Applications, ACTA press, Canada, Vol.33, No.4, pp.316-325, 2011.
- Arun, M. and Krishnan, A. "Low Power Bloom Filter Architectures Using Multi Stage Lookup Techniques", Australian Journal of Electrical & Electronics Engineering, Australia, Vol.8, No.3, pp.1-10,2011.
- Arun, M. and Krishnan, A. "Comparative Power Analysis of PrecomputationBased Content Addressable Memory" Journal of Computer Science, Vol. 7, No. 4, pp. 471-474, 2011.
- Arun, M. and Krishnan, A. "Implementation of Content Addressable Memory Using Block XOR Based Pre Computation Technique in Reconfigurable Platform for Network Applications", International Journal of Applied Engineering Research, Vol. 7, No.11, pp. 1877-1886, 2010.

- Arun, M. and Krishnan, A. “Multi Hashing Low Power Bloom Filter for Network Applications”, *Romanian Journal of Computer Science and Control Systems*, Vol. 3, No.1, pp. 7-12, 2010.
- Arun, M., Krishnan, A. and Periasamy, P.S. “Design and Implementation of a String Matching System for Network Intrusion Detection using FPGA-based low power multiple-hashing Bloom Filters”, *International Journal of Computer Science and Applications*, Vol. 1, No. 3, pp. 186-189, 2008.