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JOURNAL DETAILS

Publications
International Journal

1. Jayaraj U Kidav, N. M Sivamangai , M. P Pillai , Design of AWC core using DCD iterations for MVDR beamformer in journal of Microprocessors and Microsystems – Elsevier, 2020, https://doi.org/10.1016/j.micpro.2019.102969. (IF:1.049)(Scopus indexed)
2. Silpa P.A, Sivamangai,N.M "One step synthesis of graphene" in Inorganic and Nano-Metal Chemistry journal – Taylor & Francis, 2019, DOI/10.1080/24701556.2019.1661470 (IF:0.685) (Scopus indexed)
3. Princy P, Sivamangai,N.M "An Efficient Wavelet Based Transient Current Test towards Detection of Data Retention Faults in SRAM" in Journal of Electronic Testing - Springer, 2019, DOI /10.1007/s10836-019-05819-7 (IF:0.625) (Scopus indexed)
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10. Jayaraj U Kidav, N.M Sivamangai, Nidhi Antony, Dr. M.P Pillai, “Fixed and Floating point Array Signal Processor Architecture Implemented on FPGA and their performance comparisons”, International Journal of Electronics, Electrical and Computational System,ISSN 2348-117X, Volume-6,Issue 6, June 2017.
11. Jayaraj U Kidav, N.M Sivamangai, Prasad Menon, Fathima Nyla,” A Low cost and scalable research Platform for Validating Ultrasound Signal Processing Algorithms”, International Journal of Engineering Technology, Science and Research IJETSR , ISSN 2394-3386, Volume-4, Issue-6, June 2017.
12. S Sridevi Sathya Priya, P. Karthigaikumar, N M Sivamangai and V Rejula, “High throughput AES algorithm using parallel subbytes and mixcolumn”, in Wireless Personal Communications – Springer Science, 2016, DOI 10.1007/s11277-016-3858-8. (IF:0.951)(Scopus indexed)
13. Sivamangai, N. M., S.Sridevi Sathya Priya, Karthigai Kumar, P. “An efficient Hardware Architecture for high throughput AES encryptor using MUX based Sub pipelined S-box”, in Wireless Personal Communications – Springer Science, 2016, DOI 10.1007/s11277-016-3385-7. (IF:0.951)(Scopus indexed).
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