Profile of Dr.M.Anto Bennet

Name : Dr.M.Anto Bennet

Designation : Professor

Department : Electronics Communication Engineering

Address : Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and

Technology, Chennai.

Mobile : 9965576501

E-mail : antobennetm@gmail.com, bennetmab@gmail.com,

rantobennet@veltechengg.com.

Publications

- 1. Anto bennet M, et al., "A pattern analysis based underwater video segmentation system for target object detection "Multidimensional Systems and Signal Processing (SPRINGER),https://doi.org/10.1007/s11045-020-00721-4(2020).
- 2. M Anto Bennet, et al., "Design of Low Power 16 Bit Counter with Programmable Combinational Logic And Integrated Clock Gating Using 16nm Technology" International Journal of Electronics (Taylor & Francis & Informa UK Limited, trading as Taylor & Francis Group) DOI: 10.1080/00207217.2020.1793398 (2020).
- 3. M. AntoBennet, et al., "A review on various CMOS circuit styles" Materials Today (ELSEVIER): Proceedings, https://doi.org/10.1016/j.matpr.2020.04.858 (2020).
- 4. M. Anto Bennet, et al., "Adaptation of counters redundant bits with the provision of dual supply and modified clock gating to favour of low power in VLSI "Indian journal of pure & Applied physics, Vol,58,October 2020,pp.750-757.
- 5. Dr. AntoBennet, et al.," **A review on carbon nanotube field effect transistors** (**CNTFETs**) **for ultra-low power applications**", Renewable and Sustainable Energy Reviews(ELSEVIER), Volume 89, Pages 194-203, March 2018.
- 6. Dr. AntoBennet, et al., "Design of efficient wireless communication system with improved SER using different relay environment", Cluster Comput SPRINGER Journal, DOI 10.1007/s10586-017-1097-8, August 2017.

- 7. Dr. AntoBennet, et al., "An Enhanced Carbon Nanotube field effect transistor for VLSI circuit Designing in nano technology", Journal of Electrical Engineering, Vol.:19.1.27, Pages 01-13, MAY 2018.
- 8. Dr. AntoBennet, M," **Implementation Of LTE Advanced Using Different Relay environment In OFDMA System With SER Performance** ", Journal of Electrical Engineering, Vol.:19.1.27, Pages 01-05, june 2018.
- 9. AntoBennet, M ,et al.,, "Performance and Analysis of Compression Artifacts Reduction for MPEQ-4 Moving Pictures Using TV Regularization Method", Life Science Journal vol. 10, no. 2, pp. 102-110, 2013.
- 10. Anto Bennet, M, et al., "Performance and Analysis of Hybrid Algorithm for Blocking and Ringing Artifact Reduction", Journal of Computational and Theoretical nanoscience vol.12,no.1,pp.141-149,2015.
- 11. Dr. AntoBennet, M," **An Enhanced Carbon Nanotube field effect transistor for VLSI circuit Designing in nano technology**", Journal of Electrical Engineering, VoL:19.1.27, Pages 01-13, MAY 2018. (Scopus/Annexure I).(SCI INDEXED)(impact factor 0.542).12.