

Dr. C. Umayal

Associate Professor, School of Electrical Engineering
VIT University, Chennai Campus
Vandalur Kelambakkam Road, Chennai-600127
Mobile:9003035785, Email: umayal.c@vit.ac.in

PUBLICATIONS

INTERNATIONAL JOURNALS:

- 1 Roy, A.D. and Umayal, C., 2020. Performance Analysis of a Half Bridge Cell Based Asymmetrical Multilevel Inverter Topology with Minimum Components. Recent Advances in Electrical & Electronic Engineering (Formerly Recent Patents on Electrical & Electronic Engineering), 13(4), pp.531-545.
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13. AbhishekKumarYadav,Umayal C, Atul Fartiyal, 2016, 'Simulation and Hardware Implementation of Interleaved Technique Based DC-DC Buck-Boost Converter", International Journal of Engineering Sciences&Research Technology, pp.588-601. September 2016. ISSN:2277-9655.
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