Name of DC Member: Dr.V.R. Vijay kumar

List Of Publications

Year: 2020

1. "A compacted triple band antenna design for GSM, Wi-Fi and WiMAX communication schemes for information computation system",R Saravanakumar, VR Vijaykumar, International Journal of Business Information Systems, Volume:34 (1), Pg:40-58

Year:2019

1. "Robust secure communication protocol for smart healthcare system with FPGA implementation",V Sureshkumar, R Amin, VR Vijaykumar, SR Sekar, Future Generation Computer Systems, Volume:100, Pg:938-951

2. "A Deep Learning RCNN Approach for Vehicle Recognition in Traffic Surveillance System", V Murugan, VR Vijaykumar, A Nidhila, 2019 International Conference on Communication and Signal Processing, Pg: 0157-0160

Year: 2018

1. "Automatic Moving Vehicle Detection and Classification Based on Artificial Neural Fuzzy Inference System",V Murugan, VR Vijaykumar,Wireless Personal Communications Volume:100 (3), Pg:745-766

Year: 2017

1. "Background subtraction algorithm and image skeletonization based anomaly event detection for low resolution video",V Murugan, VR Vijaykumar, SV Selvan,2017 Fourth International Conference on Signal Processing, Communication and Networking (ICSCN),Pg: 1-6

2. "Histogram shifting based reversible data hiding with controlled contrast enhancement", VR VijayKumar, VS Babu, 2017 Third International Conference on Biosignals, Images and Instrumentation (ICBSII), Pg: 1-6

3. "Implementation of $2^n - 2^k -1$ Modulo Adder Based RFID Mutual Authentication Protocol",Vijaykumar V R, Raja Sekar S, Elango S, Ramakrishnan S, IEEE Transactions On Industrial Electronics-Early Access (DOI: 10.1109/TIE.2017.2711864)

Year:2016

1. "A novel lightweight and low power tag-reader mutual authentication protocol for portable RFID based security systems",VR Vijaykumar, R Reghunath, S Rajasekar, S Elango,2016 IEEE Industrial Electronics and Applications Conference (IEACon), Pg:360-365

2. "Adaptive impulse detection based selective window median filter for removal of random-valued impulse noise in digital images",K Ashok, A Kalaiselvi, VR Vijaykumar,COMPEL-The international journal for computation and mathematics in electrical and electronic engineering, 2016/9/5

3. "VLSI Architecture of Switching Median Filter for Salt and Pepper Noise Removal",VR Vijaykumar, G Santhanamari, S Elango,IAENG International Journal of Computer Science Volume:43 (1)

4. "Adaptive Window Based Multi Stage Impulse Noise Detection for Removal of Random Valued Impulse Noise in Digital Images",K Ashok, VR Vijaykumar,Asian Journal of Information Technology ,Volume:15 (3), Pg:635-638