

Dr M Nirmala Devi

Vice Chairperson

Electronics and Communication Engineering

School of Engineering

Coimbatore

m_nirmala@cb.amrita.edu

Ph: 9443692274

Total Years of Experience: 23 Years

PUBLICATIONS:

1. Ashok Kumar Mohan, Dr. Nirmala Devi M., Dr. M. Sethumadhavan, and Santhya R, “A Selective Generation of Hybrid Random Numbers via Android Smart Phones”, International Journal of Pure and Applied Mathematics, vol. 118, no. 8, pp. 311-317, 2018.
2. R. S. Ranjani and Dr. Nirmala Devi M., “Golden-chip free power metric based hardware trojan detection and diagnosis”, Far East Journal of Electronics and Communications, vol. 17, pp. 517-530, 2017.
3. A. Indoria, Varrun, V., Reddy, M. Krishna, Sathyasai, T., Anand, B., and Dr. Nirmala Devi M., “State Variable Filter Design Using Improvised Particle Swarm Optimization Algorithm”, Advances in Intelligent Computing and Systems (AISC) series, vol. 325, pp. 71–78, 2015.
4. Dr. Lavanya R., Nagarajan, N., and Dr. Nirmala Devi M., “Computer-aided Diagnosis of Breast Cancer by Hybrid Fusion of Ultrasound and Mammogram Features”, Advances in Intelligent Systems and Computing, vol. 325, pp. 403–409, 2015.
5. Dr. Ramesh Bhakthavatchalu and Dr. Nirmala Devi M., “Crypto Keys Based Secure Access Control for JTAG and Logic BIST Architecture”, International Journal of Engineering and Technology, vol. 7, no. 3, pp. 973-984, 2015.
6. Dr. Ramesh Bhakthavatchalu, Kannan, S. K., and Dr. Nirmala Devi M., “Verilog design of programmable JTAG controller for digital VLSI IC's”, Indian Journal of Science and Technology, vol. 8, 2015.
7. Dr. Ramesh Bhakthavatchalu and Dr. Nirmala Devi M., “Deterministic seed selection and pattern reduction in logic BIST”, International Journal of Applied Engineering Research, vol.

PUBLICATIONS:

10, pp. 7537-7551, 2015.

8. Karthigha Balamurugan, Dr. Nirmala Devi M., and Jayakumar, M., “Design of V-band low noise amplifier using current reuse topologies”, International Journal of Applied Engineering Research, vol. 9, pp. 27319-27330, 2014.
9. P. R. Sruthi and Dr. Nirmala Devi M., “A modified scheme for simultaneous reduction of test data volume and testing power”, Sixteenth International Symposium on VLSI Design and Test (VDAT 2012, vol. 7373 LNCS, pp. 198-208, 2012.
10. Dr. Nirmala Devi M. and Arumugam, S., “VLSI implementation of artificial neural networks - A survey”, International Journal of Modelling and Simulation, vol. 30, pp. 148-154, 2010.
11. Dr. Nirmala Devi M., N Mohankumar, and Arumugam, S., “Modeling and analysis of neuro-genetic hybrid system on FPGA”, Elektronika ir Elektrotechnika (International Journal of Electrical and Electronics Engineering (JEEE)), vol. 96, pp. 69-74, 2009.
12. S. Sarathkrishna, Karthigha Balamurugan, Dr. Nirmala Devi M., and Dr. Jayakumar M., “Design and Analysis of GaN HEMT based LNA with CPW matching”, in 2014 Eleventh International Conference on Wireless and Optical Communications Networks (WOCN), , 2014.
13. B. Anand, Aakash, I., , Varrun, V., Reddy, M. K., Sathyasai, T., and Dr. Nirmala Devi M., “Improvisation of particle swarm optimization algorithm”, in 2014 International Conference on Signal Processing and Integrated Networks, SPIN 2014, Noida, 2014, pp. 20-24.
14. Dr. Ramesh Bhakthavatchalu, Krishnan, S., Vineeth, V., and Dr. Nirmala Devi M., “Deterministic seed selection and pattern reduction in logic BIST”, in 18th International Symposium on VLSI Design and Test, VDAT 2014, Coimbatore, 2014.
15. P. R. Sruthi and Dr. Nirmala Devi M., “Modified AVR Code for Test Data Compression”, in 7th International Workshop on Unique Chips and Systems (UCAS-7) in conjunction with 18th International Symposium on High Performance Computer Architecture, IEEE Computer Society, New Orleans, Louisiana, USA, 2012.

16. A. Scaria, D. Nath, B., Dr. Nirmala Devi M., and N Mohankumar, "Hardware implementation of svd based colour image watermarking in wavelet domain", in Proceedings of 2011 International Conference on Process Automation, Control and Computing, PACC 2011, Coimbatore, 2011.