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Elliptic Curve Cryptography
Computer Arithmetics
Physically Unclonable Functions

| | All | Since 2015 |
|---|---|---|
| Citations | 15 | 14 |
| h-index | 2 | 2 |
| i10-index | 0 | 0 |

| TITLE | CITED BY | YEAR |
|---|---|---|
| An improved quad Itoh-Tsujii algorithm for FPGAs<br>V VR, N Murali, S Rajaram<br>IEICE Electronics Express 10 (18), 20130612-20130612 | 7 | 2013 |
| Fast computation of scalar multiplication over binary edwards curve processor against side channel attack<br>VR Venkatasubramani, GR Kumar, K Vignesh, G ManiRajan, S Rajaram<br>2014 International Conference on Electronics and Communication Systems … | 5 | 2014 |
| Novel techniques for Montgomery modular multiplication algorithms for public key cryptosystems<br>VR Venkatasubramani, S Rajaram<br>2011 IEEE Electrical Design of Advanced Packaging and Systems Symposium … | 2 | 2011 |
| Novel Methods for Montgomery Modular Multiplication for Public Key Cryptosystems<br>VR Venkatasubramani, M Surendar, S Rajaram<br>International Conference on Network Security and Applications, 320-330 | 1 | 2011 |
| High-Speed Inversion Using $x^{4^n}$ Units<br>M Kalaiarasi, VR Venkatasubramani, AC Grace, S Rajaram<br>Advances in Communication Systems and Networks, 591-597 | | 2020 |
| A High-performance Elliptic Curve Cryptographic Processor for FPGA Platform<br>VR Venkatasubramani, N Premkumar, K Vignesh, S Rajaram<br>Intelligent Computing and Applications, 307-312 | | 2015 |
| Design and FPGA implementation of elliptic curve cryptographic processor<br>VR Venkatasubramani<br>Chennai | | |
| Bipartite GF (2 m) Modular Multiplier Method<br>VR Venkatasubramani, M Arunarumugam, R Ragavendran, ... | | |
| A Parallel Quad Itoh-Tsujii Multiplicative Inversion Algorithm for FPGA Platforms<br>M Kalaiarasi, VR Venkatasubramani, S Rajaram<br>2020 Third ISEA Conference on Security and Privacy (ISEA-ISAP), 31-35 | | |