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Present Position

Associate professor, Department of Electronics Engineering, Madras Institute of Technology, Anna University, Chennai from June-2015.

Present Additional Responsibility

- Executive Warden, Madras Institute of Technology, Anna University, Chennai from September-2019.
- Class Committee Chairman-(ME VLSI Design and Embed, Department of Electronics Engineering, Anna University, Chennai from June-2018.

Previous Positions

- Assistant Professor, Department of Electronics Engineering, Madras Institute of Technology, Anna University, Chennai during June-2012 and May-2015.
- Lecturer Sr. Grade, Department of Electronics Engineering, Madras Institute of Technology, Anna University, Chennai during June-2008 and May-2012.
- Lecturer, Department of Electronics Engineering, Madras Institute of Technology, Anna University, Chennai during July-2003 and July-2008.

Previous Additional Responsibility

- Class Committee Chairman -(be-i Year), Department of Electronics Engineering, Anna University, Chennai during December-2017 and May-2018.
- Faculty Advisor, Department of Electronics Engineering, Anna University, Chennai during August-2015 and July-2016.
- Class Committee Chairman (me-vlsi Design And Embed, Department of Electronics Engineering, Anna University, Chennai during July-2015 and December-2017.
- Deputy Warden, , Anna University, Chennai during August-2014 and September-2019.
- President Electronics Engineers Association, Department of Electronics Engineering, Anna University, Chennai during August-2013 and April-2014.

- Faculty Advisor, Department of Electronics Engineering, Anna University, Chennai during August-2011 and April-2015.
- Faculty Advisor, Department of Electronics Engineering, Anna University, Chennai during August-2007 and April-2010.
- Additional Chief Superintendent For End Semester E, MIT, Anna University, Chennai during November-2005 and January-2006.
- Additional Course Coordinator, Department of Electronics Engineering, Anna University, Chennai during August-2003 and April-2008.
- Faculty Advisor, Department of Electronics Engineering, Anna University, Chennai during July-2003 and April-2007.

Other Employment

- Lecturer, Chennai school of ship Management for 2 years.
- Lecturer, Sri Krishna College of Engineering and Technology for 1 year.

Degree

- ❖ M.E. in Applied Electronics, Department of Electrical and Electronics Engineering, PSG Tech, Coimbatore, Bharathiar University (2001 - 2003).
- ❖ AMIE. in ELECTRICAL AND ELECTRONICS ENGINEERING , THE Institution of Engineers (I), Calcutta, 4-years (1995 - 1999).

Research Degree

- ❖ Postdoctoral Research in Network on chip from Faculty of School of Information and Communication Engineerin, KTH Royal Institute of Technology, Sweden, KTH Royal Institute of Technology, Sweden (2010 - 2011).
Title: Concept and Design of Exhaustive-Parallel Search Algorithm to Support Quality-of-service in Network-on-Chip.
- ❖ Ph.D. in Analog IC Design from Faculty of Information and Communication Engineering, College of Engineering, Guindy, Anna University (2004 - 2009).
Title: A Power Optimized 10-bit 100MS/s Pipelined Analog-to-Digital Converter for High Speed Interface Circuits.

Area of Specialisation

- Analog IC Design, VLSI Design, Devices and NoC design

Membership in Professional Organization

- Senior Member in IEEE, Member in IETE, Associate Member in IE

Research Guidance

Number of Ph.D Scholars Guided	: 2
Number of Ph.D Scholars Guiding	: 4
Number of M.E./ M.Tech. Projects Guided	: 12
Number of M.E./ M.Tech. Projects Guiding	: 1
Number of Ph.D Scholars Guided as Joint-Supervisor	: 3

Papers Published in Journals

Research Papers Published in International Journals	: 22
Research Papers Published in National Journals	: 1

1. D. Meganathan, S. Moorthi, Amrith Sukumaran, M. M. Dinesh Babu and J. Raja Paul Perinbam, "A 52.6 mW 10-bit, 100 MS/s Pipelined CMOS Analog-To-Digital Converter", Journal of Low Power Electronics, Vol. 4, pp. 1-20 (2008).
2. D. Meganathan, A. Sukumaran, M.M.DineshBabu, S.Moorthi .Deepalakshmi R, "A systematic design approach for low-power 10-bit 100 MS/s pipelined ADC", International Journal of Microelectronics, published by Elsevier. Vol. 40, Issue 10, pp. 1417-1435 (2009).
3. D. Meganathan, Raja Paul Perinbam, R.Deepalakshmi, "High speed, low power 100 MS/s front end track-and-hold amplifier for ten-bit pipelined ADC", International Journal of High Performance Systems Architecture, published by Inderscience. Vol. 2, Issue 1, pp. 1-20 (2009).
4. Moorthi S., Meganathan D., Janarthanan D., Praveen Kumar P. and Raja Paul Perinbam J, " Low Jitter ADPLL based Clock Generator for High Speed SoC Applications", International Journal of Electrical Computer and Systems Engineering, published by Taylor and Francis. Vol. 96, Issue 11, pp. 1183-89 (2009).
5. D.Meganathan, "A 24.5mW, 10-bit, 50MS/sec CMOS pipelined Analog-to-Digital Converter", International Journal of High Performance Systems Architecture, published by Inderscience. Vol. 3, Issue 4, pp. 184-200 (2011).
6. Jose Anand, J. Raja Paul Perinbam, and D. Meganathan, " Performance of Optimized Routing in Biomedical Wireless Sensor Networks using Evolutionary Algorithms", Comptes rendus de l'Academie bulgare des Sciences, published by ENGINEERING SCIENCES. Vol. 68, Issue 8, pp. 1049-1054 (2015).
7. Umamaheswari S , Meganathan D and Raja Paul Perinbam J, " Runtime Buffer Management To Improve The Performance In Irregular Network-on-Chip Architecture", Journal of Systems Architecture, published by Sadhana, Indian Academy of Sciences, Springer. Vol. 10, Issue 4, pp. 1117-1137 (2015).

8. Jose Anand, J. Raja Paul Perinbam, and D. Meganathan, " Design of GA-Based Routing In Biomedical Wireless Sensor Networks", International Journal of Applied Engineering Research , Vol. 10, Issue 4, pp. 9281-9292 (2015).
9. Jose Anand, J. Raja Paul Perinbam, and D. Meganathan, " PSO based Optimized Routing in Biomedical Wireless Sensor Networks", Jokull Journal, Vol. 65, Issue 3, pp. 9281-9292 (2015).
10. D.R. Denslin Brabin, Raja Paul Perinbam, and D. Meganathan, " A High Capacity Data Hiding Scheme for Digital Images Using Normalization", International Journal of Applied Engineering Research, Vol. 10, Issue 1, pp. 1341-1350 (2015).
11. D. R. Denslin Brabin, J. Raja Paul Perinbam, D. Meganathan,, "A Block Based Reversible Data Hiding Scheme for Digital Images Using Optimal Value Computation", Wireless personal communication, published by Springer. Vol. 94, Issue 10, pp. 1-14 (2016).
12. Jose Anand, J. Raja Paul Perinbam & D. Meganathan, "Q-Learning-Based Optimized Routing in Biomedical Wireless Sensor Networks", Networks' IETE Journal of Research, published by Taylor and Francis . Vol. 63, Issue 1, pp. 89-97 (2017).
13. Madhusudhana Reddy Barusu, Umamaheswari Sethurajan, Meganathan Deivasigamani, "Low-cost SPLN-based electrical faults identification for three-phase squirrel cage induction motor using handheld Doppler radar signal analysisMadhusudhana Reddy Barusu", IET Power Electronics, published by IEEE. Vol. 11, Issue 7, pp. 1205-1216 (2018).
14. Madhusudhana Reddy Barusu, Umamaheswari Sethurajan, Meganathan Deivasigamani, " A non-invasive method for rotor bar fault diagnosis in three-phase squirrel cage induction motor with advanced signal processing technique", Journal of Engineering, published by IET. pp. 1-6 (2018).
15. Roy Francis, Meganathan D, " A Novel DDS Architecture for Dynamic Reconfigurable Three Phase Induction Motor Drive," , Journal of Electrical Engineering, published by University . Vol. 18, Issue 2, pp. 1-7 (2018).
16. Roy Francis, Meganathan D, "An Improved ANFIS with Aid of ALO Technique for THD Minimization of Multilevel Inverters", Journal of Circuits, Systems and Computers, , published by World Scientific. Vol. 27, Issue 12, (2018).
17. S.Balaji, D.Meganathan, "FPGA accelerated computing for particle identification in high-energy physics experiments", IOP Press, published by IOP Press. pp. 1-6 (2019).
18. Roy Francis and D.Meganathan, "A dual-mode cascaded H-bridge multilevel inverter for improving THD", Journal of Electrical Engineering,, published by Springer. Vol. 101, Issue 1, pp. 225-236 (2019).
19. S.Balaji, D.Meganathan and S.Ramasamy, "Single-Event-Hardened Timing Generator for waveform digitizer based readout electronics", Journal of Instrumentation , published by IOP Press. Vol. 14, pp. 1-8 (2019).
20. 1) S.Jagannathanan, V. Sathiesh Kumara and D. Meganathan, " Design and Implementation of In-situ Human-Elephant Conflict Management", of Intelligent and Fuzzy Systems, , published by IOS Press. Vol. 36, Issue 3, pp. 2005-2013 (2019).

21. Madhusudhana Reddy Barusu, Umamaheswari Sethurajan, Meganathan Deivasigamani, "Diagnosis of Bearing Outer Race Faults Using a low-cost non-contact method with Advanced Wavelet Transforms", Elektronika ir Elektrotechnika, published by ELEKTRONIKA IR ELEKTROTECHNIKA. Vol. 25, Issue 1, pp. 44-53 (2019).
22. D.R.Denslin Brabin, J.Rajapaul Perinbam, D.Meganathan, "Watermark visibility adjustable RVW scheme for JPEG images", Wireless Personal Communication, published by Springer. (2020).
23. K.Pandiammal, D.Meganathan, "Efficient design of QCA based hybrid multiplier using clock zone based crossover", Analog Integrated Circuits and Signal Processing, Vol. 102, pp. 63-77 (2020).

Papers Presented in Programmes

Research Papers Presented in International Programmes : 18
Research Papers Presented in National Programmes : 4

1. V.Ramana and D.Meganathan, "Improving test effectiveness of SCAN based BIST by scan chain partitioning" presented in a National level conference on 2nd National Conference on Signals, Systems and Communication, organised by Anna University, India from 10-May-2007 to 11-May-2006.
2. S.Moorthi, Meganathan D, J Raja Paul Perinbam,, on International conference on Computer Electrical and Systems Science and Engineering from 07-Aug-2007 to 09-Aug-2007.
3. Meganathan D., Vishnu K.G., Hariharan and Raja Paul Perinbam J. , "Macro-level Model of Pipelined ADC" presented in a International level conference on Proceedings of the International Conference on Modelling and Simulation CITCOM 07, organised by Coimbatore Institute of Technology, India from 27-Aug-2007 to 29-Aug-2007.
4. D.Meganathan, "A Sub 1V low Power Voltage Reference Generator" presented in a National level conference on National Conference on Signals Systems and Communication, organised by Anna University, India from 08-May-2008 to 09-May-2008.
5. K.Bhoolakshmi and D.Meganathan, "VLSI Implementation of Enhanced Encryption Techniques for public protocols" presented in a National level conference on National Conference on Signals Systems and Communication, organised by Anna University, India from 08-May-2008 to 09-May-2008.
6. D.Meganathan and Axel Jantsch, "A low-power, medium-resolution and high-speed CMOS" presented in a International level conference on NORCHIP Conference, Tampere, FINLAND from 15-Nov-2010 to 16-Nov-2010.
7. D.Meganathan, Shaghayeghsadat Tabatabaei, Axel Jantsch, , "Concept and Design of Exhaustive-Parallel search a" presented in a International level conference on SoC Conference, California from 02-Nov-2011 to 03-Nov-2011.

8. Umamaheswari S , Meganathan D and Raja Paul Perinbam, "Customized Adaptive Algorithm for Deadlock Free routing in irregular mesh NoC" presented in a International level conference on IET, 4th International Conference on Sustainable Energy and Intelligent system, organised by KCG, India from 12-Dec-2013 to 14-Dec-2013.
9. Roy Francis and D.Meganathan, "Novel Static Reconfigurable PWM controller for Three Phase Induction motor drive" presented in a International level conference on 8th International Conference on Science, Engineering and Technology , organised by VIT, India from 06-May-2014 to 07-May-2014.
10. N.Jagadeesh kumar,V.Venkatesh,P.Namachivayam,.D.Meganathan, "Design and Analysis of CNTFET based SRAM in 32nm Technology" presented in a International level conference on International Conference on Advances in Design and Manufacturing (ICAD&M'14) 2014, organised by NIT-Trichy, India from 05-Dec-2014 to 06-Dec-2014.
11. Sumithra G, Yogapriya P and Meganathan D , "Shallow water path model for underwater acoustic communication" presented in a International level conference on UACE2015 - 3rd Underwater Acoustics Conference, Greece from 21-Jun-2015 to 26-Jun-2015.
12. SUMITHRA Ganapathi, Meganathan D, "Noise Reducation in UnderwaterAcoustic Signals for Tropical and Subtropical Coastal Waters" presented in a International level conference on IEEE/OES COA 2016, organised by Harbin University, China from 09-Jan-2016 to 11-Jan-2016.
13. Roy Francis and D.Meganathan, "Reconfigurable PWM Controller for Three Phase Induction motor drive" presented in a International level conference on 3rd International Conference on Signal Processing, Communications and Networking, organised by Madras Institute of Technology, India from 26-Mar-2016 to 28-Mar-2015.
14. R.Suganya, D.Meganathan, "High Performance VLSI Adders" presented in a International level conference on 3rd International Conference on Signal Processing, Communications and Networking, organised by Madras Institute of Technology, India from 26-Mar-2016 to 28-Mar-2015.
15. R. Francis and D.Meganathan, "Efficient 81-level sinusoidal and modified sinusoidal multi-stepped inverter" presented in a International level conference on 8th IET International Conference on Power Electronics, Machines and Drives (PEMD 2016), Glasgow, UK from 19-Apr-2016 to 21-Apr-2016.
16. G. Saranya, G. Sumithra and D. Meganathan, on An Approach to Detect Dolphin Whistle from Underwater Sounds, organised by Arjun College of Engineering, India from 20-Apr-2016.
17. Jagadeesh Kumar N and Meganathan D, "Design and Simulation of Low-Power High Gain, High Bandwidth Recycling Folded Cascode OTA" presented in a International level conference on 4th International Conference on Signal Processing, Communications and Networking, organised by Department of Electronics Engineering, MIT Campus, Anna University, India from 16-Mar-2017 to 18-Mar-2017.
18. Pandiammal K and Meganathan D, "QCA Based FIR Serial Multiplier using cut-set algorithm" presented in a International level conference on IEEE-Nano-2017, organised by Department of Electrical and Computer Engineering, University of Pittsburgh, USA from 25-Jul-2017 to 28-Jul-2017.

19. Madhusudhana Reddy Barusu. Umamaheswari Sethurajan, Meganathan D, "A non-invasive method for rotor bar fault diagnosis in three-phase squirrel cage induction motor with advanced signal processing technique" presented in a International level conference on The 9th International Conference on Power Electronics, Machines and Drives, organised by IET, Liverpool, UK from 17-Apr-2018 to 19-Apr-2018.
20. Madhusudhana Reddy Barusu. Umamaheswari Sethurajan, Meganathan D, "Diagnosis of Bearing Outer Race Faults Using a low-cost non-contact method with Advanced Wavelet Transforms" presented in a International level conference on ELECTRONICS -2018 Conference, Lithuania, UK from 18-Jun-2018 to 20-Jun-2018.
21. S.Jagannathan, V.Sathiesh Kumar and D.Meganathan, "Design and Implementation of In-situ Human-Elephant Conflict Management System" presented in a International level conference on Fourth International Symposium on Intelligent Systems Technologies and Applications , organised by ISTA'18, India from 19-Sep-2018 to 22-Sep-2018.
22. Pandiammal.K and D.Meganathan, "Design of 8 bit Reconfigurable ALU Using Quantum Dot Cellular Automata" presented in a International level conference on IEEE 13th Nanotechnology Materials and Devices Conference (NMDC), USA from 14-Oct-2018 to 17-Oct-2018.

Books Published

1. "Hand Book on Marine Electrotechnology" authored by D.Meganathan and published by Chennai School of Ship Management, Chennai.(2000)
2. "Electronic Circuits-I" authored by D.Meganathan and published by Yesdee, Chennai.(2020)

Programme Chaired

1. National level conference.
2. Co Chairman, International level conference titled "3rd International conference on "Signal Processing, Communication and Networking (ICSCN 2015)" conducted by Madras Institute of Technology from 26-Mar-2015 to 28-Mar-2015.
3. Chairman, National level conference titled "National Conference on Science, Engineering and Technology" conducted by VIT chennai from 07-Nov-2016 to 08-Nov-2016.
4. Co Chairman, International level conference titled "4th International conference on "Signal Processing, Communication and Networking (ICSCN 2017)" conducted by Madras Institute of Technology from 23-Mar-2017 to 25-Mar-2017.
5. Chairman, National level conference titled "National Level Technical Symposium" conducted by New Prince Shri Bhavani College of Engineering and Technology from 08-Sep-2018.

Programme Organized

1. Organiser, National level Short Course on "ASIC Design flow" from 01-Sep-2007 to 02-Sep-2007.

2. Organiser, National level Short Course on "Soft Skills Development" from 10-Mar-2008 to 28-Mar-2008.
3. Organiser, Resource Person, National level workshop on "Amplifier Design" from 19-Oct-2009.
4. Organiser, Resource Person, National level seminar on "Analog IC Design" from 14-Sep-2012 to 15-Sep-2012.
5. Organiser, National level workshop on "Image processing frame work using FPGA" from 15-Oct-2013 to 16-Oct-2012.
6. Coordinator, National level Short Course on "FDTP on VLSI Design" from 09-Dec-2013 to 15-Dec-2013.
7. Coordinator, National level workshop on "Embeeded System Design Using ARM Controller" from 02-Sep-2014.
8. Coordinator, National level workshop on "VLSI System Design (RTL-GDSII)" from 22-Sep-2014.
9. Coordinator, National level workshop on "Digital and Custom IC Design" from 17-Oct-2014 to 18-Oct-2014.
10. Working Committee Member, International level conference on "3rd International conference on "Signal Processing, Communication and Networking (ICSCN 2015)" from 26-Mar-2015 to 28-Mar-2015.
11. Coordinator, International level workshop on "UGC sponsored workshop on Embedded SoC Design" from 07-Mar-2016 to 08-Mar-2016.
12. Workshop Coordinator, National level Short Course on "CMOS, Mixed Signal and Radio Frequency VLSI Design" from 30-Jan-2017 to 04-Feb-2017.
13. Active member, International level conference on "4th International Conference on Signal Processing, Communications and Networking" from 16-Mar-2017 to 17-Mar-2017.
14. Coordinator, National level workshop on "Advanced Digital Design Flow using CADENCE" from 01-Mar-2018 to 03-Mar-2018.

Programme Attended

1. Participated in a National level workshop on "Pattern Recognition" organized by Madras Institute of Technology, Anna University, Chennai, India from 19-Mar-2010 to 19-Mar-2010.
2. Attended a International level Short Course on "Floorplanning, Physical synthesis, Place and Route (Flat and Hierarchical)+Low power implementation" organized by KTH Royal Institute of Technology, Sweden from 20-Sep-2010 to 28-Sep-2010.
3. Attended a National level Short Course on "Advances in wireless Communication and networking Technologies" organized by National Institute of Technology, Calicut, India from 13-Jun-2011 to 24-Jun-2011.

4. Attended a National level Short Course on "Basic Electronics" organized by Indian Institute of Technology, Bombay, India from 28-Jun-2011 to 08-Jul-2016.
5. Attended a National level Short Course on "Digital Signal Processing" organized by Madras Institute of Technology, Anna University, C, India from 28-Nov-2011 to 04-Dec-2011.
6. Attended a National level Short Course on "Robotics Technology for Manufacturing" organized by Madras Institute of Technology, Anna University, Chennai, India from 10-Dec-2011 to 23-Dec-2011.
7. Participated in a National level workshop on "One day awareness course on ISO 9001:2008" organized by Madras Institute of Technology, Anna University, India from 12-Mar-2013.
8. Participated in a National level workshop on "One day awareness programme for teaching faculty on internal quality assurance" organized by Madras Institute of Technology, Anna University, India from 14-Mar-2013.
9. Attended a National level Short Course on "CMOS VLSI Design" organized by NIT-Calicut, India from 23-Jun-2013 to 29-Jun-2013.
10. Attended a National level Short Course on "FDP on Microprocessor and Microcontroller" organized by Madras Institute of Technology, Anna University, India from 08-Jun-2015 to 14-Jun-2015.
11. Participated in a International level workshop on "Hands-on Digital Signal Processing" organized by ARM University Program, , Hyderabad, India from 20-Aug-2015 to 21-May-2015.
12. Participated in a National level workshop on "Two Days workshop on Advancement in Biomedical Signal and Image Processing" organized by MIT Campus, India from 29-Oct-2015 to 30-Oct-2015.
13. Attended a National level Short Course on "One Week ISTE STTP for Coordinators on CMOS, Mixed Signal and Radio Frequency VLSI Design" organized by IIT-Kharagpur, India from 19-Sep-2016 to 23-Sep-2016.
14. Participated in a National level workshop on "High Frequency Design using ADS" organized by Department of Electronics Engineering, MIT Campus, Anna University, India from 11-Aug-2017 to 11-Aug-2017.
15. Attended a National level Short Course on "Professional Ethics and Values" organized by NITTTR, Chennai, India from 27-Nov-2017 to 01-Dec-2017.
16. Attended a National level Short Course on "Personality Development and NLP" organized by National Institute of Technical Training & Research, India from 20-Aug-2018 to 25-Aug-2018.

Honours

1. "The Institution of Engineers " given by The Institution of Engineers from India (1999).

Experience Abroad

1. Visited KTH-Royal Institute of Technology, Sweden from 04-Jun-2010 to 25-Mar-2011.
Purpose of visit :Post-Doc.

Invited Lectures

1. Delivered a Lecture on "Embedded systems Designs and Applications" in AICTE sponsored Staff Development Program organized by Lourdes Matha College of Science and Technology , Thiruvananthapuram, Kerela (03-Dec-2008).
2. Delivered a Lecture on "Amplifier Concepts" in One day workshop organized by Madras Institute of Technology, Chennai (19-Oct-2009).
3. Delivered a Lecture on "Electronic Circuits-II" in Faculty Development Program organized by RMK Engineering College, , Chennai (30-Nov-2009).
4. Delivered a Lecture on "Feedback Amplifiers" in Student Development organized by Veltech Multitech Dr.Rangarajan Dr.Sakunthala Engi, Chennai (03-Feb-2010).
5. Delivered a Lecture on "Challenges in Analog IC Design" in Analog IC Design organized by Madras Institute of Technology, Anna University, Chennai (14-Sep-2012).
6. Delivered a Lecture on "Analog VLSI Design" in TEQIP funded Residential Faculty Development Program on "EC2354 VLSI Design" organized by College of Engineering, Anna University, Checkers Hotel, Chennai (26-Jul-2013).
7. Delivered a Lecture on "CMOS INVERTER DESIGN" in One week FDP on VLSI Design organized by Madras Institute of Technology, Anna University, Chromepet (10-Dec-2013).
8. Delivered a Lecture on "IC Amplifiers" in FDP on Electronic Circuits-I organized by Dhanalakshmi College of Engineering, Chennai (20-Jun-2014).
9. Delivered a Lecture on "IC amplifiers" in FDTP on Electronic Circuits-I organized by College of Engineering, Chennai (27-Jun-2014).
10. Delivered a Lecture on "Challenges in Analog IC design" in Conference organized by Anna University, Tirunelveli, Tirunelveli, Tamilnadu, India (19-Mar-2015).
11. Delivered a Lecture on "Challenges in Analog IC Design" in Guest Lecture organized by KCG College of Technology, KCG College of Technology, Karapakkam, Chennai (14-Jul-2016).
12. Delivered a Lecture on "ADVANCED VLSI DESIGN TECHNIQUES" in keynote Address organized by VINAYAKA MISSIONS UNIVERSITY, Chennai (04-Aug-2016).
13. Delivered a Lecture on "Electronic Circuits Design" in Basic Concepts in Electronics and Communication Lab Experiments organized by Madras Institute of Technology/Department of Elect, Chennai (27-Jun-2017).
14. Delivered a Lecture on "MOS Transistor Principles" in Seven Days FDTP on VLSI Design-EC6601 organized by Madras Institute of Technology/Center for Faculty , Chennai (20-Nov-2017).

15. Delivered a Lecture on "CMOS Layout and Stick Diagrams" in Seven Days FDTP on VLSI Design-EC6601 organized by Madras Institute of Technology/Center for Faculty , Chennai (23-Nov-2017).
16. Delivered a Lecture on "Digital Circuit Design" in Basic Concepts in Electronics and Communication Lab Experiments organized by Madras Institute of Technology/Department of Elect, Chennai (25-Jun-2018).
17. Delivered a Lecture on "Hardwares in Networking" in Guest Lecture organized by Valliammal Engineering College/Department of Elect, Chennai (16-Aug-2018).
18. Delivered a Lecture on "MOS Fabrication Techniques and CMOS Inverter" in Two week FDTP on VLSI Design-EC6601 organized by College of Engineering/Department of ECE, Chennai (16-Nov-2018).