



Premanand Chandramani

Professor, Dept of ECE,
SSN College of Engineering

RF Microelectronics

VLSI

Mixed-signal

Smart grid

| | All | Since 2015 |
|-----------|-----|------------|
| Citations | 448 | 52 |
| h-index | 8 | 4 |
| i10-index | 8 | 1 |

| TITLE | CITED BY | YEAR |
|--|----------|------|
| Description and evaluation of the FAST-Net smart pixel-based optical interconnection prototype MW Haney, MP Christensen, P Milojkovic, GJ Fokken, M Vickberg, ... Proceedings of the IEEE 88 (6), 819-828 | 92 | 2000 |
| Description and evaluation of the FAST-Net smart pixel-based optical interconnection prototype MW Haney, MP Christensen, P Milojkovic, GJ Fokken, M Vickberg, ... Proceedings of the IEEE 88 (6), 819-828 | 92 | 2000 |
| 16 x 16 vcsel array flip-chip bonded to cmos vlsi circuit AV Krishnamoorthy, KW Goossen, LMF Chirovsky, RG Rozier, ... IEEE Photonics Technology Letters 12 (8), 1073-1075 | 79 | 2000 |
| Cascaded electro-optic scanning of laser light over large angles using domain microengineered ferroelectrics DA Scrymgeour, A Sharan, V Gopalan, KT Gahagan, JL Casson, ... Applied Physics Letters 81 (17), 3140-3142 | 72 | 2002 |
| Multichip free-space global optical interconnection demonstration with integrated arrays of vertical-cavity surface-emitting lasers and photodetectors MW Haney, MP Christensen, P Milojkovic, J Ekman, P Chandramani, ... Applied Optics 38 (29), 6190-6200 | 50 | 1999 |
| Triggered receivers for optoelectronics VLSI AV Krishnamoorthy, RG Rozier, TK Woodward, P Chandramani, ... Electronics Letters 36 (3), 249-250 | 19 | 2000 |
| FAST-net optical interconnection prototype demonstration MW Haney, MP Christensen, P Milojkovic, JT Ekman, P Chandramani, ... Optics in Computing'98 3490, 568-571 | 13 | 1998 |
| High-speed free-space scalable switching network for parallel computing P Chandramani, JT Ekman, P Gui, X Wang, FE Kiamilev, K Driscoll, ... Optics in Computing 2000 4089, 578-583 | 10 | 2000 |
| FAST-Net optical interconnection prototype demonstration program MW Haney, MP Christensen, P Milojkovic, JT Ekman, P Chandramani, ... Optoelectronic Interconnects V 3288, 194-203 | 10 | 1998 |
| Multichannel/spl plusmn/1.1-kV arbitrary waveform generator for beam steering using ferroelectric device F Muhammad, P Chandramani, J Ekman, F Kiamilev, D Scrymgeour, ... IEEE Photonics Technology Letters 14 (11), 1605-1607 | 8 | 2002 |

| TITLE | CITED BY | YEAR |
|--|----------|------|
| Gigabit switch using free-space and parallel optical data links for a PCI-based workstation cluster J Ekman, P Chandramani, P Gui, X Wang, F Kiamilev, M Christensen, ... LEOS 2000. 2000 IEEE Annual Meeting Conference Proceedings. 13th Annual ... | 7 | 2000 |
| System design and packaging for an optically interconnected mcm switch for parallel computing J Ekman, F Kiamilev, G Fokken, M Vickberg, P Gui, M Haney, K Driscoll, ... Advances in Electronic Packaging, 1643-1650 | 6 | 2001 |
| Component technology and system demonstration using smart pixel arrays based on integration of VCSEL/photodetector arrays and Si ASICs Y Liu, EM Strzelecka, J Nohava, E Kalweit, H Chanhvongsak, T Marta, ... SPIE Photonics West Symposia | 6 | 2000 |
| FAST-Net optical interconnection prototype demonstration MW Haney, MP Christensen, P Milojkovic, J Ekman, P Chandramani, ... Journal of Optics A: Pure and Applied Optics 1 (2), 228 | 5 | 1999 |
| Hardware-software co-design framework for sum of absolute difference based block matching in motion estimation KRS Chandran, PV Chandramani Microprocessors and Microsystems 74, 103012 | 4 | 2020 |
| Study and analysis of DR-VCO for rad-hardness in type II third order CPLL KA Karthigeyan, PV Chandramani Microelectronics Reliability 82, 190-196 | 4 | 2018 |
| Run-Time Reconfiguration of Processing Elements through Soft-Core Processor PC R.Nithya, K.R.Sarath Chandran International Conference on Communications and Signal Processing (ICCSP ... | 4 * | 2014 |
| RASCor: realtime associative stereo correspondence V Simhadri, P Chandramani, Y Ozturk 2007 IEEE International Conference on Image Processing 6, VI-197-VI-200 | 4 | 2007 |
| Design of a multigigabit optical network interface card P Chandramani, P Gui, J Ekman, X Wang, F Kiamilev, M Christensen, ... IEEE Journal of selected topics in quantum electronics 9 (2), 636-646 | 4 | 2003 |
| Compact multichannel 2 kV arbitrary waveform generator for ferroelectric device arrays F Muhammad, P Chandramani, JT Ekman, FE Kiamilev, V Gopalan, ... LEOS 2001. 14th Annual Meeting of the IEEE Lasers and Electro-Optics Society ... | 4 | 2001 |
| Automated design of ICs with area-distributed I/O pads R Rozier, R Farbarik, F Kiamilev, J Ekman, P Chandramani, ... 1998 IEEE/LEOS Summer Topical Meeting. Digest. Broadband Optical Networks ... | 4 | 1998 |
| A system architecture for use with free space optical interconnects in a 3D stacked processor environment J Roril, P Marchand, P Chandramani, J Ekman, F Kiamilev, F Zane, ... 1998 IEEE/LEOS Summer Topical Meeting. Digest. Broadband Optical Networks ... | 4 | 1998 |

| TITLE | CITED BY | YEAR |
|--|----------|------|
| Demand response management system with discrete time window using supervised learning algorithm GK Chellamani, PV Chandramani Cognitive Systems Research 57, 131-138 | 3 | 2019 |
| Comparison of tunability and phase noise associated with injection locked three staged single and differential ended VCOs in 90nm CMOS J Johnson, M Ponnambalam, PV Chandramani 2017 Fourth International Conference on Signal Processing, Communication and ... | 3 | 2017 |
| Phase displacement study in MOSFET based ring VCOs due to heavy-ion irradiation using 3D-TCAD and circuit simulation M Ponnambalam, N Vinodhkumar, R Srinivasan, PV Chandramani Microelectronics Reliability 65, 27-34 | 3 | 2016 |
| Spur reduction technique for fractional-N frequency synthesizer with MASH 1-1-1-1 Sigma Delta modulator V Tamilselvan, M Ponnambalam, PV Chandramani 2014 International Conference on Communication and Signal Processing, 895-899 | 3 | 2014 |
| Injection locked differential ring VCO M Ponnambalam, P Chandramani 2013 IEEE Conference on Information & Communication Technologies, 424-427 | 3 | 2013 |
| High-voltage CMOS arbitrary waveform generator for linear SLM array P Chandramani, F Muhammad, J Ekman, F Kiamilev, E Moore, M Weiler LEOS 2001. 14th Annual Meeting of the IEEE Lasers and Electro-Optics Society ... | 3 | 2001 |
| Automated design of integrated circuits with area-distributed input-output pads R Rozier, R Farbarik, F Kiamilev, J Ekman, P Chandramani, ... Applied optics 37 (26), 6140-6150 | 3 | 1998 |
| Time-interleaved pipeline ADC in transceivers for 60GHz applications BG Sai, P Chandramani 2013 IEEE Conference on Information & Communication Technologies, 420-423 | 2 | 2013 |
| Design of a multigigabit optical network interface card C Premanand, P Gui, E Jeremy IEEE Journal of Selected Topics in Quantum Electronics 9 (2), 411-416 | 2 | 2003 |
| Programmable network interface for parallel optical data links F Kiamilev, P Chandramani, P Gui, J Ekman, B Vanvoorst, F Rose, ... Conference Digest. 2000 Conference on Lasers and Electro-Optics Europe (Cat ... | 2 | 2000 |
| FAST-Net optical interconnection module design and development MW Haney, MP Christensen, P Milojkovic, J Rieve, J Ekman, ... Optics in Computing, OFA6 | 2 | 1999 |
| Optical Computing-Multichip free-space global optical interconnection demonstration with integrated arrays of vertical-cavity surface-emitting lasers and photodetectors MW Haney, MP Christensen, P Milojkovic, J Ekman, P Chandramani, ... Applied Optics 38 (29), 6190-6200 | 2 | 1999 |

| TITLE | CITED BY | YEAR |
|---|----------|------|
| An optimized methodical energy management system for residential consumers considering price-driven demand response using satin bowerbird optimization GK Chellamani, PV Chandramani Journal of Electrical Engineering & Technology 15 (2), 955-967 | 1 | 2020 |
| Wideband inter-digitated hairpin resonating BPF with multiple CSRRs as DGS for harmonic suppression M Ponnambalam, PV Chandramani Analog Integrated Circuits and Signal Processing 98 (1), 181-186 | 1 | 2019 |
| Improving HRR in 3P–8P harmonic rejection mixer using modified input transconductance stage in hard switching mixer M Ponnambalam, M Kanaga, PV Chandramani Analog Integrated Circuits and Signal Processing 91 (1), 1-7 | 1 | 2017 |
| Spur reduction technique for integer-N frequency synthesizer T Musheer, P Chandramani 2013 IEEE Conference on Information & Communication Technologies, 414-419 | 1 | 2013 |
| CMOS DSP and microprocessor cores using optoelectronic VLSI FE Kaimilev, J Ekman, P Chandramani, AV Krishnamoorthy, RG Rozier 2000 Southwest Symposium on Mixed-Signal Design (Cat. No. 00EX390), 9-13 | 1 | 2000 |
| Design of CMOS ICs for flip-chip integration with optoelectronic device arrays P Chandramani, FE Kiamilev, AV Krishnamoorthy, RG Rozier, JT Ekman, ... Optoelectronic Integrated Circuits and Packaging III 3631, 198-204 | 1 | 1999 |
| Fast-net optical interconnection prototype characterization MW Haney, MP Christensen, P Milojkovic, J Rieve, J Ekman, ... InterPACK'99: Pacific RIM/ASME International Intersociety Electronics ... | 1 | 1999 |
| A high speed CMOS scanner chip to be flip-chip bonded with MQW modulators PV Chandramani University of North Carolina at Charlotte | 1 | 1999 |
| Excess loop delay compensation using RZ DAC for the third order CT $\Delta\Sigma$ modulator with CRFB-FF structures PE Pushpam, JA Twinkle, PV Chandramani 2019 2nd International Conference on Power and Embedded Drive Control ... | | 2019 |
| Energy-efficient system-on-chip reconfigurable architecture design for sum of absolute difference computation in motion estimation process of H. 265/HEVC video encoding KRS Chandran, PV Chandramani Concurrency and Computation: Practice and Experience, e5461 | | 2019 |
| Implementation and verification of MASH 1-1-1 for fractional-N frequency synthesizer in Zynq-7000 series SoC platform SM Perumal, KA Karthigeyan, PV Chandramani 2017 Devices for Integrated Circuit (DevIC), 825-829 | | 2017 |
| Design and analysis of a narrowband hairpin shaped bandpass filter M Nivethitha, M Ponnambalam, PV Chandramani 2017 International Conference on Innovations in Information, Embedded and ... | | 2017 |

| TITLE | CITED BY | YEAR |
|---|----------|------|
| A Two-Stage 0.18 μm CMOS Differential Low-Noise Amplifier with Integrated LC Balun for 2.4 GHz Applications JL Lakshmi, PV Chandramani Artificial Intelligence and Evolutionary Computations in Engineering Systems ... | | 2016 |
| Mining-Based Device Control for Home Automation CG Kumar, SJ Josephine, PV Chandramani Artificial Intelligence and Evolutionary Computations in Engineering Systems ... | | 2016 |
| Design and implementation of two stage 5-bit pipelined SAR ADC M Ponnambalam, PV Chandramani 2014 International Conference on Communication and Signal Processing, 977-980 | | 2014 |
| Injection locked Differential Ring VCO PC Maran Ponnambalam IEEE International conference on Information and Communication ... | | 2013 |
| Design of a Gigabit Optical Network Interface Card and Layout Methodology for High-Voltage Drivers in Large Arrays for Modulators and MEMS Devices P Chandramani DELAWARE UNIV NEWARK | | 2004 |
| CMOS Test Chip for Flip-Chip Integration with VCSEL and Photodetector Arrays P Chandramani, J Rieve, J Ekman, F Kiamilev, MP Christensen, ... Spatial Light Modulators and Integrated Optoelectronic Arrays, STuB4 | | 1999 |
| VLSI development of smart-pixel ICs: a hybrid DSP core and a multi-threaded programmable DSP J Ekman, P Chandramani, R Rozier, F Kiamilev, J Rorie, F Zane, ... 1998 IEEE/LEOS Summer Topical Meeting. Digest. Broadband Optical Networks ... | | 1998 |
| Naïve Bayes classifier enabled home energy management scheme for cost-effective end-user comfort M Khan, GK Chellamani, PV Chandramani Journal of Intelligent & Fuzzy Systems, 1-11 | | |
| DATAPATH DESIGN FOR EMBEDDED STEREO VISION DEVELOPMENT SYSTEM M Korkmaz, Y Ozturk, P Chandramani, K Stewart | | |
| EDUCATION AND CAREERS 2000 DAB Miller, Y Liu, EM Strzelecka, J Nohava, MK Hibbs-Brenner, E Towe, ... | | |