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LIST OF PUBLICATIONS

International Journals

1. **Prasannamoorthy, V.** and Devarajan, N. “Computational Fault Diagnosis Technique for Analog Electronic Circuits using Markov Parameters”, International Journal of Computer Science and Information Security, Vol. 8, No. 1, pp. 195-202, **2010**.
2. **Prasannamoorthy, V.**, Bharat Ram, R., Manikandan, V. and Devarajan, N. “Time Domain Analysis Based Fault Diagnosis Methodology for Analog Circuits – A Comparative Study of Fuzzy and Neural Classifier Performance”, International Journal of Comp. Science and Information Security, Vol. 8, No. 2, pp. 306-313, **2010**.
3. **Prasannamoorthy, V.** and Devarajan, N. “Fault Detection and Classification in Power Electronic Circuits using Wavelet Transform and Neural Network”, Journal of Computer Science, Science Publications, USA, Vol. 7, No. 1, pp. 95-100, **2011**.
4. **Prasannamoorthy, V.** and Devarajan, N. “Time Domain Technique for Fault Diagnosis of Analog Circuits with Flexible Accuracy Algorithm”, European Journal of Scientific Research, Vol. 51, No. 2, pp. 211-221, **2011**.
5. **Prasannamoorthy, V.** and Devarajan, N. “Frequency Domain Technique for Fault Diagnosis in Analog Circuits - Software and Hardware Implementation”, Journal of Theoretical and Applied Information Technology, Vol. 22, No. 2, pp. 107-119, **2010**.
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7. **Prasannamoorthy, V.** and Saravana Sundaram, S. “A Study on the Characteristics of FSTPI Fed Induction Motor with Input Active Boost Converter for Power Factor Correction” , Life Science Journal, Vol. 10, No. 7(s), pp. 809 – 813, **2013**.
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9. **Prasannamoorthy, V.** , Arunkumar, S. "MCKELM-IDS Efficient Feature Transformation and Optimal Feature Subset Selection Based Intrusion Detection Approach", Advances in Natural and Applied Sciences, Vol. 8(19), pp. 88-99, 2014.
10. **Prasannamoorthy, V.** , Gowthaman, E. and Akalya A. "Security Solution for Meta-Recognition", International Journal of Applied Engineering and Research, Vol.10, No.55, May 2015.
11. **Prasannamoorthy, V.** and Keerthiha A. "Modeling and Simulated Performance of Five Phase Variable Speed Induction Motor for Solar Powered Applications", International Journal of Applied Engineering Research, Vol. 10, No. 55, pp. 3629 – 3634, May 2015.
12. **Prasannamoorthy, V.** and Nabeel Ibrahim "A Novel Maximum Power Point Tracking Technique for Photovoltaic Module by Using MPP Loci Algorithm" , International Journal of Applied Engineering Research, Vol. 10, No. 55, pp. 3571 – 3574, May 2015.
13. **Prasannamoorthy, V.** , Sundaramoorthy, P. and Merin Jacob "A Novel Multilevel Inverter Employing Additive and Subtractive Topology", Circuits and Systems , Vol. 7, pp. 2425-2436, July 2016.
14. **Prasannamoorthy, V.** , Sundaramoorthy, P. and Kathiravan, R. "Design and Development of Sag and Swell in Hybrid Compensator for Voltage Flickering in Grid Loads", Journal of Web Engineering, Vol. 17, No. 6, pp. 2333 – 2340, July 2018.
15. **Prasannamoorthy, V.** , Garikapati Narendra "Performance Comparison of Solar PV and PVT Systems", International Science and Innovative Engineering and Technology, Vol. 4, pp. 141 - 147, ISBN 978-93-81288-18-4, May 2019.
16. **Prasannamoorthy, V.** , Al Ameen, M "Phase Shifted Full Bridge Converter with Hybrid Energy Storage for Electric Vehicles", International Research Journal of Engineering and Technology, Vol. 6, Issue No. 5, pp. 7080 – 7084, e-ISSN: 2395-0056, p-ISSN: 2395-0072, May 2019.
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19. **Prasannamoorthy, V.** , Nandhini, R.. “Design of Multilevel Inverter using Neutral Point Clamped Inverter with Reduced Part Count”, International Journal of Scientific Research in Engineering and Management, ISSN: 2582 - 3930, Vol. 04, Issue No. 02, pp. 1 - 5, Feb 2020.
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