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Area of interest: VLSI Design and Testing, Communication Systems

PAPERS PUBLISHED IN INTERNATIONAL JOURNALS & NATIONAL JOURNALS:

- 1. Ramesh Jayabalan and Govindaraj Vellingiri (2017), "Adaptive neuro fuzzy inference system-based power estimation method for CMOS VLSI circuits" International Journal of Electronics, Taylor and Francis (Impact Factor 0.729) Volume 105, Issue 3, July 2018, PP.398-411.
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- 3. Ramesh Jayabalan and Michaelraj Kingston Roberts, (2016), 'An Improved Self Adaptive Min-Sum Decoding Algorithm for Flexible Low-Density Parity-Check Decoder' National Academy Science Letters, November 2016, Springer (Impact Factor 0.292) (ISSN: 0250-541X)
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- 5. Ramesh Jayabalan and Michaelraj Kingston Roberts, (2015), 'A Modified Optimally Quantized Offset Min-Sum Decoding Algorithm for Low Complexity LDPC Decoder', Wireless Personal Communications (Springer), Vol.80, Issue No.2, pp.561-570 (Impact Factor: 0.979) (ISSN: 0929-6212).
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- 8. Ramesh Jayabalan and Michaelraj Kingston Roberts, (2015), 'An Improved Low Complex Hybrid Weighted Bit-Flipping Algorithm for LDPC Codes', Wireless Personal Communications (Springer), Vol.82, Issue No.1, pp.327-339 (Impact Factor: 0.979) (ISSN: 0929-6212).
- 9. Ramesh J., Michaelraj Kingston ROBERTS & S. Finney Daniel (2013), "Performance Analysis of Steepest Descent Decoding Algorithm for LDPC Codes", International Journal on Recent Trends in Engineering & Technology, vol. 4, no 2, pp. 112-117.
- 10. **Ramesh J.** and Gunavathi K. (2007), 'A Novel Built-In Self-Test Architecture for Charge-Pump Phase Locked Loops', International Journal on Programmable Devices, Circuits and Systems (ICGST-PDCS), Vol. 7, Issue 1, pp.1-6.
- 11. **Ramesh J.**, Vanathi P.T and Gunavathi K. (2008), 'Fault Classification in Phase Locked Loops using Back Propagation Neural Networks', International Journal of Soft Computing Applications (IJSCA), No. 3, pp. 77-95.
- 12. **Ramesh J.**, Gunavathi K. (2009), 'A Novel Linear Ramp Generator for Analog and Mixed Signal Built-In Self-Test Applications', International Journal of Electrical Engineering and Embedded Systems, Issue 1, pp. 21-32.
- 13. **Jayabalan Ramesh**, Vanathi P.T and Gunavathi K. (2008), 'Fault Classification in Phase Locked Loops using Back Propagation Neural Networks', Electronics and Telecommunication Research Institute (ETRI) Journal, Vol. 30, No. 4, pp. 546-554. (Impact Factor 0.97)
- 14. Ramesh Jayabalan and Michaelraj Kingston Roberts, (2014), 'A Modified Normalized Min-Sum Algorithm for Irregular LDPC codes', International Journal of Engineering and Technology (IJET), vol.5, no.6, pp. 4881-4893, (ISSN: 0975-4024).
- 15. **Ramesh J.** and Dr. K. Gunavathi (2009), 'A Novel Current Mirror for On-Chip Ramp Generation', National Journal of Technology, Vol. 5, No.2, pp. 12-23.
- 16. **Ramesh J.** and Arun Kumar Madhuvappan (**2015**), "Enhanced TZ search Algorithm for HEVC Encoders" Accepted for publication in the Journal of Computation and Theoretical Nanoscience, Volume 14, No. 2, February-2014 ISSN: 2229-5518 Page No.: 1048 1054.
- 17. **Ramesh J.** and Arun Kumar Madhuvappan (**2014**), "Video Compression Motion Estimation Algorithms A Survey", International Journal of Scientific & Engineering Research, Volume 5, Issue 2, February-2014 ISSN: 2229-5518 Page No.: 1048 1054.
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- 19. **Ramesh J** and B. Vinodh Kumar (2016) "Improved Automatic CAN protocol based on Payload reduction and selective bit stuffing' Circuits and Systems Journal, Vol.7, No.10, August, 2016, 3415-3429.

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- 21. **J. Ramesh**, K.Thiruvenkadam and Anjali S.Pillai (2018), "Area- Efficient Dual-Mode Fused Floating-Point Three Term Adder" **Accepted for publication** in Journal of Circuits, Systems and Signal Processing, Springer (Impact Factor 1.694).
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- 23. **J.Ramesh** and Dhanasekar.S, (2015), "FPGA Implementation of Variable Bit Rate 16 QAM Transceiver System", International Journal of Applied Engineering Research, ISSN 0973-4562 Volume 10, Number 10 (2015) pp. 26497-26507.
- 24. **B. Vinodh Kumar, Dr. J. Ramesh**, 2015, "Clustering based selective XOR technique to reduce transmission delay in CAN", Research India Publications, International Journal of Applied Engineering Research, ISSN 2153 1293. ISSN 0973-4562, vol. 10, no.29, April, pp. 22538 22541. (Annexure II)
- 25. **J.Ramesh** and Sanjana Prasad, **2019.** "PAPR reduction in OFDM using scaled particle swarm optimization based partial transmit sequence technique" Journal of Engineering, IET Journals, eISSN 2051-3305
- 26. **J.Ramesh,** T.Mythili and Dr. P. Ramanathan, **2019**, "Innovative Localization Algorithm using the Line of Intersection Technique in Wireless Sensor Networks" Journal of Internet Technology, Vol. 5, pp. 3460-3468.
- 27. Sanjana Prasad, Dr. J. Ramesh, 2020, "PAPR reduction in OFDM system using Modified SLM with different phase sequences" Journal of wireless personal communication, 110(2), 913-928.

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International Conference: 12

- 1. **Ramesh J.** and Dr. K.Gunavathi K. (2007), 'A 8-bit TIQ based 780 MSPS CMOS Flash A/D Converter" Proceedings of International Conference on Computational Intelligence and Multimedia Applications (ICCIMA), Sivakasi, pp. 201-205.
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- 4. **Ramesh J.**, S.Dhanasekar, P.T.Vanathi and S.Radharani (2010), 'Speckle Noise Reduction in Ultrasound Images using Adaptive Filters', Proceedings of 4th International Conference on Intelligent Systems and Control, Karpagam College of Engineering, Coimbatore.
- 5. **Ramesh J.**, Dr. P.T.Vanathi, Dr. K.Gunavathi and S.Aravind Kumar, (2011), "Robust and Automated Lung Nodule diagnosis from CT images based on Fuzzy Systems" Proceedings of the IEEE **International conference** on Process Automation control and computing, Coimbatore.
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- 7. **Ramesh J.**, Dr. K.Gunavathi, S. Ashwin and S.Aravind Kumar, (2012) "Novel and Secure Encoding and Hiding techniques using Image Stegnography: A Survey" has been accepted for publication in the proceedings of ICETEEEM 2012, International conference on emerging trends in Electrical Engineering and Energy Management, Chennai.
- 8. **Ramesh J** & Michaelraj Kingston ROBERTS (2013), "A reduced complexity FFT based Sum-Product decoding algorithm for LDPC codes", International Conference on intelligent and efficient electrical systems (ICIEES'13), PSG College of Technology, Coimbatore, INDIA.
- 9. **Ramesh J** & Govindaraj V (2013), "Power minimization by Prims algorithm based reordering", **International Conference** on intelligent and efficient electrical systems (ICIEES'13), PSG College of Technology, Coimbatore, INDIA.
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- 11. **Ramesh J** and Sanjana Prasad, (2017), "Peak to Average Power Ration reduction techniques in OFDM systems" ICMR sponsored IEEE international conference on Science, Technology, Engineering and Management (ICSTEM'17), 3rd 4th March 2017, Kalaignar Karunanidhi Institute of Technology, Coimbatore.
- 12. **Ramesh J** and Sanjana Prasad, (2017), "Scaled offset PSO based PTS for PAPR reduction in OFDM systems" Accepted for presentation in 8th IEEE Annual Ubiquitous Computing, Electroncis and Mobile communication conference (IEEE UEMCON 2017) Columbia University, New York, USA to be held on 19th -21st, Ocotber 2017.
- 13. **Ramesh J** and Dhanasekar.S, 2017 "FPGA Implementation of Variable Bit rate OFDM Transceiver system for Wireless applications", International Conference on Innovations in Electrical, Electronics, Instrumentationand Media Technology ICIEEIMT 2017, Karunya university, Coimbatore. pp. 343-346. DOI: 10.1109/ICIEEIMT.2017.8116863

14. **J. Ramesh** and B. Vinodh Kumar, "Automotive in vehicle network protocols," 2014 International Conference on Computer Communication and Informatics, Coimbatore, 2014, IEEE Xplore: 16 October 2014 pp. 1-5. doi: 10.1109/ICCCI.2014.6921836

National Conference: 34

- 1. **Ramesh J.**, Sanjana Prasad, G. Dhanaprakash (2018), "PAPR Reduction in OFDM system using selective Mapping Technique with Riemann Sequence" National Conference on "VLSI Design Communication and Nano Technologies (VDCNT'18), PSG College of Technology, Coimbatore during 23rd 24th, March 2018.
- 2. **Ramesh J.**, R. Anitha & Michaelraj Kingston.R (2013), "Multi Layer Perceptron Neural Network Based Decoder for LDPC codes", DRDO Sponsored Second National Conference on "Communication Technology Interventions for Rural and Social Development" Sri Krishna College of Engineering and Technology, Coimbatore during 14th -15th, February 2013.
- 3. **Ramesh J.**, Finney Daniel, R. Anitha, Michaelraj Kingston.R (2013), "Performance Analysis of LDPC Iterative Decoding Techniques for IEEE 802.16e", National Conference on Advanced Computing and Communication Systems" held at Government College of Technology, Coimbatore on 26th April 2013.
- 4. **Ramesh J.**, Finney Daniel, Michaelraj Kingston.R (2013), "Performance Analysis of Steepest Descent Decoding Algorithm for LDPC Codes", National Conference on Advanced VLSI, Image Processing and Communication Systems Einstein College of Engineering, Tirunelveli during 4th 5th April 2013. (**Received the Best Paper Award**)
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- 7. **Ramesh J** & Michaelraj Kingston ROBERTS (2013), "Performance analysis on effective encoding algorithms for LDPC codes", 6th National Conference on Signals, Systems and Security (NCSSS-2014), BANNARI AMMAN INSTITUTE of Technology, Coimbatore, INDIA.
- 8. **Ramesh J** & Michaelraj Kingston ROBERTS (2013), "Design and development of effective decoding algorithms for LDPC codes", 6th National Conference on Signals, Systems and Security (NCSSS-2014), BANNARI AMMAN INSTITUTE of Technology, Coimbatore, INDIA.

- 9. **Ramesh J** & Govindaraj.V (2013), "An Efficient LFSR seed selection for test power minimization in VLSI Circuits" Fourth National Conference on Recent Trends in Communication Computation and Signal Processing, Amrita University, *Coimbatore, INDIA*.
- 10. **Ramesh J** & Govindaraj.V (2013), "An Efficient BIST architecture for C17 Benchmark circuit" National conference on VLSI. Communication & Wireless *Technologies*, PSG College of Technology, *Coimbatore, INDIA*.
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- 12. **Ramesh J**., Michaelraj Kingston ROBERTS & C. Deepika (2014), "Design of decoder architecture for IEEE 802.15.3c LDPC codes", National Conference on Research Challenges in Wireless Communication Systems and VLSI Design (NCWCV-2014), PSG College of Technology, Coimbatore, INDIA. (**Received the Best Paper Award**)
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- 14. **Ramesh J**., Michaelraj Kingston ROBERTS & R. Praveen Kumar (2014), "Design and development of decoder architecture for IEEE 802.11n LDPC codes", 6th National Conference on Signal Processing, Communication and VLSI Design (NCWCV-2014), ANNA UNIVERSITY Regional Centre, Coimbatore, INDIA. (**Received the Best Paper Award**)
- 15. **Ramesh J**., Michaelraj Kingston ROBERTS & C. Deepika (2014), "Design and implementation of a decoder architecture for IEEE 802.15.3c LDPC codes", 6th National Conference on Signal Processing, Communication and VLSI Design (NCWCV-2014), ANNA UNIVERSITY Regional Centre, Coimbatore, INDIA. (**Received the Best Paper Award**)
- 16. **Ramesh J.** and Shoukath Ali (2012) 'FPGA Implementation of Coded OFDM systems', Proceedings of Second National Conference on Research Challenges in Communication Systems and VLSI Design (NCRCV'12), PSG College of Technology, Coimbatore
- 17. **Ramesh J.** and Shoukath Ali (2012) 'Power Reduction in Coded OFDM', Proceedings of National Conference on Communication Technology Interventions for Rural and Social Development (NCCTIRD-2012), Sri Krishna College of Engineering and Technology, Coimbatore.
- 18. **Ramesh J.,** Dr. P.T.Vanathi, Preethi, Revathi, Keerthana and Haritha Lakshmi (2012) 'Performance Analysis of High Performance Adder Architectures', Proceedings of National Conference on Communication Technology Interventions for Rural and Social Development (NCCTIRD-2012), Sri Krishna College of Engineering and Technology, Coimbatore
- 19. **Ramesh J.**, S.Dhanasekar (2011), 'Medical Image Segmentation using Edge Detection Techniques', Proceedings of First National Conference on Research Challenges in Communication Systems and VLSI Design (NCRCV'11), PSG College of Technology, Coimbatore.

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- 22. **Ramesh J.** and Gunavathi K. (2008), 'A Novel BIST architecture for Fault Diagnosis in TIQ Flash Analog to Digital Converters', Proceedings of National Conference on VLSI, Embedded Systems, Signal Processing and Communication Technologies (NCVESCOM), Arupadai Veedu Institute of Technology, Chennai, pp. 10-16.
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- 24. **Ramesh J.** and Gunavathi K. (2008), 'Performance Analysis of Phase Locked Loops with on-chip Loop Filters', Proceedings of National Conference on VLSI for Communication, Computation and Control, Karunya University, Coimbatore.
- 25. **Ramesh J.** and Gunavathi K. (2008), 'Statistical Method of Fault Classification in Phase Locked Loops', Proceedings of National Conference on Signals Systems and Security (NCSSS), Bannari Amman Institute of Technology, Sathyamangalam. (**Received the Best Paper Award**)
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- 29. **Ramesh J.** and Vijaya Kumar P. (2006), 'Design and Simulation of Data Converters', Proceedings of Second National Conference on VLSI Design (NCVD), PSG College of Technology, Coimbatore, pp. 287-298.
- 30. **Ramesh J.** and Gunavathi K. (2006), 'Ramp Generators for Analog and Mixed Signal BIST architectures', Proceedings of Second National Conference on VLSI Design (NCVD), PSG College of Technology, Coimbatore, pp. 116-125.
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- 33. **Ramesh J.** and Gunavathi K. (2003), 'VLSI Design of High Resolution Analog to Digital Converters using Sigma Delta Modulators', Proceedings of National Conference on VLSI Design and Test (NCVDAT), PSG College of Technology, Coimbatore, pp. 115-124.
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- 35. **Ramesh J.** and Gunavathi K. (2003), 'A Novel High Performance Second order Sigma Delta Modulators for 1.28 MSPS Analog to Digital Converter', Proceedings of 27th National Systems Conference (NSC), Indian Institute of Technology, Kharagpur, pp. 431 435. (**Received the Best Paper Award**)