

## LIST OF PUBLICATIONS

1. P Saravanan, P Kalpana , "[Performance Analysis of Reversible Finite Field Arithmetic Architectures Over GF \(p\) and GF \(2m\) in Elliptic Curve Cryptography](#)", Journal of Circuits, Systems and Computers 24 (08), 1550122,2015
2. P.Saravanan,P.Kalpana, "Design of SubBytes and InvSubBytes Transformations of AES Algorithm Using Power Analysis Attack Resistant Reversible Logic Gates" , Australian Journal of Basic and Applied Sciences,Jan 2015,pp-8-18.
3. P.Saravanan, P.Kalpana, " Performance analysis of energy efficient XOR gate implementation resistant to power analysis attacks" Journal of Engineering science and Technology,Ver 2013.2.1,2015
4. P. Saravanan & P. Kalpana, "A Novel Implementation of SRAM PUF for Secure Applications", International Journal of Applied Engineering Research, 658-662,2015
5. P.Saravanan,P.Kalpana, " A Novel approach to design A5/1 Stream cipher using power analysis attack resistant reversible logic gates" International Journal of Enterprise network management,Ver 2014.2,2016
6. P. Saravanan & P. Kalpana, "A Novel Approach to Attack Smartcards Using Machine Learning Method', Journal of Scientific and Industrial Research,vol 76,pp 95-99,2017.
7. Umapathi Krishnamoorthy, Meenakshi Sundaram Nachiappan, Kalpana Palanisamy, "[Investigation of the effect of finite-sized ions on the nanowire field-effect transistor in electrolyte concentration using a modified Poisson–Boltzmann model](#)" Physics and Chemistry of Liquids,Taylor & Francis, pp 231-240,2018
8. A.Uma, P. Kalpana and T. Naveen Kumar, "Design of DA-based FIR filter architectures using LUT reduction techniques,Lecture notes in Electrical Engineering,springer, 453,pp 221-230,2018.
9. P.Saravanan,P.Kalpana "[Novel Reversible Design of Advanced Encryption Standard Cryptographic Algorithm for Wireless Sensor Networks](#)", Wireless personal communications, pp 1-32,2018.
10. [Krishnamoorthy, U., Nachiappan, M., Palanisamy, K., "The impact of the modified Poisson–Boltzmann model on protein bound to a lipid coated silicon nanowire field effect transistor biosensor in an electrolyte environment](#), Physics and Chemistry of Liquids,Taylor & Francis,2019.

11. Mythili R,P.Kalpana, "[Comparative Analysis of Parameter Extractor for Low-Power Precomputation Based Content Addressable Memory](#), Wireless personal communications,2020.
12. Mythili R,P.Kalpana, "[High speed network intrusion detection system \(NIDS\) using low power precomputation based content addressable memory](#)" [Computers, Materials and Continua](#) ,62(3), pp. 1097-1107 ,2020
13. [Kamalakaran, S., Kalpana, P., "Medical data transmission using the product of TLDPC and BCH error control coding systems with two interleavers",International Journal of Communication Systems](#) ,33(12),e4439,2020
14. Ms.Lalitha kathambari,A.Uma,P.Kalpana "Design of Low power successive approximation ADC using segmented architecture", National Journal of Technology,2017