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PUBLICATION DETAILS

Journal Publication for the last five years: (Scopus Indexed)

Title	Year
S.P. Velmurugan, P. Sivakumar, M. PallikondaRajasekaran, "Multimodality Image Fusion using Centre-Based Genetic Algorithm and Fuzzy Logic" International Journal of Biomedical Engineering and Technology, Vol:24, Issue: 1	2018
. P. Sivakumar, Jenyfal Sampson, Nivetha , "Design the efficient SRAM circuit using 4 transistor with sleepy logic", International Journal of Pure and Applied Mathematics, Vol:118, Pg. No:115-123.	
S.P Velmurugan, Dr. P. Sivakumar, Dr.M. PallikondaRajasekaran, "Classification of Brain Tumor Using Multimodal Fused Images and PNN" International Journal of Pure and Applied Mathematics, Vol:115, Issue:6, Pg. No: 447-457.	2017
P.Manikandan, Dr.P.Sivakumar, Mrs.UmaMaheswari, "Study of Reconfigurable Antenna" International Journal of Electronics, Electrical and Computational System, The IEI, Telungana, Volume:6, Issue: 11.	
Baskar V, Aarthi N, JeyaPrakash K, Manikandan P, Sivakumar P, "Smart Class for Visually Challenged Persons" International Journal Of Innovative Research In Management, Engineering And Technology, Vol. 3, Issue 3.	2017
R.Radeep Krishna ,R.RajaSudharsanP.Siva Kumar, " Wire length and Area Reduction of ISPD-98 Benchmark Using Parallel Genetic Algorithm in Modern VLSI Chip Placement" , International Journal of Advanced Information Science and Technology, Vol 34, pp 48-51, Feb (Indexed in Thomson Reuters).	2016
Application of effective memetic algorithm for vlsi physical design P Sivakumar, Chennai	2014
Parallel memetic algorithm for VLSI circuit partitioning problem using graphical processing units P Subbaraj, P Sivakumar, V District, V District Journal of Computer Science 8 (5), 705	2012
R. Radeep Krishna; P. Siva Kumar; R. Raja Sudharsan, "Optimization of wire-length and block rearrangements for a modern IC placement using evolutionary techniques" IEEE International Conference on Intelligent Techniques in Control, Optimization and Signal Processing (INCOS),Pages: 1 – 4.	2017
K. Pandiaraj; P. Sivakumar; R. Sridevi, "Minimization of wirelength in 3d IC routing by using differential evolution algorithm", IEEE International Conference on Electrical, Instrumentation and Communication Engineering (ICEICE).	
P. Sivakumar; G. Karthy; K. Vidya Bharani, "Fault tolerant improvement mechanism for 3D memories using built-in self repair scheme", 2017 IEEE International Conference on Electrical, Instrumentation and Communication Engineering (ICEICE).	

K. Pandiaraj; P. Sivakumar; N. Geetharamani, "Reduction of temperature rise in 3D IC routing" 2017 IEEE International Conference on Electrical, Instrumentation and Communication Engineering (ICEICE)	
R. Rohini; Jenyfal Sampson; P. Sivakumar, "Comparison on 6T, 5T and 4T SRAM cell using 22nm technology" 2017 IEEE International Conference on Electrical, Instrumentation and Communication Engineering (ICEICE).	
R. Deepika; P. Manikandan; P. Sivakumar, "Optimization of pyramid horn antenna using genetic algorithm and evolution strategy", 2017 IEEE International Conference on Electrical, Instrumentation and Communication Engineering (ICEICE).	
P.Manikandan, Dr.P.Sivakumar, C.Swedheetha, "Design of Adaptive Frequency Reconfigurable Antenna For MIMO Applications", International Conference on Soft Computing In Data Analytics, Sri Sivani College Of Engineering, Srikakulam, Andhra Pradesh, Pg.No:16.	2016
P.Patchi Raja, M.PallikondaRajasekaran, P.Siva Kumar, R.Radeep Krishna, "Moving Object Detection and Image Quality Enhancement using Canny Edge Detection and Morphological Filtering", IEEE Sponsored 3rd International Conference On Electronics And Communication Systems (ICECS 2016) , 757-760, Feb.	2016
R.Radeep Krishna, R.RajaSudharsan ,P.Siva Kumar, " Wire length and Area Reduction of ISPD-98 Benchmark Using Parallel Genetic Algorithm in Modern VLSI Chip Placement" , International Conference on " VLSI Computation, Networking Drives, Image Processing and system Design, Mar-26	2016
Dr.P.Sivakumar, Mr.P.Manikandan, Dr.Manimegalai, Ms.Swedheetha C "Dual band, Dual Polarized Frequency Reconfigurable Antenna for SATCOM Application", Antenna Test & Measurement Society (ATMS India-16) 01-03 Feb, 2016	2016
Dr.P.Sivakumar, Mr.G.Karthy, Ms.K. Sangeetha " Reduction of area and power in input vector monitoring Concurrent BIST architecture" to be published in International Conference on Electronics and communication Systems (ICECS - 2016) on 25th and 26th February 2016.	
Dr.P.Sivakumar, Mr.G.Karthy, Ms.K. Sangeetha " Reduction of area and power in input vector monitoring Concurrent BIST architecture" to be published in International Conference on Electronics and communication Systems (ICECS - 2016) on 25th and 26th February 2016.	
Dr.P.Sivakumar, Mr.G.Karthy, Ms.K. Vidya Bharani "Fault tolerance improvement mechanism using BISR for 3D memories" is published in International Conference on Engineering Technology (ICET - 2016) held from 16th to 17th December 2016 at Karpagam college of Engineering , Coimbatore.	
Dr.P.Sivakumar, Mr.G.Karthy, Ms.K. Vidya Bharani "Fault tolerance improvement mechanism using BISR for 3D memories" is published in International Conference on Engineering Technology (ICET - 2016) held from 16th to 17th December 2016 at Karpagam college of Engineering , Coimbatore.	
M. PallikondaRajasekaran, P. Sivakumar, and K. JeyaPrakash "Improving Engineering Education through Outcomes Based Assessment: A Case Study " IEEE 4th International Conference on MOOCs, Innovation and Technology in Education. (MITE2016) , 9 th -10 th Dec, 2016.	
Dr.P.Sivakumar, Mr.P.Manikandan, G.M Keerthana , S.G.S.Manosri, G.Loganayagi "Design of Reconfigurable Antenna for L and S Band", International Conference on Modelling, Simulation and Control (ICMSC - 2015),Karpagam Engineering college, 15th oct, 2015.	2015