

DC MEMBER-3 (Affiliated to Anna University)

BALAKUMARAN.T
Assistant Professor,
Department of ECE
Coimbatore Institute of Technology,
Coimbatore -641014

Mobile:9944524451, E-Mail : balakumaran@cit.edu.in

PUBLICATIONS – INTERNATIONAL JOURNAL

1. S Vani, GR Suresh, T Balakumaran, Cross T Ashawise, —EEG Signal Analysis for Automated Epilepsy Seizure Detection Using Wavelet Transform and Artificial Neural Network, Journal of Medical Imaging and Health Informatics, Vol. 9, Issue 6, pp. 1301-1306, August 2019
2. Balakumaran, Marimuthu Gowrishankar, —Microcalcification cluster detection using multiscale products based Hessian matrix via the Tsallis thresholding scheme, Pattern Recognition Letters (Elsevier), Issue.94, pp. 127–133 , July 2017 (IF. 2.810) (Thomson Reuters)
3. S.Vani, G.R.Suresh, T.Balakumaran, —Automatic Recognition of Epilepsy Seizure by Using Lifting Based Wavelet Transform and Artificial Neural Network, International Journal of Printing, Packaging & Allied Sciences, Vol. 4, No. 6, December 2016
4. T.Balakumaran, D.Sasidharan, —Design of Microstrip Patch Antenna Array using Parasitic Coupling, International Journal of Computer Applications, National Conference on Information and Communication Technologies (NCICT 2015), No.1,2015

PAPER PUBLISHED IN INTERNATIONAL CONFERENCE

1. K.M.Priya, T.Balakumaran,, — Implementation of Dc-DC Boost converter with PIC Microcontroller for Electric Vehicles, International Conference on Research advancements in applied Engineering sciences, computer and communication technologies(ICRAACSCCC- 2018) conducted by BVRIT, Narsapur, Telangana on 12th and 13th July 2018.
2. M. Nikhil Theja, Dr.T.Balakumaran, —Energy Efficient Low Power High Speed Full Adder using Hybrid Logic, IEEE International Conference on Circuit, Power and Computing Technologies (ICCPCT), conducted by Noorul Islam University, Nagercoil on 18th and 19th March 2016.

PAPER PUBLISHED IN NATIONAL CONFERENCE

1. B.Varsha, Dr.T.Balakumaran, —Algorithmic Based Sign Detection and MAC Unit Design using Modified Booth Algorithm, as the best paper in the National Conference on Innovation and Emerging Technologies for Future Smart Cities (NCIETFSC'17),

conducted by Coimbatore Institute of Technology, Coimbatore on 6th and 7th April 2017.

2. M. Nikhil Theja, Dr.T.Balakumaran, —Efficient Full Adder Design for Low Power and High Speed using Hybrid Logicl, National Conference on Circuit, Networks and Systems (NCCNS), conducted by Coimbatore Institute of Technology, Coimbatore on 22nd April 2016