

1. Kokila J, Arjun Murali Das, Shameedha B, Ramasubramanian N, **"Hardware Signature Generation Using a Hybrid PUF and FSM model for an SoC Architecture"**, Periodica Polytechnica Electrical Engineering and Computer Science, 2019.
2. Bhukya Krishna Priya, Sampath Kumar, B Shameedha Begum, N Ramasubramanian, **"Cache lifetime enhancement technique using hybrid cache-replacement-policy"**, Microelectronics Reliability, Volume 97, pp-1-15, 2019.
3. B. Shameedha Begum, N. Ramasubramanian, **"Design of an Intelligent Data Cache with Replacement policy"**, IJERTCS, Volume 10(2).2019.
4. Ruchika, N. Ramasubramanian, B. Shameedha Begum, **"Credit Card Fraud Detection"**, International Conference on Computing and Telecommunication Engineering, Springer, DIEMS, 2019.
5. Krishna Priya, Sampath Kumar, N. Ramasubramanian, B. Shameedha Begum, **"Enhancing the Lifetime of STT-RAM with MRU Replacement Algorithm"**, International Conference on Recent Advances in Information Technology(RAIT) IEEE, IIT(ISM) Dhanbad, 2018.
6. Satyanarayana Vollala, B Shameedha Begum, Amit D Joshi, N Ramasubramanian, **"Bit Forwarding 3-Bits Technique for Efficient Modular Exponentiation"**, International Journal of Information Security and Privacy (IJISP), Volume 11(2), pp.11-24, 2017.
7. Amit D. Joshi, Indrajeet S, N. Ramasubramanian, B. Shameedha Begum, **"Analyzing Multi-core Cache Coherence Protocols from Energy and Performance Perspective"**, International Conference on Recent Innovation in Signal processing and Embedded System (RISE) IEEE, 2017.
8. Satyanarayana Vollala, Indrajeet S, B. Shameedha Begum, and N. Ramasubramanian, **"Evaluation of Password Encrypted Key Exchange authentication Techniques: Design approach perspective"**, International Conference on Internet of things and Machine Learning, John Moores **University Liverpool, UK**, 2017.
9. Vollala Satyanarayana, Ramasubramanian N, Begum, B S, Joshi A D, **"Dual core Implementation of Right to Left Modular Exponentiation"**, 5th International Conference on Advanced Computing, Networking and Informatics, Volume 1, pp. 43-53, Springer, 2017.
10. Shameedha Begum, T Vidya, Amit D. Joshi and N Ramasubramanian, **"A Reconfigurable Cache Design for Embedded Dynamic Data cache"**, IJCTA, Volume 9(17), pp. 8509-8517, 2016.
11. Satyanarayana Vollala, Shameedha Begum, Amit D. Joshi and N. Ramasubramanian, **"High-Radix Modular Exponentiation for Hardware Implementation of Public-Key Cryptography"**, IEEE International Conference on Computing, Analytics and Security Trends (CAST-2016), Pune, India, pp. December-2016. **(Awarded Best Paper)**
12. Shameedha Begum, Arun Krishnakumar, Amit D. Joshi and N Ramasubramanian, **"Design of a Reconfigurable Embedded Data Cache"**, IEEE International Conference on Computing, Analytics and Security Trends (CAST-2016), Pune, India, pp. ,December-2016.
13. Joshi, A. D., Vollala Satyanarayana., Begum, B. S., & Ramasubramanian N, **"Performance Analysis of Cache Coherence Protocols for Multi-core Architectures: A System Attribute Perspective"** ACM International Conference on Advances in Information Communication Technology & Computing, Bikanir, India, Article No. 22, August–2016.
14. Babysyla, Satheesh Kumar and Shameedha Begum, **"Defending against malwares: Sandbox Detection and Prevention of Malwares in Android Devices"**, JCSEITR, Volume 5(1), pp.7-20, 2015.
15. Satyanarayana Vollala, B. Shameedha Begum, and N. Ramasubramanian, **"Hardware design for multiplicative modular inverse based on table look up technique"**, IEEE International Conference on Computing and Network Communications (CoCoNet) Trivandrum, India, pp. 520-523, March – 2015.
16. Begum, B. Shameedha, and N. Ramasubramanian, **"A comparative study of cache performance for embedded applications"** IEEE International Conference on Computing and Network Communications (CoCoNet), Trivandrum, India, pp. 872-876, March - 2015.
17. B. Shameedha Begum, S. Vasatharathna, **"Leakage Current reduction on CMOS Devices using Gate replacement and Divide and Conquer Approach"**, Natioanl Conference on New Frontiers in Computing(NCONFIC-07), KSR College of Technology, India, March 2007. **(Awarded First Prize)**