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#### **Area of interest:**

High Performance Heterogeneous Computing, Information and Network Security

#### **Publications:**

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- 1. Arun Manoharan, Hemprasad Patil, Sujatha Rajkumar, Usharani Seshasayee, "Emerging Trends in Artificial Intelligence for Internet of Things", Department of Embedded Technology, Vellore Institute of Technology, ISBN-9789389640083, Vol. I, Dec 2019.
- 2. Arun Manoharan, "High Speed Signature Detection Architectures for Network Applications FPGA based Reconfigurable Architectures", LAP LAMBERT Academic Publishing, Germany, First Edition, 2012. (ISBN 978-3-659-24982-2) @Amazon.
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- 13. Anish Chandran, Arun Manoharan, "Optimization of Tridiagonal Matrix Algorithm [TDMA] on Multicore Architectures: Computational framework and mathematical modelling", International Journal of Grid and High Performance Computing, Volume 11, Issue 4, Article 1, 2019 (140917-052547).
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