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Publications Details

1. Pavithra Guru R. and **Vaithianathan V**, "An efficient VLSI circuit partitioning algorithm based on satin bowerbird optimization (SBO)", Journal of Computational Electronics (2020).
2. Shanthapriya R and **Vaithianathan V**, "Secured healthcare monitoring system in wireless body area network using the polynomial based technique", Polish Journal of Medical Physics and Engineering (2019).
3. J.Jegadish Kumar, S. Joseph Gladwin, V. Karthick and **V. Vaithianathan**, "Realization of AES with Cellular Automata-based S-Box for High-Speed Image Encryption", Asian Journal of Information Technology (2016)
4. **Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan (2012), 'A Low Power, High Gain, Low Noise Amplifier with Improved Noise Figure and Input Matching for Ultra Wide Band Applications', Iranian Journal of Science and Technology: Transactions of Electrical Engineering, Vol. 36, No. E2, pp 163-174.
5. **Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan (2012), 'A 2-12 GHz Ultra Wide Band CMOS Low Noise Amplifier with High Gain and Linearity', American Journal of Applied Sciences, Vol. 9, No.8, pp.1158 – 1165.
6. **Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan, A. Naveen Bharadwaj (2012), 'A Low Power Fully Digital Transmitter for Ultra Wide Band Impulse Radio', European Journal of Scientific Research, Vol.74 No.2, pp. 259-271, April 2012.
7. **Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan, M. Chandra Praveen (2012), 'Active Inductor Based Low Noise Amplifier for Ultra Wide Band Receiver', International Journal of Computer Applications, Vol. 41, No.15, pp: 53-60, March 2012.
8. **Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan, P. Aishwarya, B. Anupreethi, S. Divya (2012), 'A 3-14 GHz Low Noise Amplifier for Ultra Wide Band Applications', International Journal of VLSI Design & Communication Systems, Vol. 3, No. 1, pp 137-151.
9. **Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan (2012), 'A 1.7-2.5 GHZ Active Inductor Based Low Power Low Noise Amplifier for Multi-Standard Applications', International Journal of Computer Applications, Vol. 39, No.5, pp: 16-24.
10. Sharmila Devi and **Vaithianathan**(2015), '60GHz Millimetre Wave Low Noise Amplifier Using 65nm CMOS Technology', International Journal of Applied Engineering Research, Vol. 10, No. 21, pp: 20241-20249.
11. Lakshmi Priya and **V. Vaithianathan**(2015), 'Digitally Tunable Dual Band Low Noise Amplifier for Ultra Wide Band Applications', International Journal of Applied Engineering Research, Vol. 10, No. 21, pp: 20250-20255.
12. Anusha and **V. Vaithianathan**(2015), 'An Ultra Wide Band Low Noise Amplifier with an improved Group Delay', International Journal of Applied Engineering Research, Vol. 10, No. 21, pp: 20256-20261.
13. Vinothini, **V. Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan (2014), 'Reconfigurable LNA for MB-OFDM Receiver using Active Inductor', International Journal of Computer Applications, Vol. 89, No.18, pp: 22-28.
14. Keerthana, **V. Vaithianathan**, Dr. J. Raja, Dr. R. Srinivasan (2014), 'Analysis of Active Feedback and its Influence on UWB Low Noise Amplifiers', International Journal of Computer Applications, Vol. 89, No.18, pp: 29-35.

