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List of Publications:

1. **R.Saravana Ram**, M. Vinoth Kumar, S. Ramamoorthy, B. Saravana Balaji & T. Rajesh Kumar "An Efficient Hybrid Computing Environment to Develop a Confidential and Authenticated IoT Service Model", Wireless Personal Communication (2020), <https://doi.org/10.1007/s11277-020-07056-0>, Pages 1-25
2. M.Vinoth Kumar, **R.Saravana Ram**, Saravana Balaji .B, B.Amutha, M.Kowsigan," An improved migration scheme to achieve the optimal service time for the active jobs in 5G cloud environment", Computer Communications, Volume 160, 1 July 2020, Pages 807-814
3. **Saravana Ram R**,Vinoth Kumar M, Balambigai Subramanian, Nebojsa Bacanin, Miodrag Zivkovic, Ivana Strumberger"Speech enhancement through improvised conditional generative adversarial networks",Microprocessors and Microsystems, Volume 79, November 2020, 103281
4. **Saravana Ram, R.**, Gopi Saminathan, A. & Arun Prakash, S. "An Area Efficient and Low Power Consumption of Run Time Digital System Based on Dynamic Partial Reconfiguration". International journal of Parallel Programming ,48, 431–446 (2020).
5. **R. Saravana Ram**, M. Lordwin Cecil Prabhaker, K. Suresh, Kamalraj Subramaniam, M. Venkatesan, "Dynamic partial reconfiguration enhanced with security system for reduced area and low power consumption" Microprocessors and Microsystems, Volume 76, July 2020, 1-8
6. **R Saravana Ram**, S Arun Prakash, Muthu Balaanand, CB Sivaparthipan, "Colour and orientation of pixel-based video retrieval using IHBM similarity measure", Multimedia Tools and Applications. 79, pages10199–10214(2020)

7. M Lordwin Cecil Prabhaker, **R Saravana Ram**, “Real-Time Task Schedulers for a High-Performance Multi-Core System”, Automatic Control and Computer Sciences volume 54, pages291–301(2020).
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9. **R. Saravana Ram**, L.R. Marx, “Design and implementation of run time digital system using field programmable gate array–improved dynamic partial recon-figuration for efficient power consumption”, J. Comput. Theor. Nanosci. 13 (7) (2016) 4749–4755
10. **R Saravana Ram**, LR Marx, “Implementation of Energy Conserved VLSI System with Transducer System in Low Power Technology”, Asian Journal of Research in Social Sciences and Humanities. 6 (6) (2016) 465–481.
11. **R Saravana Ram**, S.Vanisri, J.V.Manju and K.Megala, “Design Of FSM Based Sensor for Effective Detection of Recycled ICs”, International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.77 (2015).