

1. R.Kiran Kumar , S.Shiyamala, “Design and Performance of Charge-Plasma-Based Schottky – FET CMOS Circuit Ring Oscillator for High Density ICs”, **Springer - Silicon** , July 2020 (**SCI – Indexed**) , DOI: 10.1007/s12633-020-00581-4
2. A.Sai Suneel and S.Shiyamala , “Peak detection based energy detection of a spectrum under Rayleigh fading noise environment” **Springer-Journal of Ambient Intelligence and Humanized Computing**, Feb 2020 (**SCI – E Indexed**)
3. R.Kiran Kumar , S.Shiyamala, “A 2-D Analytical Modeling of Dual Work Function Metal Gate MOSFET using High-K Gate Dielectric with enhanced RF/Analog Performance for Low Power Applications”, **Springer - Silicon** , Jan 2020 (**SCI – E Indexed**)
4. Priyanka halle , S.Shiyamala, “Trust and Cryptography centered Privileged Routing providing Reliability for WSN considering DoS attack designed for AMI of Smart Grid ”, International Journal of Innovative Technology and Exploring Engineering (IJITEE), Volume: 9,Issue :3, PP 1794 - 1800, Jan 2020 (Scopus Indexed)
5. **S.Shiyamala** , Sai Ganesh, “Design of High Stability Factor Lna using Cascade Mosfet at 850MHz Rf Frequency, International Journal of Recent Technology and Engineering, Volume: 8,Issue :3, PP 8925-8928,2019 (Scopus Indexed)
6. Priyanka halle , **S.Shiyamala** Secure Routing through Refining Reliability for WSN Against DoS Attacks using AODSD2V2 Algorithm for AMI , International Journal of Innovative Technology and Exploring Engineering (IJITEE), Volume: 8,Issue :10, PP 282-289,2019 (Scopus Indexed)
7. **S.Shiyamala** , Reicelin Rani, “ Design of High Speed H.265 Sample Adaptive Offset Estimation for Ultra-Hd Tv Encoding using Clock Synchronization Code, International Journal of Recent Technology and Engineering, Volume: 8,Issue :2, PP 719-724,2019 (Scopus Indexed)
8. Sai Suneel , **S.Shiyamala**, “Dynamic threshold selection through noise variance for spectrum sensing” International Journal of Engineering and Advanced Technology, Volume: 8,Issue :3, PP 719-724,PP :230-234,FEB 2019. (Scopus Indexed Journal)
9. Sai Suneel A and **Shiyamala S** . “Novel Energy Detection of Spectrum based on Noise Measurement a Review” Journal of Advanced Research in Dynamical and Control Systems, Vol 11, 01- Special Issue 9, pp 870 -873, 2019 (SCOPUS Indexed).
10. .Kavitha , Diwakar. K, **S.Shiyamala**,“Upper bounds on integrators of signal dependant delta-sigma ADC”, Journal of Advanced Research in Dynamical and Control Systems, Vol 10, Issue 9, pp 1116-1120, 2018(Accepted) (SCOPUS Indexed).
11. **S.Shiyamala**, T. Kavitha, “A modified MAP decoder architecture for low power consumption and delay using unfolded technique”, Journal of Advanced Research in Dynamical and Control Systems, Vol 10, Issue 9, pp 1159 -1166, 2018 (SCOPUS Indexed).
12. **S.Shiyamala**, T. Kavitha, “Design of low delay ring oscillator using CMOS”, International Journal of Engineering and Technology. Vol 7, Issue 3, pp 334 -336, 2018(SCOPUS

Indexed)

13. **S.Shiyamala**, T. Kavitha, P.K.Kowsalya “Improving the durability of the surveillance monitoring network”, International Journal of Engineering and Technology, Vol 7, Issue 3, pp 956 -959, 2018 (SCOPUS Indexed)
14. T.Kavitha **S.Shiyamala** , “Routing Protocols of Wireless Sensor Networks and Unmanned Aerial Vehicle”, International Journal of Engineering and Technology, Vol 7 , No 2.21 , 2018. (SCOPUS Indexed)
15. Sai Suneel A and Shiyamala S . “ Feasibility Investigation on Cognitive Radio Network Systems: Hearsay Algorithm” International Conference on Universal Computing , Communications and Data Engineering (CCODE2018) on Feb 7th 2018, SSRN, Tirupati.
16. Ragul Vasant Nale, S.Shiyamala, “ A Comparative Study on power Consumption in Smartphone Technology Through Communication Standards”, IJCRT, Vol. 5, issue 3, Jan 2018.
17. T.Kavitha **S.Shiyamala** , “Routing Protocols of Wireless Sensor Networks and Unmanned Aerial Vehicle”, International Journal of Electronics, Electrical and Computational System, Vol. 6, No. 9,2017.
18. **S.Shiyamala**, T. Kavitha, “Real Time Monitoring and Controlling Device for Elderly Personnel with Hypoglycemia”, International Journal of Electronics, Electrical and Computational System, Vol. 6, No. 9,2017.
19. **S.Shiyamala** T. Kavitha, and P.Nagarajan, “Real Time Motion Detection and Tracking System by Kalman Filter”, Journal of Theoretical and Applied Information Technology Vol.96, No. 16, Aug 2017. (SCOPUS Indexed)
20. Kavitha T **Shiyamala S.** and V.Rajamani “Multipath Routing and Wavelength Assignment Technique in Optical WDM Mesh Networks”, Journal of Optical Communications, Vol.38 , Issue 4, Dec 2017, (SCOPUS Indexed)
21. P.Nagarajan, T. Kavitha and **S.Shiyamala**, “Efficient Timing Element Design Featuring Low Power VLSI Applications”, International Journal of Engineering and Technology, Vol 8, No 4, Aug 2016. (SCOPUS Indexed)
22. T. Kavitha, **S.Shiyamala** and P.Nagarajan, “FPGA implementation arbiters algorithm for network-on-chip”, ARPN Journal of Engineering and Applied Sciences, Vol 11, No 19, Oct 2016.(SCOPUS Indexed)
23. Sudhir Kamble , **Shiyamala S.** Development of PROFIBUS Analog IO Using ASIC VPC3+S for Industrial Communication International Journal of Innovative Research in Science, Engineering and Technology Vol. 5, Issue 5, May 2016
24. T.Kavitha, **S.Shiyamala**, A.Farithkhan and J.Gnana Arun Johnson “Designing the Antenna for ISM band Application” International Journal of Applied Engineering Research,Vo.10, No.19,pp:40197-40202, 2015. (SCOPUS Indexed)
25. Ezhuvaraja and **Shiyamala S.** , “Power Dissipation minimization in Datapath design using operand isolation circuits”, Vol 10, No 5, International Journal of Applied Engineering and Research , 2015(**Annexure II**)
26. Murugesan,S. and **Shiyamala S.** “Soft error tolerant highly reliable multiport memory cell design” , International Journal of Advanced Technology in Engineering and Science, 2015.