

**Dr. S. Moorthi**

Assistant Professor

Electrical and Electronics Engineering

National Institute of Technology, Tiruchirappalli - 620015

Mobile : +91-9443210281

E-mail : [srimoorthi@nitt.edu](mailto:srimoorthi@nitt.edu) , [srimoorthi@gmail.com](mailto:srimoorthi@gmail.com)

**List of Publications: 29**

1. Dastagiri Reddy, B.; Anish, N.K.; Selvan, M.P.; Moorthi, S., "Embedded Control of n-level DC-DC-AC Inverter", accepted for publication, in IEEE Transactions on Power Electronics. DOI: 10.1109/TPEL.2014.2341245.
2. Dastagiri Reddy, B.; Selvan, M.P.; Moorthi, S., "A Simplified Embedded Control Scheme for Two-Stage Multistring Off-Grid Inverter", accepted for publication in IET Power Electronics.
3. Vasumathi B., S. Moorthi, "Improved neural network algorithms with time-varying Widrow-Hoff learning rule for harmonic estimation", Australian Journal of Electrical and Electronics Engineering, Vol. 9, No. 4, December 2012: 377-385.
4. Naizath Amirtharaj J., K. Siddharth, S. Moorthi, N. Kumaresan, " Voltage control Scheme for a Single Phase Voltage Source Inverter (VSI) using FPGA", Australian Journal of Electrical and Electronics Engineering, Vol. 9, No. 2, July 2012.
5. B. Vasumathi and S. Moorthi, "Implementation of hybrid ANN-PSO algorithm on FPGA for harmonic estimation", Engineering Applications of Artificial Intelligence, Elsevier Ltd., April 2012.
6. B. Vasumathi and S. Moorthi, "Modified ADALINE algorithm for harmonic estimation and Selective Harmonic Elimination in inverters", International Journal of Electronics, Taylor and Francis, November 2011.
7. S. Aditya and S. Moorthi, "Wide Tuning Low Noise OTA based Current Controlled Ring Oscillator to suit PLL Applications", National Journal of Technology, 10, pp. 66-72, PSG College of Technology, Coimbatore, INDIA, 2014.

8. M.A. Tajammul, A. Hemani, M.A. Shami, **S. Moorthi**, "A NoC Based Distributed Memory Architecture with Programmable and Partitionable Capabilities", IEEE NorChip Conference, Finland, November 2010.
9. Subanna ch, Naizath Amirtharaj , Siddharth K and **Moorthi S**, "FPGA based Closed loop Torque Control Scheme for a Stepper Motor," Proceedings of the 7th International Conference on Trends in Industrial Measurement and Automation, Jan. 2011.
10. M.A. Tajammul, A. Hemani, M.A. Shami, **S. Moorthi**, "NoC Based Distributed Partitionable Memory System for a Coarse Grain Reconfigurable Architecture", 24th International Conference System on VLSI Design, January 2011.
11. B. Vasumathi, **S. Moorthi**, "Harmonic Estimation Using Modified ADALINE Algorithm with Time-Variant Widrow - Hoff (TVWH) Learning Rule", Proceedings of IEEE International Symposium on Computers and Informatics, March 2011.
12. S. Senthil Kumar, D. Ganesh, P. Raja, **S. Moorthi**, "Voltage Controller in Photo-Voltaic system without Battery storage for Stand-alone applications", 2011-IEEE International Conference on Electrical, Control and Computer Engineering (InECCE), June 2011.
13. Krishna Chandran Vinay, Shyam H N, S. Rishi, **S. Moorthi**, "FPGA Based Implementation of Variable-Voltage Variable-Frequency Controller for a Three Phase Induction Motor", International IEEE Conference on Process Automation, Control and Computing, (PACC 2011) CIT, Coimbatore, 20-22 July 2011.
14. **S. Moorthi**, D. Meganathan, N. Krishnaprasad, J. Raja paul perinbam, "A Novel 14 ~ 170 MHz All Digital Delay Locked Loop with Ultra Fast Locking for SoC Applications", IEEE Conference on Recent Advances in Intelligent Computing, Trivandrum, 22-24 September 2011.
15. **S. Moorthi**, D. Meganathan, M. Shankar, R. Sridhar, J. Raja paul perinbam, "A Low- Jitter Phase-Locked Loop Architecture for Clock Generation in Analog to Digital Converters", IEEE Conference on Recent Advances in Intelligent Computing, Trivandrum, 22-24 September 2011.

16. Vivek R., Nalamwar Sanchit Gopal, Rahul Ashok, **S. Moorthi**, "FPGA based DC Servo motor Control for Remote Replication of Movements of a Surgical Arm", IEEE-TENCON 2011, Indonesia, 21-25 November 2011.
17. Venkatraman, K., Selvan, M.P. and **Moorthi, S.**, "Performance of Series Active Filter in Low Voltage Distribution System with Non Linear Loads", *IEEE International Conference INDICON-2011*, 16-18 December, 2011, Hyderabad, INDIA, pp. 1-6.
18. Venkatraman, K., Selvan, M.P. and **Moorthi, S.**, "Performance Analysis of Series Active Filter under Non Sinusoidal Supply and Non Linear Loading", *IEEE International Conference on Power and Energy Systems (ICPS -2011)*, 22-24 December, 2011, IIT Madras Chennai, INDIA, pp. 1-6.
19. Anish NK, Deepak Krishnan, **S. Moorthi**, Selvan MP, "FPGA Based Microstepping Scheme for a Stepper Motor in Solar tracking Applications", *International Conference on Renewable Energy Utilization 2012 - ICREU 2012*, CIT Coimbatore, INDIA, 04-06 January 2012.
20. Venkatraman, K., Selvan, M.P. and **Moorthi, S.**, "Investigation on Series Active Filter with Small Energy Source for DC Voltage Control", *Springer International Conference on Control, Communication and Power Engineering (CCPE-2012)*, 27-28 April, 2012, Bangalore, INDIA, pp. 454-459.
21. Anish NK, Deepak Krishnan, **S. Moorthi**, Selvan MP, "FPGA Based Microstepping Scheme for Stepper Motor in Space-Based Solar Power Systems", *Seventh International Conference on Industrial and Information Systems, IEEE-ICIIS 2012*, IIT Madras, Chennai, INDIA, 06-09 August 2012.
22. Gauri Shankar Singh, Devesh Singh, **S. Moorthi**, "Low power Low Jitter Phase Locked Loop for High Speed Clock Generation", *IEEE- PRIMEASIA 2012*, December 5-7, 2012.
23. Sandeep Rao, Aravind Natarajan, **S. Moorthi**, Selvan MP, "Real Time Object Tracking in a Video Stream using FPGA", *IEEE- INDICON 2012*, December 7-9, 2012.
24. Venkatraman K, T.R. Aashish, Selvan MP, **S. Moorthi**, "Experimental verification of FPGA Controller Based Series Active Filter", *IEEE- PRIMEASIA 2012* December 5-7, 2012.

25. Anish NK, **S. Moorthi**, "Intelligent Location identification and passenger – alert system in Indian Railways using GPS receiver", AFRICON 2013, September 9-12, 2013.
26. Anish NK, Kowshick B., **S. Moorthi**, "Ethernet based Industry automation using FPGA", AFRICON 2013, September 9-12, 2013.
27. Anish NK, Murali T., Dastagiri Reddy B., **S. Moorthi**, Selvan M. P., "FPGA Based Control Scheme for a Single-Stage Grid-Connected Solar Photovoltaic System", AFRICON 2013, September 9-12, 2013.
28. S. Aditya and **S. Moorthi**, "Wide Tuning Low Noise OTA based Current Controlled Ring Oscillator to suit PLL Applications", International Conference on Intelligent and Efficient Electrical Systems (ICIEES), December 2013. (Awarded as the Best Paper).
29. S. Aditya and **S. Moorthi**, "A Low Jitter Wide Tuning range Phase Locked Loop with Low Power Consumption in 180nm CMOS Technology", IEEE-Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PRIMEASIA), December 2013.