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### **AREAS OF INTEREST:**

Computer Networks, Soft Computing, Power Electronics & Drives,  
Control Systems, Signals & Systems, VLSI and Embedded Systems.

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### **LIST OF PUBLICATIONS**

1. **Vijayabhasker, R.,** and Sangeetha R. “Efficient Source Balanced Routing Protocol To Enhance Quality Of Service Factors In Disruption Tolerant Networks” Tierarztliche Praxis Journal, Vol. 40, pp. 911-922, **Sep 2020. (ISSN: 0303 - 6286)**
2. **Vijayabhasker, R.,** and Sangeetha R. “Enhancing QoS Factors Using Buffer Management Policies In Disruption Tolerant Networks” Journal of Information And Computational Science, Vol. 10, No.8, pp. 366-370, **Aug 2020. (ISSN: 1548 - 7741)**
3. **Vijayabhasker, R.,** and Sangeetha R. “Spatial Temporal Data Adverse Routing to Enhance Quality of Service Factors In Disruption Tolerant Networks” Journal of Emerging Trends And Innovative Research-JETIR-UGC Journal, Vol. 6, No.6, pp. 911-922, **June 2019. (ISSN: 2349 - 5162)**
4. **Vijayabhasker, R.,** and Sangeetha R. “Analysis of Packet Dropping, Storage Issue, Security Issue And Routing Misbehaviour In Disruption Tolerant Networks” International Journal of Engineering and Advanced Technology-(IJEAT)-Scopus Journal, Vol. 8,No.22 pp. 119-122, **Dec 2018. (ISSN: 2249 - 8958)**
5. **Vijayabhasker, R.,** and Sangeetha R. “Behaviour of Selfish Nodes and the Schemes Used to Eliminate Them In Disruption Tolerant Network” International Journal of

Research and Analytical Reviews (IJRAR) - UGC Journal, Vol. 5, No.4, pp. 004-013,  
Dec 2018. (ISSN: 2348 - 1269)

6. **Vijayabhasker, R.,** Gokulachandar, A. and Palaniswami, S. “MAMRN- MIMO Antenna Magnetic Field Reduction Using Signal to Noise Detection in Wireless Devices” Journal of Electrical Engineering, August 2018 .(ISSN: 1582-4594, Annexure – I, Impact Factor: 2.58)
7. **Dr.R.Vijayabhasker,** Santhosh Kumar C., “Information Retrieval using Multi Agent systems with Ontologies Organizations”, International Journal of Emerging Technology in Computer Science and Electronics” Vol.24, No. 9, June 2017 (ISSN: 0976-1353.)
8. **Vijayabhasker,R.,** “Accelerated Fuzzy PI Controlled SEPIC for Voltage Controlled Inverter”, Asian Journal of Information Technology, Vo.15, No. 5, pp 970-977, June 2016. .(ISSN: 1993-5994, Updated Journal, Impact Factor: 0.35)
9. **Vijayabhasker, R.,** Thenmozhi, G. and Palaniswami, S. “Effect of Cascade Controller on the Drum Dynamics of CFBC Boiler” World Applied Sciences Journal, Vol. 34 No.7, pp.922-930, July 2016. .(ISSN: 1991-6426, Annexure - II, Impact Factor: 0.71)
10. **Vijayabhasker, R.,** Thenmozhi, G. and Palaniswami, S. “Application of Different Control Algorithms for the Realization of Reheater Temperature Control” Middle-East Journal of Scientific Research, Vol. 24, No.5, pp.1720-1723, May 2016. .(ISSN: 1999-8147, Annexure - II, Impact Factor: 0.36)
11. **Vijayabhasker, R.** and Kamatchi Kannan, V. “Performance Analysis of Adaptive Auxiliary Controlled Interleaved Boost Converter for PV panel to Grid Systems”, International Journal of Advanced Engineering Technology, Vol 7, No.2, pp. 488-495, May 2016. .(ISSN: 0976-3945, Updated Journal, Impact Factor: 1.07)
12. **Vijayabhasker, R.,** Somasundaram, D., Palaniswami, S. and Venkatesa Kumar, V. “G-Band Chromosome Segmentation, Overlapped Chromosome Separation and Visible Band Calculation”, International Journal of Human Genetics ( IJHG), Vol. 14, No. 2, pp. 73-81, June 2014.(ISSN: 0972-3757, Annexure - I, Impact Factor: 0.306)

13. **Vijayabhasker, R.**, Palaniswami, S., Venkatesa Kumar, V. and Newlin Rajkumar, M.  
“An analysis on the characteristics of Cloud Computing” International Journal of Soft Computing , Vol. 9 Issue 1, 10-19, Sep 2014. (**ISSN: 1816-9503, Annexure - II, Impact Factor: 1.32**)
14. **Vijayabhasker, R.**, Palaniswami, S., Newlin Rajkumar, M. and Venkatesa Kumar, V.  
“An Heightened Switching Technique for an Interleaved Boost Converter Operated in Lower Duty Cycle”, Life Science Journal, Vol. 10, No. 2, pp. 119-125, March 2013.  
(**ISSN: 1097-8135, Annexure - I, Impact Factor: 0.158**)
15. **Vijayabhasker, R.** and Palaniswami, S. “A Survey on Soft Switching, Power Factor Correction and Applications on Interleaved Boost Converters”, European Journal of Scientific Research, Vol. 99, No. 1, pp. 167-179, April 2013. (**ISSN: 1450-202X, Annexure - II, Impact Factor: 0.736**)
16. **Vijayabhasker, R.**, Palaniswami, S., Venkatesa Kumar, V. and Newlin Rajkumar, M.  
“An Analysis of Interleaved Boost Converter with LC Coupled Enhanced Soft Switching ”, American Journal of Applied Sciences, Vol. 10, No. 4, pp. 331-343, April 2013. (**ISSN: 1546-9239, Annexure - II, Impact Factor: 1.4**)
17. **Vijayabhasker, R.**, Palaniswami, S., Venkatesa Kumar, V. and Newlin Rajkumar, M.  
“Certain Investigations on Cloud Computing Using Heterogeneous pattern” Journal of Applied Sciences Research, Vol. 9 Issue 9, p5453, Sep 2013. (**ISSN: 1819-544X, Annexure - II, Impact Factor: 1.4**)
18. **Vijayabhasker, R.**, Vijayasaro, V., Jeevitha V.S. and Ramya Sanalkumar. “Embedded Network Security Using Pattern Matching Algorithm”, Bonfring International Journal of Man Machine Interface, Vol 1, Special issue No.1, pp.04-09, Feb 2012.
19. **Vijayabhasker, R.** and Gemma benilda, C. “Low power l2 cache architecture using partial tag bloom filter”, International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET), Volume 3, (SI) No.3, March 2014.
20. **Vijayabhasker, R.** and Kiruthika, G. “Memory Accessed by Its Content Using Parity Bit”, International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET), Volume 3, ( SI) No.1, pp.135-145, Feb 2014.

21. **Vijayabhasker, R.** and Sri Nandhini, R.V. "Implementation of Elliptic Curve Multiplication", International Journal of Advanced Information Science and Technology (IJAIST), Volume 23, No. 23, March 2014.
22. **Vijayabhasker, R.** and Aishwarya, C. "Enhanced pipelined architecture for adaptive FIR filter based on distributed arithmetic", International Journal of Advanced Information Science and Technology (IJAIST), Volume 23, No. 23, pp. 186-191, March 2014.
23. **Vijayabhasker, R.** and Preethibha, C. "Fine-Grain Power-Gated Logic for Asynchronous Circuit Using Partial Charge Reuse", IOSR Journal of Engineering (IOSRJEN), Volume 04, No.5., pp. 52-60, May 2014.
24. **Vijayabhasker, R.** and Akilandeswari, N. "Design of an Energy-Efficient Constant Delay Logic For Low Power Applications", International Journal Of Research And Innovation In Engineering Technology (IJRIET), Volume 01, No.03, pp. 31-38, June 2014.
25. **Vijayabhasker, R.**, Mohan Kumar, K. and Barkunan, S.R. "Area Optimization in Masked Advanced Encryption Standard", IOSR Journal of Engineering (IOSRJEN), Volume 04, No. 03, pp. 25-29, June 2014.
26. **Vijayabhasker, R.** and Nithya, K. "Design of New High Speed Multi Output Carry Look-Ahead Adders", International Journal Of Research And Innovation In Engineering Technology (IJRIET), Volume 01, No.2, pp. 01-05, July 2014.
27. **Vijayabhasker, R.** and Abinaya, T. "Design Of Karatsuba-Ofman Pipelining To Cryptographic Application", International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 1, pp. 14-19, Jan 2015.
28. **Vijayabhasker, R.** and Balasundaram. "High Speed Multiplication Using BCD Codes For DSP Applications", International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 1, pp. 1-6, Jan 2015.
29. **Vijayabhasker, R.** and Bala vigneshwari, M. "Enhancement of Delay Resolution in All Digital Duty Cycle Corrector Using High Resolution NAND DCDL Method", International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 2, pp. 36-41, Feb 2015.

30. **Vijayabhasker, R.** and Challa Vidya Tejaswini. “A High Speed Partial Sum Network for Low Latency Successive Cancellation Polar Decoder”, International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 2, pp. 30-35, Feb 2015.
31. **Vijayabhasker, R.** and Kannadhasan, C. “Area Efficient Fault Tolerant Model For Parallel FIR Filters”, International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 1, pp. 20-24, Feb 2015.
32. **Vijayabhasker, R.** and Poornima, P. “An Efficient DLMS and NLMS Adaptive Filter with Low Adaptation Delay”, International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 2, pp. 14-22, Feb 2015.
33. **Vijayabhasker, R.** and Rhinose Fathima, J. “Monitoring of Input Vector with Modified BIST”, International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 1, pp. 7-13, Jan 2015.
34. **Vijayabhasker, R.** and Sindhu, R. “B-TCAM: An Efficient Bank Selection Based TCAM Architecture”, International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol. 2, No. 2, pp. 23-29, Feb 2015.

#### **PUBLICATIONS IN CONFERENCES**

TOTAL INTERNATIONAL CONFERENCES	: <b>50</b>
TOTAL NATIONAL CONFERENCES	: <b>300</b>