Dr. Francis Antony Selvi P

Associate Professor & HOD Electrical and Electronics Engineering RVS College of Engineering Dindigul- 624005

LIST OF JOURNALS

- 1. Dr.P.Francis Antony selvi, C.Banupriya &Mrs. S. Sumathi, "Multipurpose Guide for Visually Impaired People using Raspberry pi", International Research Journal of Engineering and Technology (IRJET), vol. 7, issue 7, 05 July 2020, pp.313–318. (ISSN No: 2395-0056)
- P.Francis Antony selvi & M.S.K.Manikandan, 2017, "Ant based multipath backbone routing for load balancing in MANET", Institution of Engineering and Technology -IET Communications, vol. 11, issue 1, 05 January 2017, pp.136–141. (ISSN No: 1751-8636, ANNEXURE I) Impact factor 1.061
- Dr.K.Kannan , Mrs.P.Francis Antony Selvi & Mrs.D.Pushpalatha, "Torque Ripple Compensation Technique Based BLDC Motors using Cuk Converter using ARDUINO", Volume 2 Issue 5 (05 -2016) International Journal of Current Trends in Engineering & Research (IJCTER) e-ISSN 2455– 1392 Volume 2 Issue 5, May 2016 pp. 1 - 11 Scientific Journal Impact Factor: 3.468
- 4. Mrs.P.Francis antony selvi, S.Mathavi, N.Meenakshi, S.Lakshmi, "Performance analysis of tag bit based low power cache memory" International Journal of Recent Trends in Engineering & Research (IJRTER) Volume 02, Issue 1; 2016 [ISSN: 2455-1457]
- 5. P.Francis Antony selvi & M.S.K.Manikandan, 2014, "Latency And energy aware backbone routing Protocol for MANET", Journal of Theoretical and Applied Information Technology 10th June 2014. Vol. 64 No.1 (ISSN No: 1992-8645, ANNEXURE II) Impact factor 0.38

International conference

- 1. P.Francis Antony selvi & M.S.K.Manikandan, 2013, "Multipath backbone routing for latency and energy reduction in MANET", IEEE International Conference COMNETSAT 2013
- 2. Presented paper entitled on "Novel energy stored single stage photovoltaic power system with constant DC-link peak voltage" at Thiruvalluvar College of Engineering and Technology, Vandavasi, India During 7th and 8th March 2015.
- 3. Presented paper entitled on "Performance analysis of tag bit based low power cache memory" at RVS college of Engineering and Technology, Dindigul During 1st and 2nd April 2016.