



Scopus

Search Sources Lists SciVal ↗



Create account

Sign in

Sivamangai, N. M.

Karunya Institute of Technology and Sciences, Coimbatore, India [Show all author info](#)

16640056200 [Connect to ORCID](#)

[Edit profile](#) [Set alert](#) [Potential author matches](#) [Export to SciVal](#)

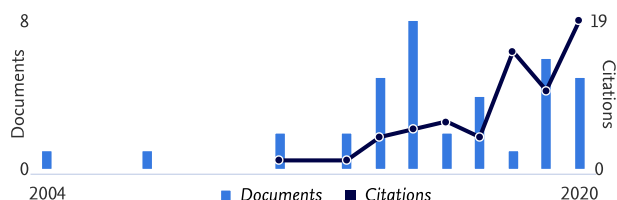
Metrics overview

37 Documents by author

65 Citations by 62 documents

5 *h*-index: [View *h*-graph](#)

Document & citation trends



[Analyze author output](#) [Citation overview](#)

Most contributed Topics 2015–2019

Synthetic Apertures; Beamforming; Plane Waves

3 documents

Advanced Encryption Standard; S-Box; Block Ciphers

3 documents

Static Random Access Storage; Finfet; Virtuoso

2 documents

[View all Topics](#)

37 Documents Cited by 62 Documents 0 Preprints ^{New} 42 Co-Authors Topics

[Export all](#) [Add all to list](#)

Sort by [Cited by \(highest\)](#)

[View list in search results format](#)

[View 704 references](#)



[Set document alert](#)

Conference Paper

Hardware implementation of DNA based cryptography

Naveen Jarold, K., Karthigaikumar, P., Sivamangai, N.M., Sandhya, R., Asok, S.B.

2013 IEEE Conference on Information and Communication Technologies, ICT 2013, 2013, pp. 696–700, 6558184

[View abstract](#) [View at Publisher](#) [Related documents](#)

11

Cited by

Conference Paper

Design of 10T SRAM cell for high SNM and low power

Grace, P.S., Sivamangai, N.M.

Proceedings of the 3rd International Conference on Devices, Circuits and Systems, ICDCS 2016, 2016, pp. 281–285, 7570609

[View abstract](#) [View at Publisher](#) [Related documents](#)

7

Cited by

Conference Paper

FPGA implementation of efficient AES encryption

Priya, S.S.S., Karthigai Kumar, P., Sivamangai, N.M., Rejula, V.

ICIIECS 2015 - 2015 IEEE International Conference on Innovations in Information, Embedded and Communication Systems, 2015, 7193081

[View abstract](#) [View at Publisher](#) [Related documents](#)

7

Cited by

Article

A low power SRAM cell with high read stability

Sivamangai, N.M., Gunavathi, K.

ECTI Transactions on Electrical Engineering, Electronics, and Communications, 2011, 9(1), pp. 16–22

[View abstract](#) [Related documents](#)

6

Cited by

Conference Paper

Mixed random 128 bit key using finger print features and binding key for AES algorithm

Priya, S.S.S., Karthigaikumar, P., Mangai, N.M.S.

Proceedings of 2014 International Conference on Contemporary Computing and Informatics, IC3I 2014, 2014, pp. 1226–1230, 7019656

[View abstract](#) [View at Publisher](#) [Related documents](#)

5

Cited by

Article

Architecture and FPGA prototype of cycle stealing DMA array signal processor for ultrasound sector imaging systems

Kidav, J.U., Sivamangai, N.M., Pillai, M.P., Raja M, S.

Microprocessors and Microsystems, 2019, 64, pp. 53–72

[View abstract](#) [View at Publisher](#) [Related documents](#)

3

Cited by

Conference Paper

A biometric encryption using face recognition system for watch list

Valarmathi, R., Priya, S.S.S., Kumar, P.K., Sivamangai, N.M.

2014 International Conference on Information Communication and Embedded Systems, ICICES 2014, 2015, 7034018

[View abstract](#) [View at Publisher](#) [Related documents](#)

3

Cited by

Conference Paper

Evaluation of testability of digital circuits by fault injection technique

Evangeline, C., Sivamangai, N.M.

2nd International Conference on Electronics and Communication Systems, ICECS 2015, 2015, pp. 92–96, 7125048

[View abstract](#) [View at Publisher](#) [Related documents](#)

3

Cited by

Conference Paper

A zone based self repairing SRAM architecture using Adaptive Body Biasing schemes

Gunavathi, K., Sivamangai, N.M.

ICIIS 2007 - 2nd International Conference on Industrial and Information Systems 2007, Conference Proceedings, 2007, pp. 67–72, 4579150

[View abstract](#) [View at Publisher](#) [Related documents](#)

3

Cited by

Article

An Efficient Wavelet Based Transient Current Test towards Detection of Data Retention Faults in SRAM

Princy, P., Sivamangai, N.M.

Journal of Electronic Testing: Theory and Applications (JETTA), 2019, 35(4), pp. 473–483

[View abstract](#) [View at Publisher](#) [Related documents](#)

2

Cited by

Conference Paper

Multiplexer based high throughput S-box for AES application

Priya, S.S., Das, K.G., Sivamangai, N.M., Kumar, P.K.

2

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Generation of 128-bit blended key for AES algorithm

2

Priya, S.S.S., Karthigaikumar, P., SivaMangai, N.M.

Cited by

Advances in Intelligent Systems and Computing, 2015, 338, pp. 431–439

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

A secure cryptographic scheme for audio signals

2

Asok, S.B., Karthigaikumar, P., Sandhya, R., Naveen Jarold, K., Mangai, N.M.S.

Cited by

International Conference on Communication and Signal Processing, ICCSP 2013 - Proceedings, 2013, pp. 748–752, 6577156

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article

A novel technique for minimisation of March test using read equivalent stress

1

Prince, P., Sivamangai, N.M.

Cited by

International Journal of Nanomanufacturing, 2020, 16(2), pp. 184–201

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article • [Open Access](#)

Review on fabrication of graphene nanoholes

1

Silpa, P.A., Sivamangai, N.M.

Cited by

e-Journal of Surface Science and Nanotechnology, 2019, 17, pp. 10–15

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Overview of Current Compliance Effect on Reliability of Nano Scaled Metal Oxide Resistive Random Access Memory Device

1

Cited by

Napolean, A., Sivamangai, N.M., Samuel, J., John, V.

Proceedings of the 4th International Conference on Devices, Circuits and Systems, ICDCS 2018, 2019, pp. 290–296, 8605178

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article

High Throughput AES Algorithm Using Parallel Subbytes and MixColumn

1

Cited by

Sridevi Sathya Priya, S., Karthigaikumar, P., Sivamangai, N.M., Rejula, V.

Wireless Personal Communications, 2017, 95(2), pp. 1433–1449

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Power analysis of bit interleaving 9T SRAM array

1

Cited by

Karthika, S., Sivamangai, N.M.

Proceedings of the 3rd International Conference on Devices, Circuits and Systems, ICDCS 2016, 2016, pp. 275–280, 7570608

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Recognition of elephants in infrared images using mean-shift segmentation

1

Cited by

Suseethra, S., Chandy, D.A., Mangai, N.M.S.

2014 International Conference on Information Communication and Embedded Systems, ICICES 2014, 2015, 7034016

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Recognition of elephants in infrared images using clustering-based image segmentation

1

Cited by

Mangai, N.M.S., Vinod, S.T., Chandy, D.A.

International Journal of Electronic Security and Digital Forensics, 2015, 7(3), pp. 234–244

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Design and implementation of low power Advanced Encryption Standard S-Box using pass transistor XOR-AND logic

1

Cited by

Christy, M.A., Priya, S.S.S., Mangai, N.M.S., Karthigaikumar, P.

2014 International Conference on Electronics and Communication Systems, ICECS 2014, 2014, 6892616

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article

Design for test technique for leakage power reduction in nanoscale Static Random Access Memory

1

Cited by

Sivamangai, N.M., Gunavathi,

Journal of Computer Science, 2011, 7(8), pp. 1252–1260

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Design and Simulation of Multiple Quantum well based InGaN/GaN Light Emitting Diode for High power applications

0

Cited by

Saranya, G., Sivamangai, N.M., Priya, B., Benazir Begam, R.

ICDCS 2020 - 2020 5th International Conference on Devices, Circuits and Systems, 2020, pp. 109–112, 9075814

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Temperature effects on an HfO₂-TiO₂-HfO₂ stack layer resistive random access memory cell for low power applications

0

Cited by

Napolean, A., Sivamangai, N.M., Naveenkumar, R.

ICDCS 2020 - 2020 5th International Conference on Devices, Circuits and Systems, 2020, pp. 275–278, 9075700

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article

One step synthesis of graphene

0

Cited by

Silpa, P.A., Sivamangai, N.M.

Inorganic and Nano-Metal Chemistry, 2020, 50(2), pp. 73–79

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article • Article in Press

A novel method for minimizing transient current test time by exploiting RES in SRAM

0

Cited by

Prince, P., Sivamangai, N.M.

Analog Integrated Circuits and Signal Processing, 2020

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Design and physical implementation of array signal processor ASIC for sector imaging systems

0

Cited by

Kidav, J.U., Sivamangai, N.M., Pillai, M.P., Sreejeesh, S.G.

Proceedings - 32nd International Conference on VLSI Design, VLSID 2019 - Held concurrently with 18th International Conference on Embedded Systems, ES 2019, 2019, pp. 448–453, 8710942

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Graphene and its derivatives for Nanoscale Semiconductor Memories-A Density Functional Theory based approach

0

Cited by

Silpa, P.A., Sivamangai, N.M.

Materials Today: Proceedings, 2019, 11, pp. 991–1000

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

A Study on the Future of DNA Biosensors as Disease Detectors

James, M.K., Sivamangai, N.M.

2018 International Conference on Circuits and Systems in Digital Enterprise Technology, ICCSDET 2018, 2018, 8821077

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Detection of set in a memory array using C-element

Princy, P., Larson, R., Sivamangai, N.M.

Proceedings of IEEE International Conference on Innovations in Electrical, Electronics, Instrumentation and Media Technology, ICIEEIMT 2017, 2017, 2017-January, pp. 166–169

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

A review on nanoscale materials for memory applications

Silpa, P.A., Sivamangai, N.M.

Proceedings of IEEE International Conference on Innovations in Electrical, Electronics, Instrumentation and Media Technology, ICIEEIMT 2017, 2017, 2017-January, pp. 103–105

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Design and Development of 32 channel Receiver Beam Former

Ananth Hari, R., Muthaiah, R., Kidav, J.U., Nageswara Rao, C.H., Sivamangai, N.M.

2017 International Conference on Networks and Advances in Computational Technologies, NetACT 2017, 2017, pp. 80–85, 8076745

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Article

Efficient hardware implementation of AES algorithm using bio metric key

Priya, S.S.S., Karthigaikumar, P., Mangai, N.M.S., Sandhya, R.

International Journal of Information and Communication Technology, 2015, 7(4-5), pp. 437–454

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Clustering based image segmentation for elephant detection

Vinod, S.T., Mangai, N.M.S., Chandy, D.A.

2014 International Conference on Electronics and Communication Systems, ICECS 2014, 2014, 6892635

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Difference set codes for soft error protection in memories

Thomas, K.T., Mangai, N.M.S.

2014 International Conference on Electronics and Communication Systems, ICECS 2014, 2014, 6892598

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Implementation of low-area S-box based on normal basis

Thomson, K.E., Mangai, N.M.S., Priya, S.S.S.

2014 International Conference on Electronics and Communication Systems, ICECS 2014, 2014, 6892607

0

Cited by

[View abstract](#) [View at Publisher](#) [Related documents](#)

Conference Paper

Design of 80387 floating point math coprocessor

Karthigaikumar, P., Rajaa, T.T.A., Sivamangai, N.M., Jackulinemoni, D.

0

Cited by

About Scopus

- What is Scopus
- Content coverage
- Scopus blog
- Scopus API
- Privacy matters



Language


- 日本語に切り替える
- 切换到简体中文
- 切换到繁體中文
- Русский язык

Customer Service

- Help
- Contact us

ELSEVIER

Terms and conditions  Privacy policy 

Copyright © Elsevier B.V . All rights reserved. Scopus® is a registered trademark of Elsevier B.V.

We use cookies to help provide and enhance our service and tailor content. By continuing, you agree to the use of cookies.

