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Publications-journal

1. Kola Muralikumar and P. Ponnambalam "Analysis of Cascaded Multilevel Inverter with a Reduced Number of Switches for Reduction of Total Harmonic Distortion" IETE Journal of Research Taylor and Francis, 2020
2. Murthy Priya, Pathipooranam Ponnambalam , Kola Muralikumar,"Modular-multilevel converter topologies and applications – a review", IET Power Electronics, vol. 12, no. 2, pp. 170-183, 20 2 2019
3. Challa Babu, P Ponnambalam," The theoretical performance evaluation of hybrid PV-TEG system", Energy Conversion and Management 173 (2018) Elsevier, 450-460
4. Challa Babu, P Ponnambalam, "The role of thermoelectric generators in the hybrid PV/T systems: A review", Energy Conversion and Management 151 (2017) Elsevier, 368-385
5. P Ponnambalam, Muralikumar.K, Vasundhara.P, Sreejith.S, Challa. Babu, "Fuzzy Controlled Switched Capacitor Boost Inverter" Energy Procedia 117 (2017) Elsevier, 909–916
6. P.Ponnambalam, M. Praveenkumar, Challa Babu, P. Dhambi Raj, "Analysis of Stacked Multicell Converter with Fuzzy Controller", Advances in Intelligent Systems and Computing (Springer), Vol 546, pp 318-330, 2017
7. P.Ponnambalam, K. Aroul, P.Prasad reddy, K. Muralikumar, "Analysis of Fuzzy Controller for H-bridge Flying Capacitor Multilevel Converter", Advances in Intelligent Systems and Computing (Springer), Vol 546, pp 307-317, 2017
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11. Sreejith. S, Velamuri Suresh, P.Ponnambalam, "Static economic dispatch incorporating windfarm using Flower pollination algorithm", Perspectives in Science (Elsevier), Vol 8, pp 260-262, 2016
12. K. Yamini, B. Vasudha, Avinash Sharma, P.Ponnambalam "Implementation of Fuzzy Logic Controller for Cascaded Multilevel Inverter with Reduced Number of Components", Indian Journal of Science and Technology, Vol 8(S2), pp 278–283, 2015

Conference Papers

1. M. Priya, K. Muralikumar, P. Ponnambalam, "VSC Based Modular Multilevel Converter For Grid Applications Using PI Controller", 2019 Innovations in Power and Advanced Computing Technologies, i-PACT 2019, VIT Vellore.
2. D. Dinesh kumar, Challa Babu, P. Ponnambalam, "A Flyback Partial Power Processing Converter based Solar PV Systems Interfacing the Grid", 2019 Innovations in Power and Advanced Computing Technologies, i-PACT 2019, VIT Vellore.
3. T.Y. Saravanan, P. Ponnambalam, "A Bidirectional Converter for Integrating DVR-U Cap to Improve Voltage Profile Using Fuzzy Logic Controller", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
4. M. Priya, P. Ponnambalam, K. Muralikumar, "THD Analysis of Flying-Capacitor Multilevel Converter Using Fuzzy Logic Controller", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
5. K. Muralikumar, P. Ponnambalam, M. Priya, "Implementation of Fuzzy-Based Multicarrier and Phase Shifted PWM Symmetrical Cascaded H-Bridge Multilevel Inverter", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
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7. K. Muralikumar, C. Sivakumar, Ankit Rautela, P. Ponnambalam, "Fuzzy Based Cascaded Multilevel Inverter Topology with Galvanic Isolation", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
8. K. Muralikumar, P. Ponnambalam, "Closed Loop Control of Diode Clamped Multilevel Inverter Using Fuzzy Logic Controller", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
9. Jyothi, P. Ponnambalam, "Hybrid Variable Length Partial Pulse Modulation for Visible Light Communication", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
10. Challa Babu, P. Ponnambalam, "PV Module Temperature Estimation by Using ANFIS", 2018, International Conference on Soft Computing for Problem Solving, SocProS 2018, VIT Vellore.
11. Y. Viswanath, K. Muralikumar, P. Ponnambalam, M. Praveenkumar, "Symmetrical cascaded switched-diode multilevel inverter with fuzzy controller", 2017, International Conference on Soft Computing for Problem Solving, SocProS 2017, IIT Bhubaneswar.
12. M. Praveenkumar, M. Manimozhi, P. Ponnambalam, G. Gokulakrishnan, "A Novel PID Controller Designed via Polynomial Approach for Stable/Unstable Second-Order Process with Time Delay", 2017, International Conference on Soft Computing for Problem Solving, SocProS 2017, IIT Bhubaneswar.

13. K. Leela Madhav, Challa Babu, Ashutos Mahapatra, "Fuzzy logic controller for nine level multi-level inverter with reduced number of switches", 2017 Innovations in Power and Advanced Computing Technologies, i-PACT 2017, VIT Vellore
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15. P Ponnambalam, Muralikumar.K, Vasundhara.P, Sreejith.S, Challa. Babu, "Fuzzy Controlled Switched Capacitor Boost Inverter", 2017, 1st International Conference on Power Engineering, Computing and CONTROL, PECCON-2017, VIT Chennai
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18. J. Gowri Shankar, J. Belwin Edward , P. Ponnambalam, K. Sathish kumar, "A Single Phase Hybrid Multilevel Inverter with High Step up DC-DC Converter for Photovoltaic System", 2016, IEEE TENCON 2016 – Technologies for Smart Nation, Singapore