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Publications during the last five years (2014-2020) - Dr. Sreedevi V T

Conferences:

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4. M Jayaraman, **VT Sreedevi**, "Power quality analysis of a PV fed seven level cascaded H-bridge multilevel inverter", IEEE International Conference on Advanced Communications, Control and Computing Technologies, 2014, Pages-281-285.
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6. Meenakshi Jayaraman, **VT Sreedevi**," FPGA implementation of a passive filter for harmonic reduction in inverter outputs in PV-based renewable energy systems", International Conference on Intelligent Computing and Applications, pp 501-514,2017.
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8. Reena Monica P, Naveen Chaubey, **Sreedevi V.T** "An optimized geometry-physicsbased compact model of CNTFET"Materials Today: Proceedings, Volume 3, Issue 6 (2016), pp 2295-2304.
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13. Sandeep kumar G, Kanimozhi G, **Sreedevi.V.T**, "Comparative analysis of digital linear and nonlinear controller for a PFC boost converter, IEEE -2nd International conference on Innovations in Information Embedded and Communication systems (ICIIECS'15) pp-304-308, 2015.

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2. Meenakshi Jayaraman, **Sreedevi V T**, "Power quality Improvement in a cascaded multilevel inverter interfaced grid connected system using a modified inductive- capacitive-inductive filter with reduced power loss and improved harmonic attenuation", **Energies,** Vol.10, Issue 11, Pages-1834, 2017.
3. Devi Venkatesh**, Sreedevi V.T,** "Design and analysis of an integrated LC3–Valley fill passive LED driver, **International Journal of Electronics,** Vol.105, issue 12, Pages: 2052-206,2018.
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35. R Kumar, R Rambhad, P Sriramalakshmi, VT Sreedevi "Five-Level Switched Inductor-Based Cascaded H-Bridge Quasi-Switched Boost Inverter for Renewable Energy Applications" - Advances in Greener Energy Technologies, 2020