

Dr. Dhandapani S

Publications: (Last 5 years)

1. Tony Santhosh, G. and **Dhandapani, S.** “Optimal routing in VANET using improved meta-heuristic approach: a variant of Jaya” IET Communications, Volume 14, Issue 16, October 2020, pp. 2740 – 2748. ISSN No. 1751-8636. (SCI Indexed)
2. Mariyaprincy, A. and **Dhandapani, S.** “Future Internet-Analysis of IoT Architectures, Technologies used in IoT, Applications and Future Scope of LoRa Technology in IoT” in International Journal of Advanced Science and Technology, Volume. 29, No.8s Page No: 3655-3663. 2020.
3. Kalaipriya, O. and **Dhandapani, S.** “Classification of Lung Cancer Cells Using GLCM and BPN Algorithm” in Journal of TEST Engineering and Management, Volume. 82, Page No: 24404 -24411. May- June 2020.
4. Tony Santhosh, G. and **Dhandapani, S.** “Hybridization of Monarch Butterfly and Grey Wolf Optimization for Optimal Routing in VANET” International Journal of Engineering and Advanced Technology, Volume 09, Issue 2, December- 2019, Page No:3049-3060, ISSN No: 2249-8958
5. **Dhandapani, S.** and Buvaneswari, PR. “Video Steganography using IWT, DWT, LBP Methods and its Research” International Journal of Engineering and Advanced Technology, Volume 8, Issue 6S3, September 2019,Page No:2022-2026, ISSN No: 2249-8958.(Scopus Indexed)
6. **Dhandapani, S.** and Buvaneswari, PR. “Video steganography using IWT, DWT, LBP methods and its comparative analysis”, International Conference on Eletrical, Eleronics and Intelligent Computing, July 2019.
7. Srinivasan, P. and **Dhandapani, S.** “Enhancement of Low Voltage Ride through Capability for DFIG Based Wind Turbine with STFCL, DVR and Energy Storage System” International Journal of Recent Technology and Engineering, Vol. 8, No. 2, pp.2882-2886, July 2019. (Scopus Indexed)
8. Srinivasan, P. and **Dhandapani, S.** “LVRT Enhancement Capability of DFIG based WECS by implementing STFCL-SMES” International Journal of Pure and Applied Mathematics, Vol. 119, No. 16, pp.3495-3500, July 2018.
9. Santhiya, M. and **Dhandapani, S.** “Estimation of Phase noise and error in MIMO-OFDM” International Journal of Innovative Research in Technology, Vol. 4, No. 12, pp. 546-554, May 2018.
10. Srinivasan, P. and **Dhandapani, S.** “Modeling and Simulation of HVRT and LVRT enhancement capability for doubly fed induction generator based wind energy conversion system” International Journal of Engineering & Technology, Vol. 7, pp. 405-408, Mar. 2018.

11. Srinivasan, P. and **Dhandapani, S.** “LVRT Enhancement Capability of DFIG based WECS using PI Controller” International Conference on Smart Structures and Systems, Mar. 2018.
12. Srinivasan, P. and **Dhandapani, S.** “Improvement of LVRT capability by combining switch type fault current limiter and super capacitor for DFIG based wind turbines” IIOAB journal, Vol. 8, Suppl. No.3, pp.53-59, Apr. 2017.
13. Tony, S. and **Dhandapani, S.** “Design of rapid vehicular Communication using SDVN” International Conference on Engineering and Applied Sciences, March 2017.
14. **Dhandapani, S.** and Srinivasan, R. “Low power and area efficient full adder design using GDI technique” Asian Journal of Information Technology, Vol.15, No.4, pp. 750-755, Apr. 2016. (**Scopus Indexed**)
15. **Dhandapani, S.** and Nithya, M. “FPGA Implementation of an LUT Adaptive Filter Using Distributed Arithmetic for the Segmentation of Real Time HD Video” [Journal of Computational and Theoretical Nanoscience](#), Vol. 12, No. 11, Nov. 2015, pp. 4450-4454. (**Scopus Indexed**)
16. Sathish, A., **Dhandapani, S.** and Prasanth, K.T. “Design and Implementation of Identity Based Signature for E-Mail Authentication”, International Journal of Applied Engineering Research, Vol. 10, No. 55, pp. 2476-2480, 2015. (**Scopus Indexed**)
17. Abhishek, K.S. and **Dhandapani, S.** “Video Watermarking technique for authentication and Security”, International Journal of Applied Engineering Research, Vol. 10, No. 6, pp. 15325-15336, 2015. (**Scopus Indexed**)
18. Nivetha, R. and **Dhandapani, S.** “Reducing Switching Activities Through Data Encoding in Network on Chip”, Middle-East Journal of Scientific Research, 23 (SSPS): pp.73-76, 2015.
19. Anusuya, S., **Dhandapani, S.** and Buvaeswari, P. R. “Design and Implementation of Motion Artifact Reduction ASIC for Wearable ECG Recording”, Middle-East Journal of Scientific Research, 23 (SSPS): pp.77-82, 2015.
20. **Dhandapani, S.** and Subathra, K. “Tracking of Number Plate Symbols Using Dynamic Image Processing and Genetic Algorithm”, Middle-East Journal of Scientific Research, 23 (4): pp.729-734, 2015.