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- 1) VLSI Design of a Squaring Architecture based on Yavadunam Sutra of Vedic Mathematics
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- 2) Cost optimization of PV-diesel systems in nanogrid using cuckoo search algorithm and its application in mobile towers
CN Murugesan, C., Marimuthu
International Journal on Emerging Technologies, 2020, 11 (3), 328–335
- 3) An efficient high speed squaring and multiplier architecture using yavadunam sutra and bit reduction technique
A Deepa, CN Marimuthu, C Murugesan
Journal of Physics: Conference Series 1432 (1), 012080
- 4) Design of a high speed Vedic multiplier and square architecture based on *Yavadunam Sutra*
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Sādhana 44 (9), 197
- 5) Optimization of motion estimation hardware using charged system optimization with low power multipliers for H. 264 encoders
L Vigneash, CN Marimuthu
Cluster Computing 22 (5), 10669-10677
- 6) Cost optimization of PV-diesel systems in nanogrid using LJ cuckoo search and its application in mobile towers
C Murugesan, CN Marimuthu
Mobile Networks and Applications 24 (2), 340-349
- 7) A novel center-tapped transformer based multilevel inverter with common DC source
G Arthy, CN Marimuthu
Journal of Vibroengineering 20 (8), 3040-3053
- 8) A Hybrid Low Power Fast Full Adder Using XOR And XNOR Gates
RKU Maheswari, CN Marimuthu
International Journal of Recent Advances in science and technology 5 (3), 64-68
- 9) High speed VLSI architecture for squaring binary numbers using Yavadunam sutra and bit reduction technique
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International Journal of Applied Engineering Research 13 (6), 4471-4474
- 10) Modified RS encoder architecture with reduced critical path delay for high speed data communication
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2017 International Conference on Intelligent Sustainable Systems (ICISS ...
- 11) A survey on control algorithms for DSTATCOM
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- 12) A high speed VLSI architecture of a pipelined reed solomon

encoder for data storage in communication systems

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Asian Journal of Research in Social Sciences and Humanities 7 (2), 228-238

13) Implementation of Power Reduction Adder with High Recital Multiplier

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Asian Journal of Research in Social Sciences and Humanities , 6 (8), 898-907

14) Intelligent Vehicle Control System Using Shared Steering Control Mechanism

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Journal of Network Communications and Emerging Technologies (JNCET) www ...

15) A New Multilevel Inverter Design with Reduced Power Electronic Components for Three Phase Grid Connected System

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Asian Journal of Research in Social Sciences and Humanities 6 (11), 642-658

16) Immune RBF neural network algorithm for DSTATCOM

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2016 International Conference on Computer Communication and Informatics ...

17) Fast Multiplier for FIR Filters

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International Journal of Scientific and Technical Advancements 2 (1), 57-61

18) Signature Based Vulnerability Detection Over Wireless Sensor Network for Reliable Data Transmission

NMS Kumar, S Deepa, CN Marimuthu, T Eswari, S Lavanya

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19) Shared Steering Control between a Driver and an Automation: Stability in the Presence of Driver Behavior Uncertainty

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International Journal of Recent Research in Electrical and Electronics ...

20) Implementation of Han-Carlson Adder for Error Tolerant Applications

MCN Sasireka.S

International Journal of Electrical and Electronics Research 3 (4), 39-50

CONFERENCES (NATIONAL AND INTERNATIONAL)

1. Marimuthu C.N., SakthiSudhan. K, Thangaraj, P and.Prabhu. P "Secure Audio Steganography for Hiding Text and Audio Files" in Proceedings of IEEE-International Conference on Advances in Engineering, Science and Management organized by E.G.S Pillay engineering college, Nagapattinam on 30 and 31th, March 2012.
2. Marimuthu,C.N., Jeeva, S. and Bharathi,S, "Power efficient architecture of pre - computation based content addressable memory", Proceedings of the IEEE Xplore on computer communication and Informatics (ICCCI),2012 International Conference held at Sri Sakthi Institute of Engineering and technology, Coimbatore, India, pp 1-5,10-12,January2012.
3. Marimuthu,C.N. and Deepa.S, "Microprocessor cache compressor design using P-Match algorithm", Proceedings of the IEEE Xplore on Computer Communication and Informatics (ICCCI), 2012 International Conference held at Sri Sakthi Institute of Engineering and technology, Coimbatore, India, pp. 1-5, 10-12 January 2012.

4. Marimuthu, C. N., Thangaraj. P and Haripriya, "Permanent error Diagnosis in ON-CHIP Interconnects for self Adaptive system", in Proc. of National Conference on communication and Signal processing(NCCSP'11) Organized by SSS College of Engineering Komarapalayam, March 11,2011.
5. Marimuthu C. N., Thangaraj P. and Srinivasan, "Life time of the system using mobile Robots to reap data from sensor fields", in Proc. of International Conference on Systemic, Cybernetics and Informatics (ICSCI'10), Organized by Pentagram Research Centre, India, January 27-30, 2010.
6. Marimuthu, C. N., Thangaraj. P and Aswathy Ramesan, "Low power Multiplier Design using Low power Ring counter", in Proc. of International Conference on Intelligent Systems and Control (ICSO'10), Organized by Karpagam college of Engineering , Coimbatore, India, February 4-5, 2010.
7. Marimuthu, C. N., Thangaraj. P and Hanna Mathew, "A Novel Delay for Low power applications", in Proc. of International Conference on Intelligent Systems and Control (ICSO'10), Organized by Karpagam college of Engineering , Coimbatore, India, February 4-5, 2010.
8. Marimuthu, C. N., Thangaraj. P and Gowsalya, "A Low power twin precision Multiplier", in Proc. of National Conference on Systemics, Cybernetics and Informatics (SCI'10),), Organized by Kongu Engineering College, Perundurai, India, October 23-24, 2009.
9. Marimuthu, C. N., Thangaraj. P and Poornapriya, "Low power Low Frequency Multiplier", in Proc. of International Conference on Advanced Communication and Informatics (ICACI'2009), Organized by Thanthai Periyar Institute of Technology, Vellore, India, January 11-13, 2009.
10. Marimuthu, C. N. and Riboy Cheriyan, "Low Energy Double Edge Triggered Flip Flop using CBS Technology", 15th International Conference on VLSI Design and Embedded systems(ICVLSI'08), Velammal Engineerign College, Chennai, India, pp.48-53,2008.
11. Marimuthu, C. N., Thangaraj. P and Alice K Paul, "Active Leakage Power Reduction For FPGAS", in Proc. of International Conference on Advanced Communication Systems (ICACS'2007), Organized by Government College of Technology, Coimbatore, India, January 10-12, 2007.
12. Marimuthu, C. N. and Thangaraj. P, "Implementation of Logical Networks for QCA Using AOI", in Proc. of National Conference (VESCO 2007), Organized by Kongu Engineering College, Perundurai, Erode, India, January 27, 2007.
13. Marimuthu, C. N., Thangaraj. P and Robin Abraham, "A Digital self-Adjusting Minimum Power Supply System based on FSM", in Proc. of National Conference on Recent Advancements In Information Technology" (NCRAIT'07), Organized by SRI Ramakrishna Engineering College, Coimbatore, India, February 9-10, 2007.
14. Marimuthu, C. N. and Thangaraj. P "Active Reducing Leakage Current in CMOS VLSI Circuits Using MLV Inputs", in Proc. of National Conference on Signals, Systems and Security(NCSSH'-2006), Organized by Bannari Amman Institute of Technology, Sathyamangalam, India, January 24-25, 2006.
15. Marimuthu, C. N., Thangaraj. P and Ravi, "A Dynamic Method To Reduce Static Power In CMOS VLSI Circuits", in Proc. of National Conference on Innovations in Communication and Computing (ICICC'06), Organized by SNS College of Technology, Coimbatore, India, March 17, 2007.