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### **LIST OF PUBLICATIONS**

1. D. Murali and M. Rajaram, "The Effects of Static Synchronous Compensator and Static Synchronous Series Compensator on Damping Oscillations in Power Systems", AMSE Journal France: Modelling, Measurement & Control Series A: General Physics and Electrical Applications, Vol. 83, No. 1, pp. 43-67, 2010. H-index: 6
2. D. Murali and M. Rajaram, "Damping Improvement by Fuzzy Based Power System Stabilizers Applied in Multi-machine Power Systems", European Journal of Scientific Research, Vol. 55, No. 4, pp. 506-516, 2011. H-index: 30
3. D. Murali and M. Rajaram, "Power System Damping using Neuro-Fuzzy Based Static Synchronous Series Compensator Controllers", European Journal of Scientific Research, Vol. 60, No. 1, pp. 149-158, 2011. H-index: 30
4. D. Murali and M. Rajaram, "Neuro-Fuzzy Based Power System Stabilizers for Damping Oscillations in Multi-machine Power Systems", Journal of Electrical Engineering, Vol. 12, No. 2, pp. 31-38, 2012. H-index-4
5. D. Murali and M. Rajaram, "Adaptive Neuro-Fuzzy Based Damping Improvement using Static Synchronous Series Compensators", International Journal of Electrical Engineering, Vol. 3, No. 1, pp. 27-39, 2010.
6. D. Murali and M. Rajaram, "An Improved Control Scheme Applied to Static Synchronous Series Compensators for Damping of Oscillations in Multi-machine Power Systems", International Journal of Recent Trends in Engineering and Technology, Vol. 4, No. 3, pp. 1-6, 2010.
7. D. Murali and M. Rajaram, "Intelligent Control Schemes for SSSC Based Damping Controllers in Multi-machine Power Systems", International Journal of Engineering Science and Technology, Vol. 2, No. 8, pp. 3788-3796, 2010.
8. D. Murali and M. Rajaram, "Comparison of FACTS Devices for Power System Stability Enhancement", International Journal of Computer Applications, Vol. 8, No. 4, pp. 30-35, 2010.
9. D. Murali and M. Rajaram, "Active and Reactive Power Flow Control using FACTS Devices", International Journal of Computer Applications, Vol. 9, No. 8, pp. 45-50, 2010.
10. D. Murali and M. Rajaram, "Simulation and Implementation of DVR for Voltage Sag Compensation", International Journal of Computer Applications, Vol. 23, No. 5, pp. 26-30,

2011.

11. D. Murali and M. Rajaram, "Comparison of SSSC and d-q Control Scheme Based UPFC for Power Flow Control", *International Journal of Computer Applications*, Vol. 25, No. 5, pp. 35-39, 2011.
12. D. Murali and M. Rajaram, "Transient Energy Analysis for STATCOM and SSSC Applications", *International Journal of Electrical and Power Engineering*, Vol. 3, No. 4, pp. 191-197, 2009.
13. D. Murali, S. Govindasamy, and A. Vasanthi, "Analysis of Multilevel Inverter Topologies Output Voltage Harmonic Reduction", *International Journal of Engineering Research and Industrial Applications*, Vol. 2, No. VII, pp. 13-28, 2009.
14. D. Murali and M. Rajaram, "Comparison of Damping Performance of Conventional and Neuro-Fuzzy Based Power System Stabilizers Applied in Multi-machine Power Systems", *Journal of Electrical Engineering (Slovakia)*, Vol. 64, No. 6, pp. 366-370, 2013. H-index - 19
15. D. Murali and M. Rajaram, "Use of ANFIS Control Approach for SSSC based Damping Controllers Applied in a Two-area Power System", *Journal of Applied Research and Technology (Elsevier Journal)*, Vol. 11, No. 6, pp. 895-902, December 2013. H-index-11
16. D. Murali, "ANFIS Approach Applied to PSS and SSSC Controllers for Power System Damping Improvement", *International Journal of Innovation and Scientific Research*, Vol. 22, No. 1, pp. 24-32, April 2016.
17. D. Murali, "Dynamic Performance improvement of a Two-area Power System by PSS and Series FACTS based Damping Controllers using ANFIS Technology", *Journal of Electrical Engineering*, Vol. 16, No. 2, pp. 262-268, 2016. H-index-4
18. D. Murali, "Comparison of Adaptive Neuro-Fuzzy based PSS and SSSC Controllers for Enhancing Power System Oscillation Damping", *AMSE Journals France, Advances in Modeling: Series C. Automatic Control (theory and applications)*, Vol. 71, No. 1, pp. 24-38, 2016. H-index-4
19. T. Kanimozhi and D. Murali, "Performance Comparison of Single-phase 19- and 21-level Asymmetrical Cascaded Reduced Switch Multilevel Inverter Topologies for THD Reduction", *Journal of Electrical Engineering*, Vol.16, No.4, pp.384-393, 2016.
20. A. Dhivya and D. Murali, "Comparative Analysis of Sinusoidal PWM Strategies for Three Phase Symmetrical Cascaded H-Bridge Seventeen Level Inverter with Reduction of Switches", *Journal of Electrical Engineering*, Vol. 17, No. 1, pp.180-188, 2017.
21. G. Sujatha and D. Murali, "Automated EB Billing and Supply Control using Power Line Communication", *International Journal of Computer Applications*, Vol. 45, No. 7, pp. 44-50, 2012.
22. G. Sujatha and D. Murali, "A Practical Buffer Circuit Implementation in a High Voltage Area with Microcontroller Application", *International Journal of Applied Engineering and Technology*, Vol. 2, No. 2, pp. 7-10, 2012.
23. V. Sankar and D. Murali, "Analysis of Bidirectional DC-DC Converter for Low Power

- Applications”, *International Journal of Power Control and Computation*, Vol. 6, No. 2, pp. 97-103, 2014.
24. K. Kirubanantham and D. Murali, “Simulation Analysis of Dynamic Voltage Restorer using Z-Source Inverter”, *Indian Journal of Electronics and Electrical Engineering*, Vol. 2, No. 2, pp. 59-62, 2014.
  25. B. Janaranjani and D. Murali, “Dynamic Modeling and Simulation Analysis of a Non-ideal Buck Switching Regulator”, *International Journal of Applied Engineering Research*, Vol. 10, No. 10, pp. 9570-9574, 2015. H-index-9
  26. K. Srilatha and D. Murali, “Simulation Analysis of 7-level Reduced Switch MLI Topology using Level Shifting Carrier-based PWM Techniques”, *International Journal of Applied Engineering Research*, Vol. 10, No. 10, pp. 9575-9579, 2015. H-index-9
  27. B. Janaranjani and D. Murali, “Dynamic Performance Analysis of DC-DC Boost Regulator Operating in CCM”, *Australian Journal of Basic and Applied Sciences*, Vol. 9, No. 21, Special ICEAS, pp. 196-201, 2015. H-index-17
  28. K. Srilatha and D. Murali, “Simulation Analysis of Nine Level MLI with Six Switches for THD Reduction”, *Australian Journal of Basic and Applied Sciences*, Vol. 9, No. 21, Special ICEAS, pp. 201-208, 2015. H-index-17
  29. R. Sudha, D. Murali, I. Thangaraju, and K. Goverthan, “Modified Invasive Weed Optimization of Phasor Measurement Units”, *International Journal of Applied Engineering Research*, Vol. 10, No. 9, Special Issue, pp. 9120-9123, 2015. H-index-9
  30. A. Dhivya and D. Murali, “Average Current Mode Control Technique Applied to Boost Converter for Power Factor Improvement and THD Reduction”, *International Journal of Innovative Science, Engineering and Technology*, Vol. 3, No. 2, pp. 267-275, February 2016.
  31. A. Dhivya and D. Murali, “Dynamic Analysis of Boost Converter for Power Factor Improvement and THD Reduction”, *Singaporean Journal of Scientific Research, Journal of Selected Areas in Microelectronics*, Vol. 8, No. 2, pp. 27-34, 2016.
  32. P. Panneer Selvam and D. Murali, “Analysis of Wind and PV Energy System with Cuk and Sepic Converters using FOINC MPPT Technique”, *Singaporean Journal of Scientific Research, Journal of Selected Areas in Microelectronics*, Vol. 8, No. 1, pp. 88-93, 2016.
  33. S. Shameer and D. Murali, “Performance Analysis of a Highly Efficient Dual Output Buck Converter with ZVS Operation for LED Lighting Applications”, *Journal of Electrical Engineering*, Vol.19, No.1, pp.136-145, 2019. H-index: 4
  34. M. Sowmiya and D. Murali, “Performance Analysis of a Non-inverting DC-DC Buck-Boost Converter with PI Controller”, *International Journal of Emerging Technologies in Engineering Research*, Vol. 7, No. 7, pp. 6-12, July 2019.
  35. D. Murali and M. Bhuvaneshwaran, “Analysis of a Reduced Switch Count Single Phase Cascaded H-Bridge 33-level Inverter Topology with Asymmetrical Configuration of DC Sources”, *International Journal of Innovations in Engineering and Technology*, Vol. 13, No. 4, pp. 95-103, July 2019.

36. A. Sowmya and D. Murali, "A High Voltage Gain Step-up Resonant DC-DC Converter Topology with Reduced Switch Count", *International Journal for Modern Trends in Science and Technology*, Vol. 5, No. 12, pp. 26-31, December 2019.
37. D. Murali and S. Annapurani, "A High Voltage Gain Rectifier-fed Two-stage Step-up DC-DC Converter with PI Controller", *International Journal of Engineering Applied Sciences and Technology*, Vol. 4, No. 8, pp. 160-164, December 2019.
38. R. Saktheeswaran, D. Murali and K. Ramash Kumar, "MATLAB / SIMULINK Study and Implementation of SMC for Two-level Cascaded Positive Output Boost Converter", *Journal of Advanced Research in Dynamical and Control Systems*, Vol. 11, No. 8 (Special Issue), pp. 2280-2292, December 2019.
39. R. Saktheeswaran and D. Murali, "Experimental Validation of Multi-loop Controllers for Two-Level Cascaded Positive Output Boost Converter", *Journal of Power Electronics (Springer)*, Vol. 20, No. 2, pp. 350-364, March 2020.