

**Dr.K.Udayakumar**  
**Professor**  
**Electrical and Electronics Engineering**  
**Anna University**  
**Chennai**  
**PIN: 600025**  
**Mobile: 9444222262**  
**E mail: [k\\_udhayakumar@annauniv.edu](mailto:k_udhayakumar@annauniv.edu)**  
**Area of Specialization: Power Systems Engineering**

Papers Published in Journals

Research Papers Published in International Journals : 25

Research Papers Published in National Journals : 0

1. Sreedhar, T & Udhayakumar K, "Supply Voltage Fluctuation Immune SHEPWM For Multilevel Inverters Working With Fluctuating Dc Sources", International Journal Of Advanced Engineering Technology, published by International Journal Of Advanced Engineering Tech. Vol. 7, Issue 1, pp. 622-631 (2015).
2. Sreedhar T & Udhayakumar K, " Negative Harmonics Injection Cuddled Equal Area Criteria Based Selective Harmonic Elimination Pulse Width Modulation Techniques For Multilevel Cascaded Inverters", Journal of Computational And Theoretical Nanoscience, published by Journal of Computational And Theoretical Nanoscience. pp. Ref number CTN 20-980-112 (2016).
3. Sreedhar T & Udhayakumar K, "Genetic Algorithm Based Selective Harmonic Elimination Technique For Multilevel Inverters With Unequal Voltage Sources", Association for advancement of Modeling and Simulation techniques in Enterprises, published by AMSE. pp. Paper number 13511 (2016).
4. T. Ramachandran T. Sreedhar, K. Udhayakumar, E. Aswini, "A Single Phase Modified H-Bridge Seven Level Inverter with Reduced Power Electronic Switches", International Journal of Control Theory and Applications, published by International Science Press, Gurgaon, India. Vol. 10, Issue 29, pp. 273 - 283 (2017).