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List of Publications:

1. Power transition X filling based selective Huffman encoding technique for test-data compression and Scan Power Reduction for SOCs, L Sivanandam, S Periyasamy, UM Oorkavalan, Microprocessors and Microsystems 72, 1029372, 2020.
2. Test data compression for digital circuits using tetrad state skip scheme, L Sivanandam, UM Oorkavalan, S Periyasamy, Design Automation for Embedded Systems 21 (3-4), 197-211, 1, 2017.
3. Three State Skip Logic Built-In Self-Test Scheme for Combinational Circuits, S Lokesh, UMA Maheswari, P Sakthivel, Asian Journal of Information Technology 15 (15), 2693-2698, 2016.
4. A. Ramya-Terrance, S. Shrivastava, A. Kumari and L. Sivanandam. "Competitive Analysis of Retail Websites through Search Engine Marketing", Ing. Sol., Vol. 14, no. 25, pp. 14, May 2018.
5. S. Anbuchelian, S. Lokesh and M. Baskaran, "Improving security in Wireless Sensor Network using trust and metaheuristic algorithms," 2016 3rd International Conference on Computer and Information Sciences (ICCOINS),Kuala Lumpur, 2016, pp. 233-241, doi: 10.1109/ICCOINS.2016.7783220.
