Publications List of **Dr.A. Dheepanchakkravarthy (Affiliated institution)** in the last 5 years

1. **Dheepanchakkravarthy, A**., Jawahar, M.R., Venkatraman, K., Selvan, M.P. and Moorthi, S "Performance evaluation of FPGA-based predictive current controller for FLDSTATCOM in electric distribution system", IET Generation, Transmission and Distribution (SCIE), 13 19 4400 - 4409 OCT 2019.
2. **Dheepanchakkravarthy, A**., Selvan, M.P. and Moorthi, S., "Alleviation of Current Quality Issues Caused by Electric Arc Furnace Load in Power Distribution System Using 3-Phase Four-Leg DSTATCOM", Journal of the Institution of Engineers (India) - Series B - Springer (SCImego/Scopus Indexed) 100 01 9 - 22 JUN 2018.
3. **Dheepanchakkravarthy, A**., Akhil, S., Venkatraman, K., Selvan, M.P. and Moorthi, S. "Performance Analysis of FPGA Controlled Four-leg DSTATCOM for Multifarious Load Compensation in Electric Distribution System Engineering Science and Technology", an International Journal - Elsevier (SCIE) 21 04 692 - 703 MAY 2018.
4. **Dheepanchakkravarthy, A.**, Venkatraman, K., Selvan, M.P., Moorthi, S. and Venkatakirthiga, M. "Capability Evaluation of Four-leg DSTATCOM for Compensating Multifarious Loads", Australian Journal of Electrical and Electronics Engineering - Taylor and Francis 13 04 229 - 243 OCT 2017.
5. Naveen Kumar, A., Abdul Raghuman, S., **Dheepanchakkravrathy, A.** and Jebasalma, "A Modern Approach of a Three Phase Four Wire DVSI for Power Quality Improvement Using SRF Theory", International Journal of Engineering Research and Technology 05 03 678 - 683 MAR 2016.
6. **Dheepanchakkravrathy, A**. and Jebasalma. "A Modern Approach of a Three Phase Four Wire DSTATCOM for Power Quality Improvement Using T Connected Transformer", International Journal of Engineering Inventions 01 04 80 - 90 SEP 2012
7. Saranya, V., Vijay, M., Vijayarajan, M., **Dheepanchakkravarthy, A**. and Purushothaman, K "Hysteresis Current Controller Based Power Factor Correction Control for Compensating LED Load", International Journal of Advanced Research in Science and Engineering 07 06 89 - 99 MAR.
8. Vijay, S., **Dheepanchakkravarthy, A**., Padmavathi, R., Rajapriya, G. and Preveen kumar, M., "Design and Implementation of SPWM Based Asymmetrical Multilevel Inverter with Less Switches", International Journal of Advanced Research Trends in Engineering and Technology 5 8 209 - 221 MAR.