Name : Dr. S.Baulkani Designation : Professor

Department: Electronics and Communication Engineering,

Address: Government College of Engineering, Tirunelveli -627007

Mobile: 9442813661 E-mail: baulkani@gcetly.ac.in

Publications:

- 1. M M Gowthul Alam, S Baulkani "A Hybrid Approach for Web Document Clustering using K-means and Artificial Bee Colony Algorithm" International Journal of Intelligent Engineering and Systems, 2016
- 2. M M Gowthul Alam, S Baulkani, "Geometric structure information based multi-objective function to increase fuzzy clustering performance with artificial and real-life data" Soft Computing, Springer, 2018
- 3. MM Gowthul Alam, S Baulkani, "Reformulated query-based document retrieval using optimised kernel fuzzy clustering algorithm" International Journal of Business Intelligence and Data Mining, 2017
- 4. R. Uma Mageswari, S. Baulkani, "Routing Technique for data Transmission under Jammer in Multihop wireless network", Journal of Ambient Intelligence and Humanized Computing ,2020
- 5. M M Gowthul Alam, S Baulkani "Local and Global characteristics based-Kernel hybridization to increae optimal support vector machine performance for stock market prediction", Knowledge and Information Systems ,2019.
- 6. M M Gowthul Alam, S Baulkani "Automated Learning Methodology for Document Clustering Analysis", International Journal of Advances in Natural and Applied Sciences, 2016
- 7. G. Jemilda and S. Baulkani, Integration of New Moving Object Segmentation and Classification Techniques using Optimal Salp Swarm-Based Feature Fusion with Linear Multi K-SVM Classifier, EURASIP Journal on Image and Video Processing (Springer), 2020
- 8. G. Jemilda, S. Baulkani, D. George Paul and J. Benjamin Rajan, Tracking Moving Objects in Video, Journal of Computers ,2017.
- 9. Fathima Mubarakkaa M, Gowthul Alam M.M and Baulkani S, "Glioblastoma Brain Tumor Modeling Approach", International Journal of Advanced Research Trends in Engineering and Technology 2017.
- 10. S.P. Valan Arasu and S. Baulkani," FPGA implementation of a low power and high speed Hybrid Multiplier for Image Processing Applications, Journal of Communications Technology, Electronics and Computer Science, 2017.