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List of Publications

- 1. Muralikumar, Kola, and **P. Ponnambalam**. "Analysis of Cascaded Multilevel Inverter with a Reduced Number of Switches for Reduction of Total Harmonic Distortion." IETE Journal of Research (2020): 1-14.
- 2. Kumar, M. Praveen, M. Manimozhi, **P. Ponnambalam**, and G. Gokulakrishnan. "A Novel PID Controller Designed via Polynomial Approach for Stable/Unstable Second-Order Process with Time Delay." In *Soft Computing for Problem Solving*, pp. 81-92. Springer, Singapore, 2019.
- 3. Viswanath, Y., K. Muralikumar, **P. Ponnambalam**, and M. Praveen Kumar. "Symmetrical cascaded switched-diode multilevel inverter with fuzzy controller." In Soft computing for problem solving, pp. 121-137. Springer, Singapore, 2019.
- 4. Babu, Challa, and **P. Ponnambalam**. "The theoretical performance evaluation of hybrid PV-TEG system." Energy conversion and management 173 (2018): 450-460.
- 5. Priya, Murthy, **P. Ponnambalam**, and Kola Muralikumar. "Modular-multilevel converter topologies and applications—a review." IET Power Electronics 12, no. 2 (2018): 170-183.
- 6. Babu, Challa, and **P. Ponnambalam**. "The role of thermoelectric generators in the hybrid PV/T systems: A review." Energy conversion and management 151 (2017): 368-385.
- 7. Velamuri, Suresh, S. Sreejith, and **P. Ponnambalam**. "Static economic dispatch incorporating wind farm using flower pollination algorithm." Perspectives in Science 8 (2016): 260-262.