List of Papers / Publications of Dr D.Thirugnana Murthy (Last 5 years) :

1. Nagaraj R, **D.Thirugnanamurthy**, M.M. Rajput, "Modeling of Renewables based Hybrid Power System with Desalination Plant load using Neural Network", Int. J. Distributed Generation & Alternative Energy, Taylor & Francis Publication, Dec 2018,Vol.34(1), ISSN 2156-3306.

2. Nagaraj R, **D.Thirugnanamurthy**, M.M. Rajput, M.Sai Baba, "Brackish Water RO plant as a variable load for Renewables based Hybrid Power System for increased power output", Int. J. Desalination and Water Treatment, Taylor & Francis (Desalination) Publication, Vol.88 (2017) pp:33-40 ISSN: 1944-3994, doi: 10.5004/dwt.2017.21414.

3. Nagaraj R, **D.Thirugnanamurthy**, M.M. Rajput, M.Sai Baba, "Modelling of High Step-up Converter in closed loop for Renewable Energy Applications", Int. J. Environment, Development and Sustainability, Springer Link Publishers, 2017, Vol.19, Issue : 6,pp: 2475 –2485, ISSN : 1387-585X, doi:10.1007/s10668-016-9866-8.

4. Nagaraj R, **D.Thirugnanamurthy**, M.M. Rajput, B.K. Panigrahi, "Techno Economic analysis of hybrid power system sizing applied to small desalination plants for sustainable operation", Int.J. of Sustainable Built Environment (2016), Elsevier Publishers, , 2016, Vol.5, Issue 2, ISSN: 2212- 6090, pp 269-276, doi: 10.1016/j.ijsbe.2016.05.011

5. G. Sumathi, L. Srivani, **D. Thirugnana Murthy** and K. Madhusoodanan, Multi-Corner Timing Analysis based Reliability Estimation of PUFs Implemented in FPGAs, International Journal of Emerging Technology in Computer Science and Electronics, vol. 23, no. 5, pp. 195-200, 2016

6. Suvadip Roy, L. Srivani, **D.Thirugnana Murthy,** "Digital Filter Design Using FPGA", International Journal of Engineering and Innovative Technology, Volume 5, Issue 4, October 2015, Pages 78 -82.

7. Sudheer Patri*, M. Mohana, K. Kameswari, S. Suresh Kumar, S. Narmadha, R. Vijayshree, C. Meikandamurthy, A. Venkatesan, K. Palanisami, **D. Thirugnana Murthy**, B. Babu, V. Prakash, K. K. Rajan, Simplified Method for Measuring the Response Time of SCRAM Release Electromagnet in a Sodium Cooled Fast Reactor, published in "Nuclear Engineering and Design" – Ref. 285(2015) pp. 150-157

8. S.Sobiya, T.Sridevi, R.Lakshmipathy, A.Sivagami, E.M.T. Sirisha and **D.Thirugnana Murthy**, Design and Development of Analog Input Card with Isolation, International Journal of Emerging Technology in Computer Science and Electronics (ISSN:0976-1353) , vol:13,Issue4, March 2015, pp244-247.