ACADEMIC VITAE

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EDUCATIONAL DETAILS

1) *Course:* **Doctor of Philosophy**
   Specialisation: Wireless Communication
   Faculty/ Institution: Dept. of ECE, Pondicherry Engineering College
   University: Pondicherry University, Pondicherry, India

2) *Course:* **Master of Technology** (ECE)
   Faculty/ Institution: Dept. of ECE, Pondicherry Engineering College
   University: Pondicherry University, India

3) *Course:* **Bachelor of Engineering** (ECE)
   Faculty/ Institution: Dept. of ECE, St.Joseph's College of Engineering
   University: University of Madras, India

TECHNICAL PUBLICATIONS

Journals

1. **S.Tamilselvan**, V.Kamalakannan, "Security enhancement of text message based on matrix approach using elliptic curve cryptosytem" *Elsevier Procedia Material Science 10 (*2015), pp.489-496. ISSN: 2211-8128

2. **S.Tamilselvan,** R.Gajalakshmi, "Resource Allocation for OFDMA Macro/Femto Networks" *International Journal of Applied Engineering Research*, Volume-10, Issue-63, 2015, ISSN 0973-4562.

3. **S.Tamilselvan,** V.Anilkumar, V.Kamalakannan and V.M.S.N.Pavan Kumar.CH, "Design Analysis and FPGA Implementation of N Bit Vedic Multiplier Based on Different Adder Architectures", *International Journal of Advanced Research in*

Computer and Communication Engineering (IJARCCE), Volume-4, Issue-8, August 2015, ISSN 2319-5940. (Impact Factor: 4.582).

4. **S. Tamilselvan**, CH.V.M.S.N. Pavan Kumar, "A Review on 3GPP Femtocell Networks and its Technical Challenges", Indian Journal of Science and Technology, ISSN: 0974 -5645, Vol-9, Issue-16, April 2016. (**Scopus**)

5. **S.Tamilselvan**, Erna Guguloth, "Progressive fault Tolerant Filter Based on MCM", Australian Journal of Basic and Applied Sciences, 10(12) July 2016, Pages: 155-159, ISSN:1991-8178 EISSN: 2309-8414. (**Scopus**)

6. **S.Tamilselvan**, V.Kamalakannan, "Security Architecture for the Cloud Integrated Internet of Things", Indian Journal of Science and Technology, Vol 9(45), December 2016, p. 1-13, ISSN: 0974 -5645, DOI: 10.17485/ijst/2016/v9i45/106147. (**Scopus**)

7. **S.Tamilselvan**, V.Kamalakannan," Implementation of Menezes Vanstone elliptical curve cryptography algorithm using matrix mapping method", International Journal of Knowledge Engineering and Soft Data Paradigms, Vol. 5, Nos. 3/4, 2017. ISSN: 1755-3229 **(Inderscience)**

8. **S.Tamilselvan**, V.Kamalakannan, "Design of Secured and Intelligent Architecture for Security in Perceptual Layer of the Internet of Things", Indian Journal of Science and Technology, Vol 10(1), January 2017, page 1-9. ISSN: 0974 -5645, DOI: 10.17485/ijst/2017/v10i1/109387. (**Scopus**)

9. **S.Tamilselvan**, V.Kamalakannan, "FPGA IMPLEMENTATION OF MODIFIED ELLIPTIC CURVE DIGITAL SIGNATURE ALGORITHM", FACTA UNIVERSITATIS, Series: Electronics and Energetics Vol. 30, No 1, March 2017, pp. 1 - 17.

10. **S.Tamilselvan,** Ch.V.M.S.N.Pavan Kumar, P.Prabagar, "Interference Mitigation in Femtocell Networks by Efficient Frequency Reuse", International Journal of Knowledge Engineering and Soft Data Paradigms, ISSN: 1755-3229, Vol. 5, No.3/4, pp: 248-261, 2017. **(Inderscience)**

11. **S.Tamilselvan,** Ch.V.M.S.N.Pavan Kumar, "Performance Comparison of Paraunitary Analysis Filter Bank Based Spectrum Sensing Technique over

Multipath Channels", Indian Journal of Science and Technology, ISSN: 0974 - 5645, Vol-10, Issue-1, Jan., 2017. (**Scopus**)

12. **S.Tamilselvan,** Ch.V.M.S.N.Pavan Kumar, "Paraunitary Filter Bank Based Spectrum Sensing to Avoid Interference in Macro-Femto Cellular Networks", Journal of Advanced Research in Dynamical and Control Systems, ISSN: 1943023X, Vol-, Issue-12, Pages: 708-721, August 2017. (**Scopus**)

13. **S.Tamilselvan,** Ch.V.M.S.N.Pavan Kumar, "A Game Theoretic Approach for Dynamic Spectrum Allocation in Hybrid Access Cognitive Femtocell Network", Journal of Computational and Theoretical Nanoscience Vol. 15, 6–7, 2018.

14. **S.Tamilselvan**, V.Kamalakannan, "FPGA Implementation of Elliptic Curve Crypto processor for Perceptual Layer of the Internet of Things", EAI Endorsed Transactions on Security and Safety, Vol. 15, No.6/7, pp: 2092-2098, 15 October 2018.

15. **S. Tamilselvan** and S. Oudayacoumar, "Fabrication Design of Axially Rotated Square Resonator Based Compact Ultra-Wideband BPF Using Tight coupling", *Journal of computational and theoretical Nano science,* Vol.No.15, Issue No.5, pp.no. 1695-1699, 2018. *(Scopus)*

16. **S. Tamilselvan** and S. Oudayacoumar, "Development of Band reject Filter to Mitigate the effect of WLAN in UWB Receivers", International Research Journal of Electronics & Computer Engineering (ISSN 2412-4370 Online), Vol. No. 4, Issue No. 2, pp.1-5, June 2018. **(Cross Ref)**

17. **S. Tamilselvan** and S. Oudayacoumar, "Miniaturized conductor backed CPW high pass filter for C-band satellite applications" Microwave and Optical Technology Letters (MOTL), 2019.**Wiley Publications, USA. SCI Indexed.**

18. **S. Tamilselvan** and S. Oudayacoumar, "Development of Open stub loaded Square Resonator based Dual Band filter with a Notch band to reject 802.11a", Journal of Materials Today, 2019. *Scopus (Elsevier)*

19. **S. Tamilselvan** and S. Oudayacoumar, "Efficient Wideband Filter Using Closed LoopResonator with coupling lines", International Journal of Intelligent Enterprise (IJIE),2019. *Scopus (Inderscience)*

20. **S.Tamilselvan**, Ismayil Siyad C and Sneha V.V., "Multilayer Perceptron Scheme for Beamforming and Channel Estimation of Massive MIMO", International Journal of Recent Technology and Engineering (IJRTE), Volume 8, Issue 2S6, Pages 467-471, July 2019. *Scopus*

21. **S.Tamilselvan**, Ismayil Siyad C, "Adaptive high-resolution 3D channel estimation for massive MIMO network: Machine learning method versus deep learning" Journal of Ambient Intelligence & Humanized Computing (AIHC). **(Communicated)**

22. **S.Tamilselvan**, Erna Guguloth, "A Novel FPGA implementation of error reduction in 8,16 and 32 –bit scalable approximate rounding based TOSAM (3,7) and (4,8) multiplier", International journal of future generation communication and networking. (Web of science)

23. **S.Tamilselvan**, Erna Guguloth, "Implementation of a modified Booth multiplier design using adaptive hold logic and razor flip flop in system generator", International Journal of Electronics, Electrical and Computational System, Volume 6, Issue 9, pp. 296-299, September 2017. ISSN 2348-117X **(UGC Approved Journal)**

24. **S.Tamilselvan**, Erna Guguloth, "Implementation of high speed low power systolic multiplier based on Irreducible Trinomials", International Journal of Recent Technology and Engineering, Volume-8 Issue-5, pp. 223-225, January 2020. **(Scopus)**

25. **S.Tamilselvan**, S.Yazhinian, "Performance Enhancement of Bidirectional Noc Router with and without Contention for Reconfigurable Coarse Grained Architecture", Indonesian Journal of Electrical Engineering and Computer Science, Vol. 11, No. 3, pp1068-1074, September 2018. ISSN:2502-4752 **[Scopus]**

26. **S.Tamilselvan**, S.Yazhinian, "Design of Coarse Grained Architecture and Bidirectional Routing for Low Power and High Speed Applications", International Journal of Pure and Applied Mathematics, special issue on 'Intelligent Systems and Engineering' Volume 119. No. 14d, Pp1387-1398, 2018. ISSN:1314-339**[Scopus]**

27. **S.Tamilselvan**, S.Yazhinian, "Design of Reusable Context Pipelining for Coarse Grained Reconfigurable Architecture", International Journal for Research in Applied Science & Engineering Technology, Volume 6, Issue 4, Pp3584-3589, April 2018. ISSN: 2321-9653

28. **S.Tamilselvan**, S.Yazhinian, "Implementation of Gated Clock Technique for Low power Applications in Coarse Grained Architecture", Journal of Computational and Theoretical Nanoscience, Volume 15, No. 4, Pp1666-1674, May 2018. ISSN:1546-1955**[Scopus]**

29. **S.Tamilselvan,** S. Oudaya coumar, "Metallic Ring Backed Co-Planar Waveguide Satellite L-Band and S-Band Low Pass Filter", *International Journal of Electronics Letters, Taylor & Francis*, 2020. *(**Article in press**)*

30. **S.Tamilselvan,** S. Oudaya coumar, "A Compact Conductor Backed CPW Based Dual-Band Pass Filter for Satellite S and C-Band", Journal of Electrical Systems and Information Technology, **Springer**, vol.7, no.5, 2020.

Book Chapters

1. **S.Tamilselvan**, S.Yazhinian, "An Implementation of Bidirectional NOC Router for Reconfigurable Coarse-Grained Architecture by using Vedic Multiplier" in the Book titled Recent Trends and Advances in Artificial Intelligence and Internet of Things, Intelligent System Reference Library 172, 2020. **[Springer Series] ISSN:** 1868-4394.

2. **S.Tamilselvan**, S.Yazhinian, "Pursuance investigation of Bi-NoC with clock gating and RCP in CGRA", in the Book titled Information Management and Machine Intelligence, Springer, Proceedings of ICIMMI 2019. **[Springer Series]** (Article in Press)