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## **Journal Publications:**:

1. Bharani dharan N, Chinna thambi M, Rajaram S, 'FPGA Implementation of Novel Discrete Phase-Locked Loop', <u>Intelligent Computing and Applications Advances in Intelligent Systems and Computing Volume 343, 2015, pp 223-229.</u>

- 2. Chinna thambi M, Bharani dharan N, Rajaram S, 'ASIC Implementation of Fast and Area Efficient Numerically Controlled Oscillator Based on CORDIC', International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.2 (2015) pp. 1365-1369.
- 3. K.Kalyani, S.Rajaram, S.N.V.Saipratap, "A novel QR factorization approach to Multi-robot localization and mapping" International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.2 (2015) pp. 1356-1360.
- 4. D.Gracia Nirmala Rani, Amalin Marina, V.Dharshana & S.Rajaram,"Testing of Reversible Logic Circuits using automatic Test pattern generation techniques" 2015, International journal of Applied Engineering Research, Vol.27, No.4,2015,pp 553 558.
- 5. Gracia Nirmala Rani, D & Rajaram, S, 'A Novel Differential Evolution based optimization algorithm for Non-Sliceable VLSI 1Floorplanning' 2015, Iranian Journal of Science and Technology Transaction A, Article 5, Volume 39, Issue 3.1, Autumn 2015, Page 375-382.
- 6. Venkatasubramani, VR, Vignesh, K, Premkumar, N & Rajaram, S 2015, 'A High-performance Elliptic Curve Cryptographic Processor for FPGA Platform', Advances in Intelligent Systems and Computing, Intelligent Computing and Applications, Springer, vol.343, pp.307-312.
- 7. Gracia Nirmala Rani.D & Rajaram.S 2016, 'Analysis and Design Of VLSI Floorplanning Algorithms For Nano-Circuits' International Journal of Advanced Engineering Technology, Vol. VII/Issue I/Jan.-March., 2016/527-532.

8. Suganya, R, Rajaram.S, Sheik Abdullh.A & Rajendran. V, 2016, "A Novel Feature Selection method for Predicting Heart Diseases with Data mining Techniques", Asian Journal of Information Technology (AJIT) Updated Annexure list, 2016.