

<b>Name of the Expert</b>	<b>Dr.P.Kalpana Devi</b>
<b>Designation</b>	<b>Associate Professor</b>
<b>Department and Institute</b>	<b>Electronics and Communication Engineering , School of Electrical and Communication , VelTech Rangarajan Dr.Sagunthala R &amp; D Institute of Science and Technology , Avadi</b>
<b>Mobile No.</b>	<b>9962688222</b>
<b>Email</b>	<a href="mailto:drkalpanadevip@veltech.edu.in">drkalpanadevip@veltech.edu.in</a>

## List of Publications

<b>S.No.</b>	<b>Topic of Publication</b>	<b>Published in</b>	<b>Year of Publication</b>	<b>Volume reference</b>
1	Design of Reconfigurable antenna for SDR applications	International Journal of Science and Technology <b>Accepted (Scopus Indexed)</b>	March 2020	
2	Design of IoT based Garbage Segregation for Automatic Smart Trash Bin using NI LabVIEW	International Journal of Innovative Technology and Exploring Engineering (IJITEE) <b>(Scopus Indexed)</b>	September 2019	Volume-8 Issue-11
3	Linguistic Effects Based Novel Filter for Hearing Aid to Deliver Natural Sound and Speech Clarity in Universal Environment	Wireless Personal Communications <b>SCI Indexed SPRINGER</b>	July 2017	Vol 94
4	A Novel Frequency Range Reconfigurable Filter for Hearing Aid to deliver natural sound and speech clarity in Universal Environment	Journal of Computational and Theoretical Nano science (CTN) <b>Annexure-I</b>	July 2016	Vol 13
5	Improved non uniform Transmultiplexers for Channelization	Indian Journal Of Science And Technology <b>(Annexure –II) Scopus Indexed</b>	March 2015	Vol(8)
6	Improved Wavelet-Cusum Spectrum Sensing Algorithm in Cognitive Radio Network	International Journal of Engineering Research & Technology	Dec 2014	Vol(4)2
7	Hardware Optimization for Reconfigurable Polyphase FFT design in SDR	International Journal Of Research in Engineering and Technology	Dec 2013	

8	FPGA implementation of coefficient decimated Polyphase filter bank structure for multi standard communication receiver	Journal of Theoretical and Applied Information Technology (Annexure –II) Scopus indexed	June 2014	64
9	Polyphase Wideband Channelisation Architecture For Cognitive Radio	International Journal of Science and Research Technology	Feb 2014	2
10	Design of improved trans multiplexers for cognitive radio	Indian Journal of Applied Research	June 2014	4
11	Design Of Cosine Modulated Filter Bank For Uniform Channelization	ICCEECE2014 Singapore	2014	59
12	Flexible reconfigurable filter architecture for SDR receiver	MICC 2013 ( <a href="#">Malaysia International Conference</a> )	Nov - 2013	10 – 13
13	Frequency response marking techniques.	Indian Journal of Engineering and Technology	2013	2
14	FIR Filter Design for Wireless Communication Applications	IETE 45 <sup>TH</sup> Symposium	April 2014	-
15	Reconfigurable Polyphase Filter Bank Structure for Software Defined Radio	CARCN 2014 Chennai	March 2014	-

## Funding Details

Sl. No.	Title of Project /Workshop/ Conference/FDP	Sponsor Name	Amount of Grant (Rs. in Lakhs)
1.	Design of Reconfigurable SDR receiver	Veltech University	Rs.1,80,000
2	FDP on Electromagnetics, Antenna	Prathyusha Engineering College	Rs.5000
3	Project- Octocopter for Landmine detection	Prathyusha Engineering College	Rs.25,000
4	MODROBS – VLSI Lab	AICTE	Rs.5,00,000
5	Recent trends of EDA tools in VLSI design	BRNS	Rs.40000