

## **Paper Publications details**

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1. Kudithi, T., Sakthivel, R. "An efficient hardware implementation of the elliptic curve cryptographic processor over prime field  $F_p$ ", International Journal of Circuit Theory and Applications, 2020, 48(8), pp. 1256–1273
2. Keerthy Rai, V., Sakthivel, R. "Design of artificial neuron network with synapse utilizing hybrid CMOS transistors with memristor for low power applications", Journal of Circuits, Systems and Computers, 2020, 29(12), 2050187
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4. Sakthivel, R., Ragunath, G., "Low power area optimized and high speed carry select adder using optimized half sum and carry generation unit for FIR filter", Journal of Ambient Intelligence and Humanized Computing, 2020
5. Vanitha, M., Sakthivel, R., Mangayarkarasi, R., Sharma, S. "Nonlinear System Modelling Using Programmable Hardware for Soft Computing Applications", Advances in Intelligent Systems and Computing, 2020, 1057, pp. 293–306
6. Bhusal, M., Rohith, R., Sakthivel, R. "Single Bit Fault Detecting ALU Design using Reversible Gates", International Conference on Emerging Trends in Information Technology and Engineering, ic-ETITE 2020, 2020, 9077903
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8. Kudithi, T., Sakthivel, R., "An efficient hardware implementation of finite field inversion for elliptic curve cryptography", International Journal of Innovative Technology and Exploring Engineering, 2019, 8(9), pp. 827–832
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10. Sreejeesh, S.G., Sakthivel, R., Kidav, J.U. "Beamforming algorithm architectures for medical ultrasound", International Journal of Innovative Technology and Exploring Engineering, 2019, 8(12), pp. 2452–2459