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No. of Journal Publications: 6 (2015- 2020)

1. Dr.P.Rajeswari et.al, “Performance Analysis of Microstriplines Interconnect Structure with Novel Guard Trace as Parallel Links for High Speed Dram Interfaces” Wireless Personal Communications, Pages 1-11, DOI: <https://doi.org/10.1007/s11277-020-07025-7>, January 2020.
2. Dr.P.Rajeswari et.al, “Signal Integrity Analysis of High Speed Interconnects in SATA Connector”, International Research Journal of Engineering and Technology (IRJET), Vol: 06 Issue: 05, ISSN: 2395-0072, May 2019.
3. Dr.P.Rajeswari et.al, “Chapter-6 Mitigation Techniques to Reduce Crosstalk in SATA Interconnects”, Advances In Engineering Technology, Pages: 105, 2019
4. Dr.P.Rajeswari et.al, “Analytical and Experimental Study on Suppression of Electromagnetic Interference on High Speed Printed Circuit Board for Wireless Communication Systems” Wireless Personal Communications, Vol: 92, Issue: 2, Pages 333-349, January 2017.
5. Dr.P.Rajeswari et.al, “A Novel Trident Incurvature Shape DGS to Suppress Radiated Emission in High Speed Printed Circuit Board” Circuits and Systems, vol:7, Issue : 10, pp:3026-3037, doi: 10.4236/cs.2016.710258, August 2016.
6. Dr.P.Rajeswari et.al, “Comparative study of crosstalk reduction techniques in RF printed circuit board using FDTD method”, International Journal of Antennas and Propagation, Volume 2015, Article ID 294590, <https://doi.org/10.1155/2015/294590>, Jun 2015.