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**List of Publications**

1. Parmar, Y., Natarajan, S., & Sobha, G. (2019). DeepRange: deep-learning-based object detection and ranging in autonomous driving. IET Intelligent Transport Systems, 13(8), 1256-1264.
2. Natarajan, S., Annamraju, A. K., & Baradkar, C. S. (2018). Traffic sign recognition using weighted multi-convolutional neural network. IET Intelligent Transport Systems, 12(10), 1396-1405.
3. Sravan, M. S., Natarajan, S., Krishna, E. S., & Kailath, B. J. (2018). Fast and accurate on-road vehicle detection based on color intensity segregation. Procedia Comput. Sci, 133, 594-603.
4. Prabhakar, G., Kailath, B., Natarajan, S., & Kumar, R. (2017, July). Obstacle detection and classification using deep learning for tracking in high-speed autonomous driving. In 2017 IEEE Region 10 Symposium (TENSYP) (pp. 1-6). IEEE.
5. Natarajan, S., Kailath, B. J., Kumar, D., & Kumar, R. (2016, September). Accelerating occupancy grid map computation with gpu for real-time obstacle detection. In 2016 22nd Annual International Conference on Advanced Computing and Communication (ADCOM) (pp. 38-43). IEEE.
6. Shene, T. N., Sridharan, K., & Sudha, N. (2016). Real-time SURF-based video stabilization system for an FPGA-driven mobile robot. IEEE Transactions on Industrial Electronics, 63(8), 5012-5021.
7. Sudha, N. (2015, September). Enabling seamless video processing in smart surveillance cameras with multicore. In 2015 International Conference on Advanced Computing and Communications (ADCOM) (pp. 27-32). IEEE.
8. Sudha, N., & Chandrahas, D. B. (2015, June). A pipelined memory-efficient architecture for face detection and tracking on a multicore environment. In 2015 19th International Symposium on VLSI Design and Test (pp. 1-2). IEEE.

9. Sudha, N. (2015, June). Multicore processor—Architecture and programming. In 2015 19th International Symposium on VLSI Design and Test (pp. 1-2). IEEE.
10. Sudha, N., Sridharan, K., & Wilkinson, D. (2014, December). A pipelined architecture for motion tracking on a multicore environment. In 2014 International Symposium on Integrated Circuits (ISIC) (pp. 384-387). IEEE.
11. Natarajan, S., Puhon, N. B., & Meher, P. K. (2013). Special section on image and video processing for security.