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## Publications List of Dr. S.Jeevananthan in the Last 5 Years

- P.Muthukumar, L.Padmasuresh, K.Eswaramoorthy, and S.Jeevananthan, "Critical Analysis of Random Frequency Inverted Sine Carrier PWM Fortification for Half Controlled Bipolar Three Phase Inverters", Journal of Power Electronics, 20, 479-491, https://doi.org/10.1007/s43236-020-00034-6, January 2020.
- Rajan Palanisamy, Jeevananthan Seenithangam and Ranjith Palanisamy,"A Hybrid Output Multiport Converter for Standalone Loads and Photovoltaic Array Integration", International Transactions on Electrical Energy Systems, 2020;e12410. https://doi.org/10. 1002/2050-7038.12410.
- C.Bharatiraja, S.Jeevananthan, and J. L Munda, "A Timing Correction Algorithm Based Extended SVM for Three Level Neutral Point Clamped MLI in Over Modulation Zone", IEEE Journal of Emerging and Selected Topics in Power Electronics, Vol.6, no.1, pp.233-245, 2018.
- 4. K.Gayathri, and **S. Jeevananthan**, "Candid inquest on modeling methods of doubly fed induction generator, and corroboration through simulation study", AMSE Journal on Modelling, Measurement and Control-A, Vol.91, No.4, December, 2018.
- V. Viswanathan, and S.Jeevananthan," Commutation Torque Ripple Reduction in BLDC Motor Using Modified SEPIC Converter and Three-level NPC Inverter", IEEE Transactions on Power Electronics, Vol.33, no.1, pp.535-546, 2018.
- V.Krishnakumar, V.Kamaraj and S.Jeevananthan,"Parallel Fuzzy Logic Controllers for Independent Control of Two Permanent Magnet Synchronous Motors fed by a Five Leg Inverter for Electric Vehicles", Journal of Electrical Engineering, vol.17, no.1, paper no. 17, www.jee.ro, March, 2017.

- 7. V. Viswanathan and **S.Jeevananthan**," Hybrid Converter Topology for Reducing Torque Ripple of BLDC Motor ", IET Power Electronics, vol.10, no.12, pp. 1572 1587, October 2017.
- 8. R. Sridhar, **S. Jeevananthan**, S. S. Dash and PradeepVishnuram, "A new maximum power tracking in PV system during partially shaded conditions based on shuffled frog leap algorithm", Journal of Experimental & Theoretical Artificial Intelligence Vol. 29, no.3, 2017 ((Taylor and Francis Online)).
- 9. R. Kalaivani, K. Ramash Kumar, **S. Jeevananthan**, "Implementation of VSBSMC plus PDIC for Fundamental Positive Output Super Lift-Luo Converter," Journal of Electrical Engineering, Vol. 16, Edition: 4, 2016, pp. 243-258.
- 10. C.Bharatiraja, S.Jeevananthan, R.Latha, and V.Mohan, A Vector Selection Approach Based Hexagonal Hysteresis - Space Vector Current Controller for a Three Phase Diode Clamped MLI with Capacitor Voltage Balancing, IET Power Electronics, Vol. 9, no.7, pp.1350-1361, 2016.
- 11. C.Bharatiraja, **S.Jeevananthan**, J.LMunda and R.Latha, "Improved SVPWM vector selection approaches in OVM region to reduce common-mode voltage for three-level neutral point clamped inverter", International Journal of Electrical Power and Energy Systems(Elsevier), 79, 285–297, 2016.
- 12. V.Viswanathan and S.Jeevananthan, "Reducing torque ripple of BLDC motor by integrating dc-dc converter with three-level neutral-point-clamped inverter", COMPEL: The International Journal for Computation and Mathematics in Electrical and Electronic Engineering (COMPEL), Vol.35 No.3, pp.959-981, 2016
- 13. R. Sridhar, **S. Jeevananthan**, and Pradeep Vishnuram "Particle swarm optimisation maximum power-tracking approach based on irradiation and temperature measurements for a partially shaded photovoltaic system" International Journal of Ambient energy (Taylor and Francis Online), 2016.
- 14. Sridhar R., **Jeevananthan S**., Sai Pranahita B, "Investigations on multidimensional maximum power point tracking in partially shaded photovoltaic arrays with PSO and DE algorithms" Advances in Intelligent Systems and Computing, 394, pp. 1113-1125, 2016

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- 18. Sridhar Ramasamy, **Jeevananthan S**, Dash S.S and Krishna Chaitanya, "A dodging algorithm to reconfigure photovoltaic array to negate partial shading effect", Progress In Photovoltaics: Research and Applications, 2015.
- 19. S.Sangeetha and **S. Jeevananthan**, "Influence of crossover methods used by genetic algorithm-based heuristic to solve the selective harmonic equations (SHE) in multi-level voltage source inverter", Sadhana- Springer (Journal of Indian Academy of Sciences), vol.40, no.8, pp. 2389-2410, 2015.
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