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1. Implementation of VSBSMC plus PDIC for Fundamental Positive Output Super Lift-Luo Converter, S. Jeevananthan R. Kalaivani, K. Ramash Kumar 2016, Journal of Electrical Engineering, Journal Volume 16, Issue 4, Pages 243-258, Journal of Electrical Engineering.
2. A new maximum power tracking in PV system during partially shaded conditions based on shuffled frog leap algorithm, R Sridhar, S Jeevananthan, Subhransu Sekhar Dash, Pradeep Vishnuram, 2017, Journal of Experimental & Theoretical Artificial Intelligence, Volume 29, Issue 3, Pages 481-493, Publisher Taylor & Francis.
3. Improved SVPWM vector selection approaches in OVM region to reduce common-mode voltage for three-level neutral point clamped inverter, C Bharatiraja, S Jeevananthan, JL Munda, R Latha 2016,International Journal of Electrical Power & Energy Systems, Volume 79, Pages 285-297, Publisher Elsevier.
4. Vector selection approach-based hexagonal hysteresis space vector current controller for a three phase diode clamped MLI with capacitor voltage balancing, C Bharatiraja, Seenithangam Jeevananthan, Ramachandran Latha, V Mohan 2016, IET Power Electronics, Volume 9, Issue 7, Pages 1350-1361, Publisher IET Digital Library
5. Commutation Torque Ripple Reduction in the BLDC Motor Using Modified SEPIC and Three-Level NPC Inverter, Jeevananthan Seenithangom Vaiyapuri Viswanathan 2018, IEEE Transactions on Power Electronics, Volume 33, Issue 1, Pages 535 – 546, Publisher IEEE.
6. Critical analysis of random frequency inverted sine carrier PWM fortification for half-controlled bipolar three-phase inverters, P Muthukumar, L Padmasuresh, K Eswaramoorthy, S Jeevananthan, 2020, Journal of Power Electronics, Pages 1-13, Publisher Springer Singapore.
7. Investigations on Multidimensional Maximum Power Point Tracking in Partially Shaded Photovoltaic Arrays with PSO and DE Algorithms, R Sridhar, S Jeevananthan, B Sai Pranahita, 2016, Book Artificial Intelligence and Evolutionary Computations in Engineering Systems, Pages 1113-1125, Publisher Springer, New Delhi.
8. Particle swarm optimisation maximum power-tracking approach based on irradiation and temperature measurements for a partially shaded photovoltaic system, R Sridhar, S Jeevananthan, Pradeep Vishnuram 2017, International Journal of Ambient Energy, Volume 38, Issue 7, Pages 685-693, Publisher Taylor & Francis.
9. A tactical chaos based PWM technique for distortion restraint and power spectrum shaping in induction motor drives, V Mohan, N Stalin, S Jeevananthan 2015, Journal International Journal of Power Electronics and Drive Systems, Volume 5, Issue 3, Pages 383, Publisher IAES Institute of Advanced Engineering and Science.
10. Reducing torque ripple of BLDC motor by integrating dc-dc converter with three-level neutral-point-clamped inverter, S.Jeevananthan V.Viswanathan 2016, The International Journal for Computation and Mathematics in Electrical and Electronic Engineering, Volume 35, Issue 3, Pages 959-981, Publisher COMPEL
11. Microprocessors and interfacing, N Senthil Kumar, M Saravanan, S Jeevananthan, SK Shah, 2016, Publisher Oxford University press.

12. A dodging algorithm to reconfigure photovoltaic array to negate partial shading effect. Krishna Chaitanya (lx) Sridhar Ramasamy, Jeevananthan S, Dash S.S, 2015, Progress In Photovoltaics: Research and Applications.
13. Investigation on a modified 11-level cascaded inverter fed by photovoltaic array for standalone applications, R Sridhar, S Jeevananthan, SS Dash, Kiran Vemula, 2015, Journal of Solar Energy Engineering, Volume 137, Issue 2, Publisher American Society of Mechanical Engineers Digital Collection
14. Influence of crossover methods used by genetic algorithm-based heuristic to solve the selective harmonic equations (SHE) in multi-level voltage source inverter, S Jeevananthan, 2015, Journal Sadhana, Volume 40, Issue 8, Pages 2389-2410, Publisher Springer India.
15. An improved hybrid space vector PWM technique for IM drives, P Muthukumar, P Melba Mary, S Jeevananthan, 2016, Journal Circuits and Systems, Volume 7, Issue 9, Pages 2120-2131, Publisher Scientific Research Publishing.
16. A Carrierless PWM Strategy for Multilevel Inverters, S. Jeevananthan S. Thamizharasan, J. Baskaran, S. Ramkumar, 2015, IET Power Electronics, Volume 8, Issue 10, Pages 2034–2043, Publisher IET Power Electronics.
17. Carrierless pulse width modulation strategy for multilevel inverters, Sandirasegarane Thamizharasan, Latchumanan Usha Sudha, Jeevarathinam Baskaran, Subburam Ramkumar, Seenithangam Jeevananthan, 2015, IET Power Electronics, Volume 8, Issue 10, Pages 2034-2043, Publisher IET Digital Library.
18. A Timing Correction Algorithm Based Extended SVM for Three Level Neutral Point Clamped MLI in Over Modulation Zone, J.LMunda C.Bharatiraja, S.Jeevananthan, 2017, IEEE Journal of Emerging and Selected Topics in Power Electronics.
19. Hybrid converter topology for reducing torque ripple of BLDC motor, Vaiyapuri Viswanathan, Seenithangom Jeevananthan, 2017, IET Power Electronics, Volume 10, Issue 12, Pages 1572-1587, Publisher IET Digital Library.