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nirmal@annauniv.edu
 Area of specialisation
 Image Processing

<u>TITLE</u>	CITED BY	<u>YEAR</u>
Multi-objective module partitioning design for dynamic and partial reconfigurable system-on-chip using genetic algorithm N Janakiraman, PN Kumar Journal of Systems Architecture 60 (1), 119-139	23	2014
EEG signal with feature extraction using SVM and ICA classifiers PN Kumar, H Kareemullah International Conference on Information Communication and Embedded Systems ...	19	2014
A novel scheme for mutual authentication and cheating prevention in visual cryptography using image processing B Padhmavati, PN Kumar, MAD Rangaswamy ACEEE International Journal on Signal and Image Processing 1 (03), 1-5	12	2010
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Evolving virtual reconfigurable circuit for a fault tolerant system PN Kumar, S Anandhi, JRP Perinbam	8	2007

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YEAR

2007 IEEE Congress on Evolutionary Computation, 1555-1561

Power consumption reduction in a SDR based wireless communication system using partial reconfigurable FPGA

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2012

N Joseph, PN Kumar

International Journal of VLSI design & Communication Systems (VLSICS) Vol 3 ...

Experimental verification of soft switching push-pull dc to dc converter

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S Devikala, P Nirmalkumar

2012 IEEE International Conference on Power Electronics, Drives and Energy ...

A survey on embedded reconfigurable architectures

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H Kareemullah, N Janakiraman, PN Kumar

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Computer aided partitioning for design of parallel testable VLSI systems

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D Jose, PN Kumar, L Saravakanthan, R Dheeraj

2013 International Conference on Advances in Computing, Communications and ...

A novel method to avoid stale route cache problem of dynamic source routing protocol for mobile ad hoc network

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2013

PN Kumar

2013 International Conference on Current Trends in Engineering and ...

Enhanced performance of H. 264 using FPGA coprocessors in video surveillance

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2010

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P NirmalKumar, E MuraliKrishnan, E Gangadharan
2010 International Conference on Signal Acquisition and Processing, 157-161

Automatic detection of lung cancer nodules in computerized tomography images
D Jose, AN Chithara, PN Kumar, H Kareemulla
National Academy Science Letters 40 (3), 161-166

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Algorithmic based VLSI architecture of integrated image compression for CMOS image sensor
P Ezhilarasi, P Nirmalkumar
National Academy Science Letters 38 (1), 49-59

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FPGA based realization of OFDM transceiver system for Software Defined Radio
N Joseph, PN Kumar
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A novel mathematical model for (t, n)-Threshold visual cryptography scheme
B Padhmavathi, PN Kumar
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Reconfigurable parallelized TCAM architecture based on enhanced static memory cell
K Sakthi, PN Kumar
Microprocessors and Microsystems 76, 103073

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2020

Efficient soft error resiliency by multi-match packet classification using scalable TCAM implementation in FPGA

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2020

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K Sakthi, PN Kumar
 Microprocessors and Microsystems 74, 102985

Area efficient Cordic FFT using error correction codes and parseval checks for OFDM systems
 T Pranish, D Jose
 ARPN J Eng Appl Sci 11, 5

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VLSI circuit partitioning using ant colony optimisation to yield fault tolerant testable systems
 D Jose, PN Kumar, A Hussain, P Shanker
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