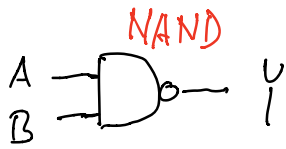
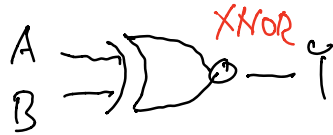
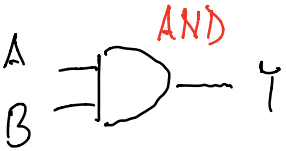


$$Y = \overline{A \cdot B}$$

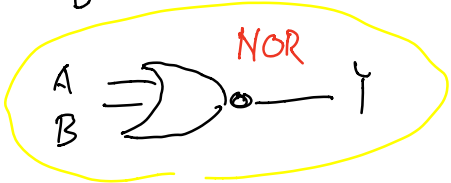


A	B	NAND	AND	OR	NOR	XOR	XNOR
0	0	1	0	0	1	0	1
0	1	1	0	1	0	1	0
1	0	1	0	1	0	1	0
1	1	0	1	1	0	0	1



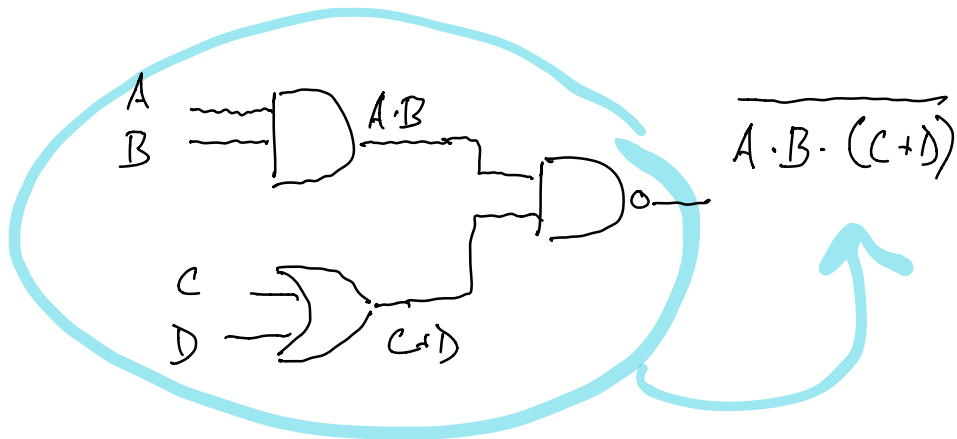
NOR:

$$Y = \overline{a + b}$$

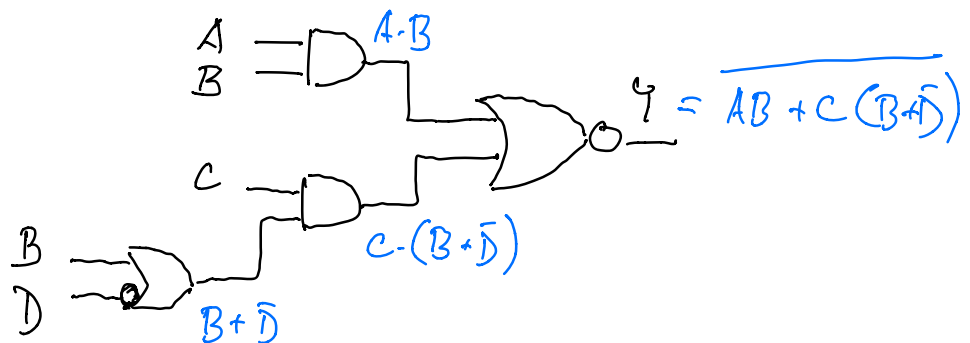


$\overline{A \cdot B \cdot C} \neq \overline{A} \cdot \overline{B} \cdot \overline{C}$

$$Y = \overline{A \cdot B \cdot (C + D)}$$



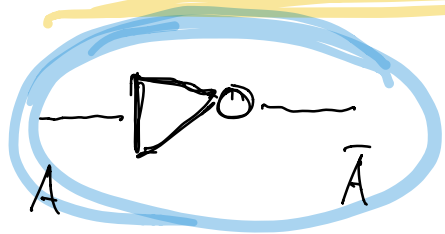
$$Y = \overline{A \cdot B + C \cdot (B + \bar{D})}$$



!  $\overline{A \cdot B \cdot C + D} \neq \overline{AB \cdot (C + D)}$  !

$\begin{matrix} ( & ) \\ & \bar{A} \\ \bar{A} \cdot B \\ A + B \sim A \cdot B \end{matrix}$

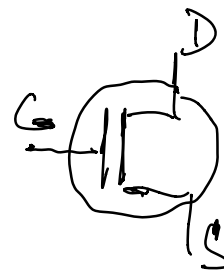
## ВХИМРНИ СТРУКТУРА ТИРАДЕЛ ЧНОС

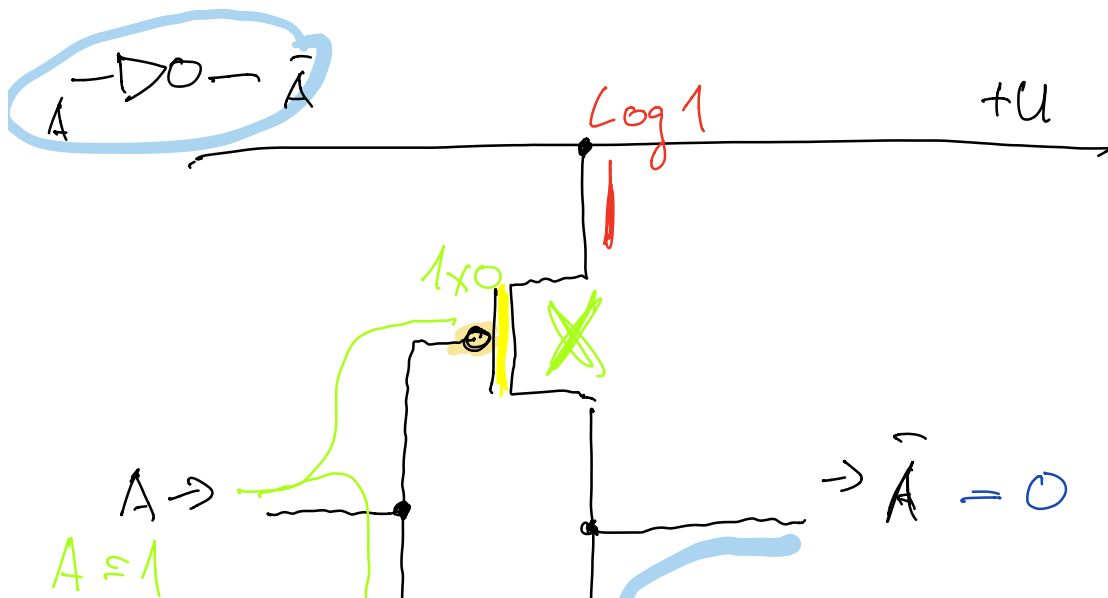
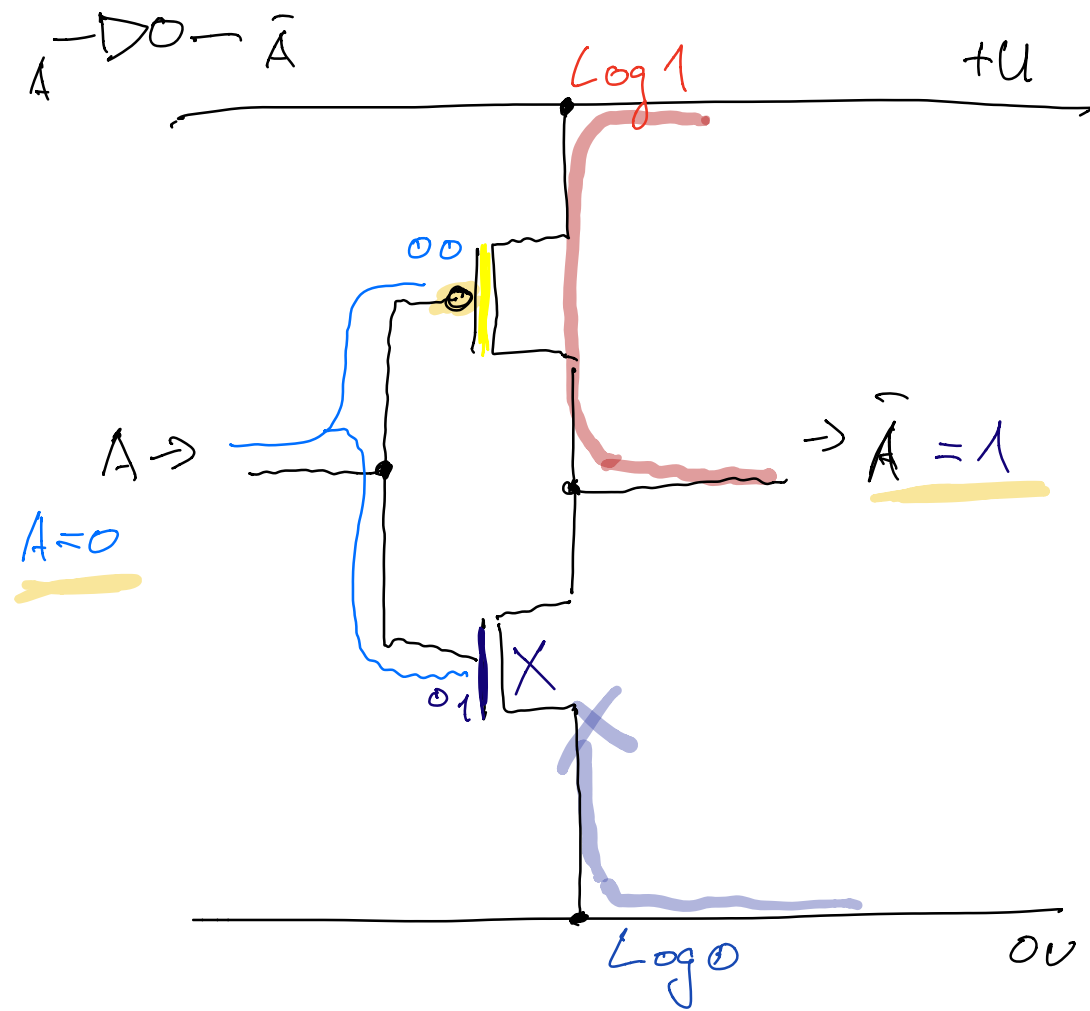


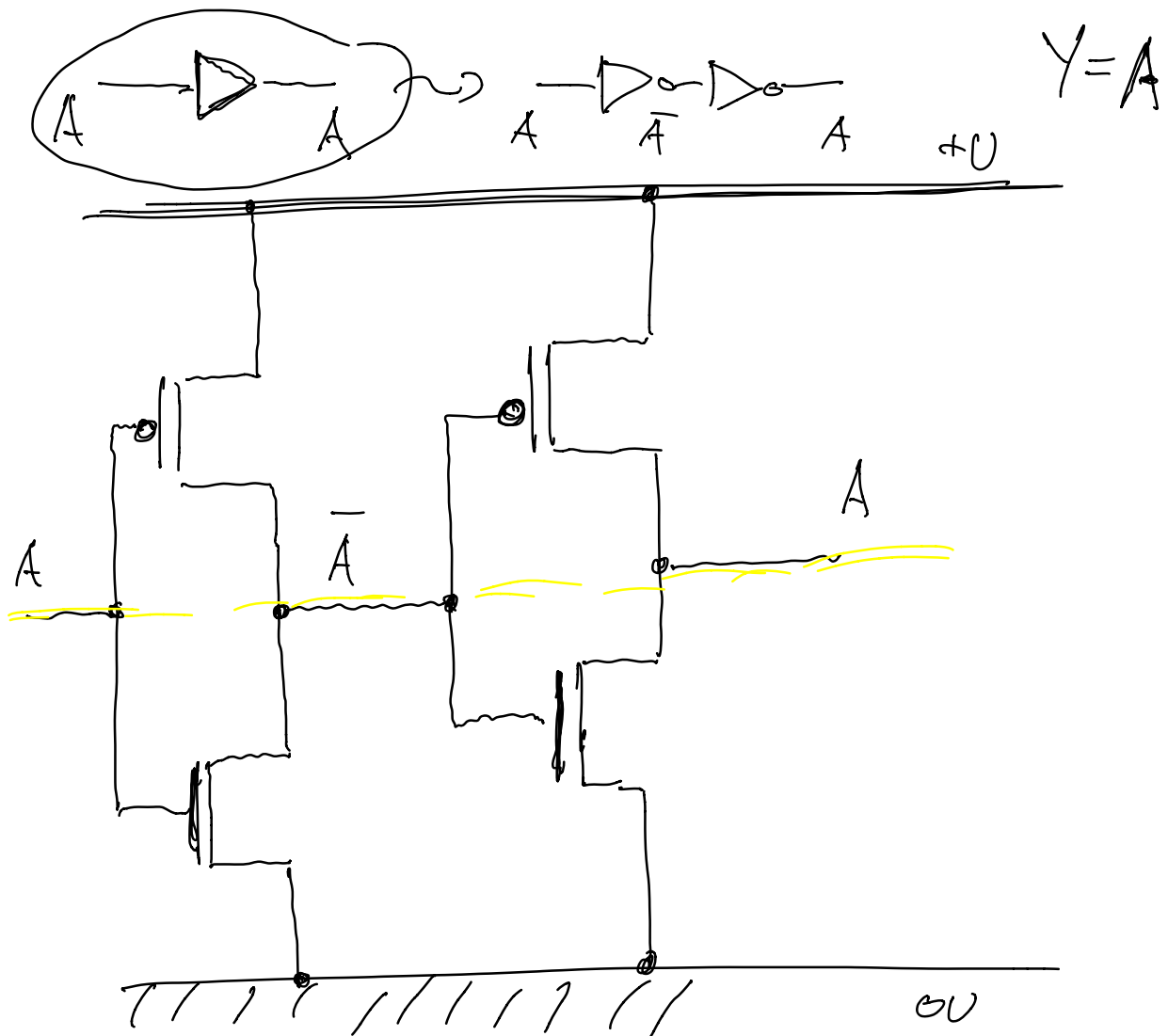
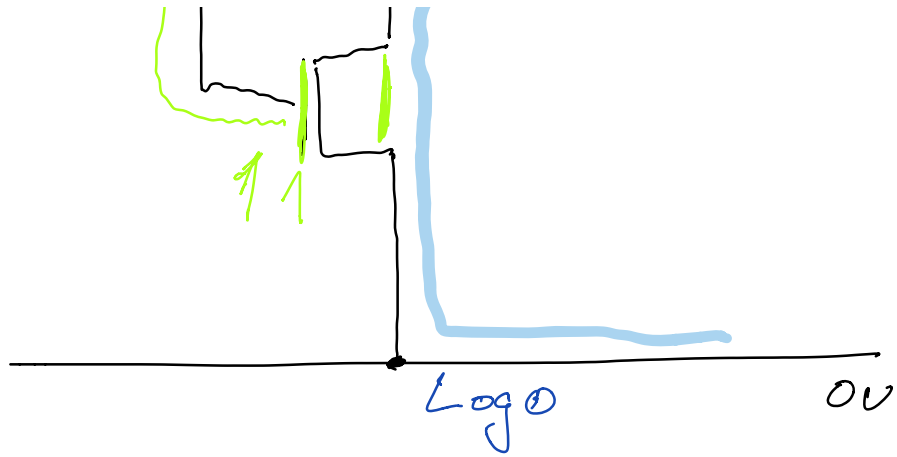
PMOS



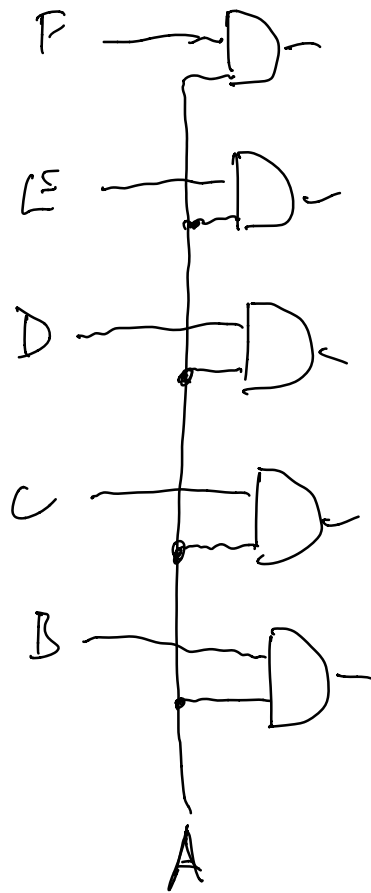
NMOS



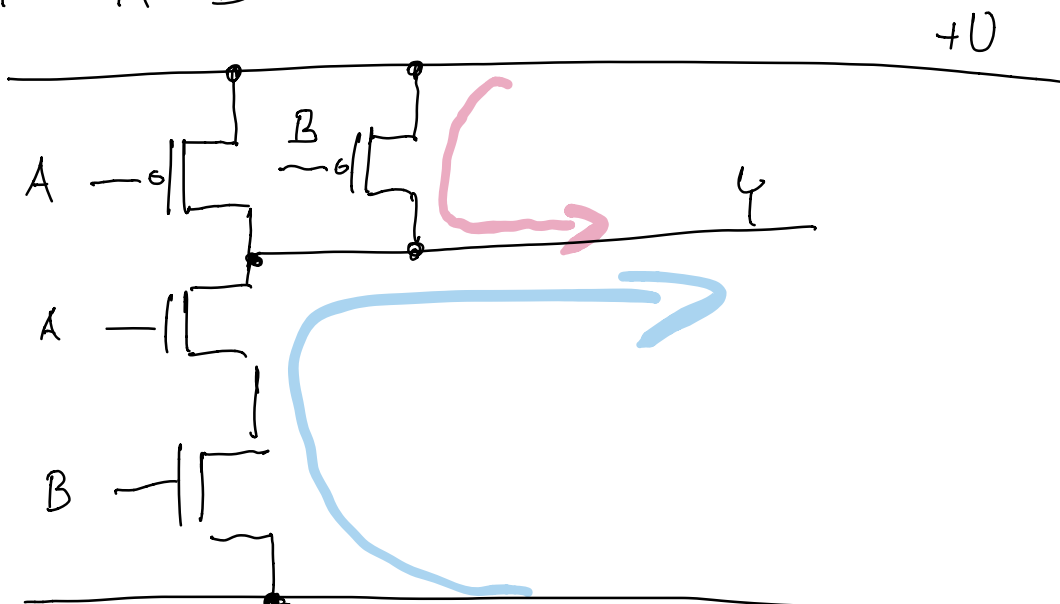




VIC E u BI-SAP



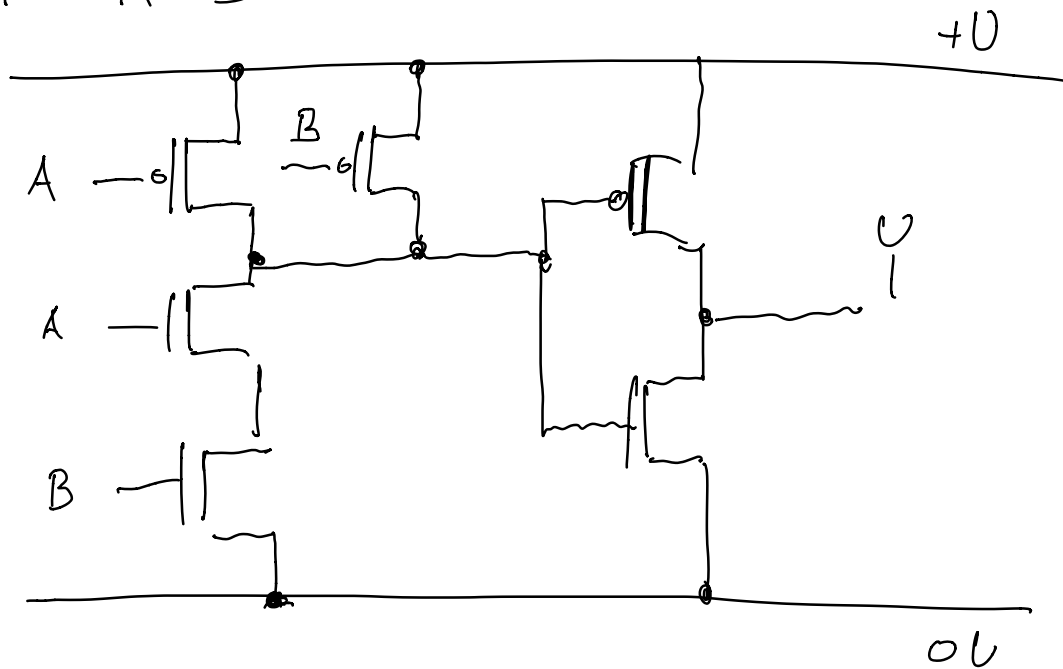
$$Y = \overline{A \cdot B} \Rightarrow \text{NAND}$$



00

A	B	NAND
0	0	1
0	1	1
1	0	1
1	1	0

$$Y = A \cdot B \Rightarrow \text{AND}$$

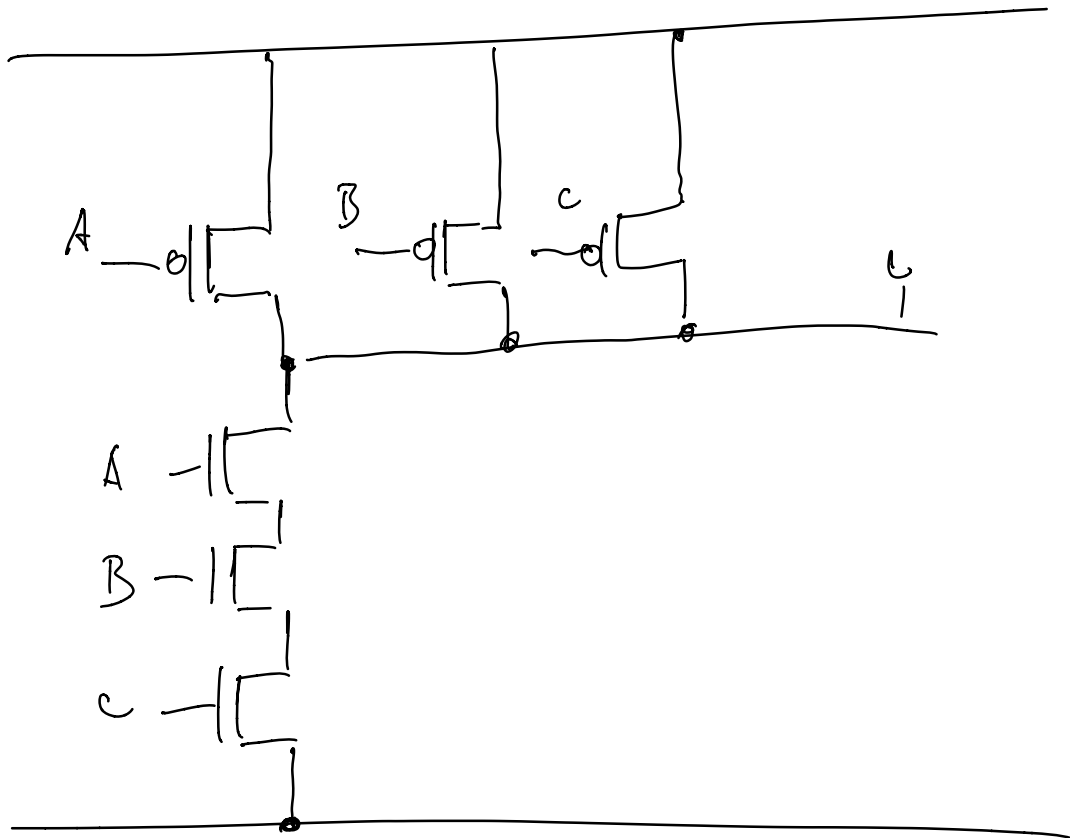


AND = 6 transistoru

NAND  $\Rightarrow$  jëm 4

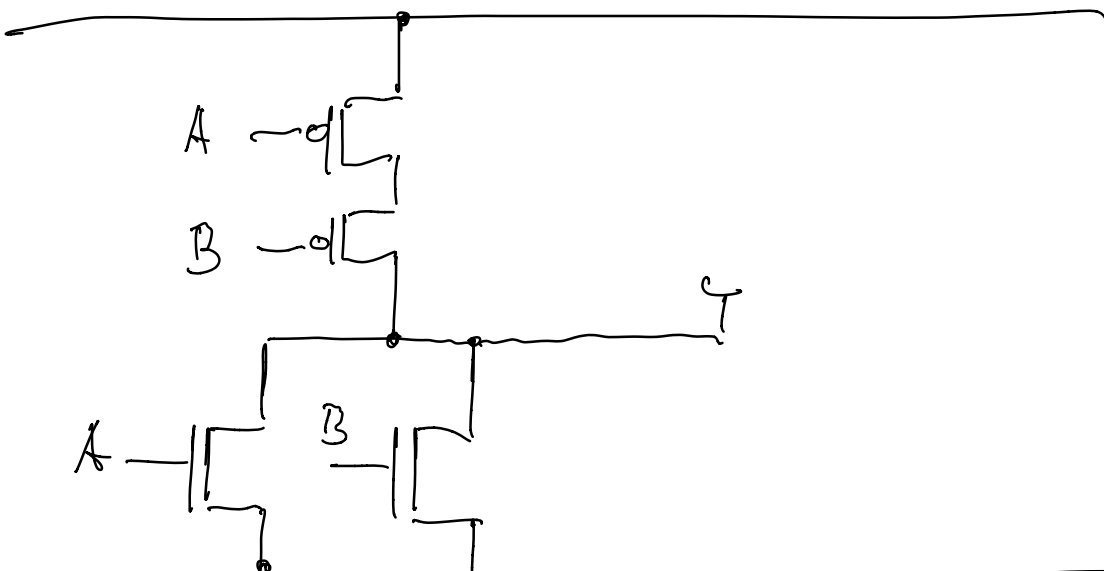
$$Y = \overline{A \cdot B \cdot C}$$

10



$$\text{No } D \Rightarrow \overline{A + B}$$

0V



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A	B	NOR
0	0	1
0	1	0
1	0	0
1	1	0