

Second Semester B.C.A. Degree Examination
October / November 2019
(2016 - 17 Scheme)
(BCB 420) COMPUTER ORGANISATION AND ARCHITECTURE

Time : 3 Hours

Max. Marks : 80

I. Answer ALL the following questions: (1x5=5)

1. Expand MAR and MDR.
2. What is Subroutines?
3. DMA stands for _____.
4. What is Cache Memory?
5. Define Micro Controller.

II. Answer any FIVE full questions: (15x5=75)

6. a) Explain basic operational concepts.
b) What is bus ? Explain bus structures.
c) Explain historical perspective of computer. **(5+5+5)**
7. a) Explain memory location and addresses.
b) Explain Instruction and Instruction Sequencing.
c) Explain Arithmetic operation and characters. **(5+5+5)**
8. a) Explain addressing modes.
b) Explain basic input/output operation.
c) Explain stacks and queues. **(5+5+5)**
9. a) Explain Direct Memory Access (DMA).
b) Explain Accessing I/O devices.
c) Explain standard I/O interfaces.
10. a) Explain semiconductor RAM memories.
b) Explain Cache Memories.
c) Explain Virtual Memories. **(5+5+5)**
11. a) Explain execution of complete instruction.
b) Explain hardwired control.
c) Explain briefly about performance consideration. **(5+5+5)**
12. a) Explain multiple bus organization.
b) Explain micro programmed control.
c) Differentiate between hardware control and micro control. **(5+5+5)**