#### PRANALI ARUNRAO POJAGE

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## **CURRICULUM VITAE**

# **OBJECTIVE**

To enhance my professional and carrier growth through continuous learning from challenging tasks and to provide best solutions to the organization in which I would get a chance to serve.

# **EDUCATIONAL QUALIFICATION**

Examination	Discipline	School/College	Board/University	Year of	%
	/Specialization			passing	
M.E.	Digital Electronics	Sipna college of engineering, Amravati	Sant Gadge Baba Amravati University	2017	67.5%
B.E.	Electronic and Telecommu nication Engineerig	Sipna college of engineering, Amravati	Sant Gadge Baba Amravati University	2015	76.24%
Polytechnic	Electronics engineering	Dr.Panjabrao Deshmukh Polytechnic, Amravati	Maharashtra State Board of Technical Education	2012	81.71%
S.S.C.	-	Subodh high school, Achalpur	Maharashtra State Board	2009	81.84%

# **PROJECTS**

# Master of Engineering project:

Name of Project: "Automatic Fast Moving Object Detection In Video Of Surveillance System"

Description: In this project we identify frames in the video which is having movement and also to identify the object which are changing its position in the successive frames.

## Bachelor of Engineering project:

Name of Project: "GPS & GSM Based Intelligent Vehicle Parameters Control System With Theft Control & Location Tracing".

Description: The main objective of this project is to track the position of Vehicle using GPS & GSM With the help of ATmega16 Microcontroller, GSM modem & GPS receiver.

# Diploma in Electronics Engineering project:

Name of Project: "Microcontroller Based Infrared Tracking Robot".

Description: In this project Robot senses IR rays of 38kHz frequency and follow the path of that ray, this projects use in many applications like fire extinguisher, heat seeking missile etc.

## **ACHIEVEMENTS**

- Participated in ROBO-RACE event TECHNIZZMA-10.
- Participated in National Level Technical Festival ROBO-RACE in TECHELONS-11.
- Participated in National Level Technical Festival JUNKYARD WAR in PRAJWALAN -11.
- Secured 1<sup>st</sup> place in PPT in PRAJWALAN-13.
- Participated in TRONIKA (circuit design) PRAJWALAN -14.
- Participated in JUNK ART PRAJWALAN -14.
- Participated in TEXAS INSTRUMENTS India Analog Maker Competition 2014.
- Participated in Project Competition in VIDYOTAN-2015.
- Participated in 'SEED IT IDOL' competition organized by SEED INFOTECH LTD.

## **PROFICIENCY**

Languages: Assembly language Programming. Digital Image Processing, Power Electronics, VHDL.

# PERSONAL INFORMATION

Name	Pranali A. Pojage
Gender	Female
Date of Birth	5 Jan, 1994
Religion	Hindu
Marital Status	Married
Father's Name	Arun Pandurangji Pojage
Mother's Name	Jyoti Arunrao Pojage
Language Known	English, Hindi, Marathi
Nationality	Indian

Hobbies	Watching Reality Show, Listening music, Playing games.
Permanent Address	At Post.Paratwada,Tq: Achalpur, Dist: Amravati.

# **DECLARATION**

I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particularly

PLACE: KALYAN PRANALI ANIKET PAWAR

DATE : 19/08/2017