

RESUME

Personal Details:

Name:	Mamta Prakash Patil
Current Location:	Kalyan , Mumbai, India
Status (Single/Married):	Married
Contact Details	patil.mamta9@gmail.com ; +91-7666751123
Years of Experience:	3.4 years
Highest Qualification:	<ol style="list-style-type: none">1. SEED Certification in Software Testing – Manual & Basic Knowledge of Automation2. Advanced Embedded System Design3. BE in Electronics Engineering.
Availability/Notice Period:	1 Month.

Overview:

A qualified Electronics Engineer with overall **3.4 Years** of experience in **Software testing(1.8)**, **Embedded System Testing(1.6)** and Debugging of Hardware and Software.

Seeking challenging assignments in Software Testing & Quality Assurance with a technology oriented organization

Skill Set:

- Skilled in understanding Software Requirement Specification and identifying the required Test Scenarios.
- Effective Test Case Designing and Test Case Preparing.
- Executing Test Cases to test the application functionality against the requirements manually.
- Generating Test Defect / Review Reports
- Basic Knowledge of automation tools.
- Tools: Jira, Bugzilla, Selenium (IDE), Jenkins, Jmeter, wireframe.
- Good interpersonal skills, committed, result oriented, hard working with a quest and zeal to learn new technologies.

Employment History:

Date:	Dec15 – Till Date
Employer:	Turtledove Technology
Position:	Software Testing Engineer
Responsibilities:	<ul style="list-style-type: none">• Experience in performing different testing techniques – Functional, Integration testing, Compliance Testing, Retesting & Regression Testing.• Preparation of Test Data for QA execution.• Involved in Test Planning, Test Environment creation, Test case writing, Test Case Implementation & Execution and Defect reporting & tracking.• Well acquainted with the SDLC & STLC.• Tools: Jira, Bugzilla, Selenium(IDE), Jenkins, Jmeter, wireframe, Basic Selenium (Webdriver).• Domain Experience: Ecommerce Projects (Magento), Mobile App Testing, CRM Based, Logistics.• Having basic knowledge in writing SQL queries.

Date:	Sept 2012 – March 2013
Employer:	Sifa Sanpara System Pvt Limited, Mumbai
Position:	Hardware Testing Engineer
Responsibilities:	<ul style="list-style-type: none"> To assist to design & test Hardware circuit according to clients requirement. To assist to design a 2-Layer PCB. To test the soldered PCB's. To maintain the stock of the components. Studied GSM & GPS modules. Protocols known I2C,SPI,USB

Date:	April 2011 – March 2012
Employer:	V-Smart InfoTech Inc. Pune
Position:	R & D Electronics Engineer
Project Brief:	<ul style="list-style-type: none"> Smart Spectra Display; Interface Unit
Responsibilities:	<ul style="list-style-type: none"> Analyzing and producing the technical requirements and specifications and recommending design modification based on client requirements. 89C51 Software designed using assembly language & Hardware design. Interfacing of microcontroller with 7 segment display, Relay & Opto coupler. Make a unit compatible with product. Testing and debugging of Software and Hardware.

Project during the Certification:

Project Name	Orange HRM
Description	Orange HRM is software that offers a flexible and easy to use solution for small and medium sized companies. The combination of various modules present in one application assures the perfect platform for re-engineering and aligning HR process along with the organizational goals.
Roles and Responsibilities:	<ul style="list-style-type: none"> Understanding the Requirements and Functional Specifications of the Project. Identified Test Scenarios required for testing. Participated in designing the Test cases. Prepared and Executed Test Cases as per System Requirements. Performed various black box testing Methodologies like Functional testing, Usability Testing. Defect Reporting Extensively performed Manual Testing process to ensure the quality of the software.

Education & Qualifications:

Year	College/University	Degree Type	Major	Percentage
2015	SEED	Certification	Software Testing (Manual + Automation)	
2010	OASIS, Pune	Certification	Advanced Embedded System Design	A-Grade
2010	North Maharashtra University	BE	Electronics	66.85%