

CURRICULUM VITAE

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Career Objective

A skilled test engineer who specialize in identifying, reporting and tracking software defects. A result oriented professional who can work in a fast paced work environment and able to deliver on time and with quality. Quick learner and enjoys being a part of a successful and productive team as well possess a strong ability to perform individually.

Academic Qualifications

Qualification	College	University/Board	Passing Year	Pointer/ Percentage
M.E.(VLSI & Embedded System)	MIT COE	Pune University	2015	7.42/10
B.E. (E&TC)	GSMCOE	Pune University	2011	60%
H.S.C.	J. T. S. Jr. College	Maharashtra State Board	2007	56.17%
S.S.C.	J. T. S. High School	Maharashtra State Board	2005	71.06%

Projects Undertaken

[1] Title: “In-Flow Inventory Management System”

Duration: 1 month

Project Description

In Flow Inventory Management System is primarily used for specifying and identifying stocked goods, replenishing depleted stocks and tracking movement of stocked goods. The scope of in Flow Inventory Management system involves replenishment lead time, carrying costs of inventory, Inventory valuation, inventory visibility, physical inventory, replenishment and demand forecasting.

Roles and Responsibilities:

- Understanding the requirement specification for the product and deriving Test Cases
- Deriving and preparing test data
- Executing Test Cases and create Defect Logs
- Involved in Manual Testing of applications
- Defect Reporting & Tracking

[2] Title: “Real time edge detection”**Duration:** 12 month**Hardware Used:** TMS320DM6437 DSP Processor

- Implemented the algorithm in C Language for detecting the edges and corners of the image.
- Captured the image from the camera which was connected to DSP processor.
- Studied the TMS320DM6437 DSP processor.
- Implemented algorithm on DSP processor.

Certifications

- Post Graduate Diploma in Software Testing.
- C language.

Testing Proficiency

Proficient or familiar with the following Concepts / Tools:

- Manual Testing
- Automation Testing
- SDLC
- STLC
- Defect Life Cycle
- Writing, Reviewing and Executing Test Cases
- Selenium
- QTP (Quick Test Professional) / UFT
- SQL Queries
- Excellent communication skills and ability to perform on the job

Achievements/Extracurricular activities

- Coordinator and member of national level competition “aura 2k11”
- Volunteer in national conference on “Advance in Signal Processing and Communication”
- Participation in workshop on “computer hardware assembly”

Activity and Interest

- Playing Cricket
- Reading

Personal details

- Date of Birth : 26th June 1989
- Address : B-14, Prince palace apartment, Nandanvan colony, Pimpale gurav, Pune, Maharashtra, India

Declaration

I hereby declare that, all the above given information is true to the best of my knowledge.

Date:

Place: Pune

Shashank R. Navale