

## RESUME

**Name:** Priyanka Pandurang Komple  
**Qualification:** BE(Electronics Engineering)  
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### CAREER OBJECTIVE:

Self-motivated and hardworking seeking for an opportunity to work in a challenging environment to prove my skills and utilize my knowledge & intelligence in the growth of the organization.

### EDUCATIONAL QUALIFICATION:

Examination	University/ Board	Institute/College	Year of passing	CPI/SPI/%Marks
Bachelor of Engineering	Mumbai	Shah And Anchor Kutchhi Engineering College.	2016	6.50
HSC	Mumbai	D.A.V. College,Mumbai	2012	70%
SSC	Mumbai	IES School,Mumbai	2010	89.45%

### TECHNICAL SKILLS:

<b>Programming Languages</b>	Excellent knowledge of <b>C/C++</b> , <b>Core JAVA</b> , basic knowledge of Advance JAVA,DOT NET.
<b>Web Technology</b>	HTML,CSS,PHP,Javascript.
<b>Database</b>	Mysql,SQL Queries.
<b>Operating Systems</b>	Windows, Linux
<b>Software/Editors</b>	MS Office,MS word, MS excel, MS PowerPoint, Microsoft SQL Server, Eclipse,Visual Studio,VMware.
<b>Manual Testing</b>	SDLC,STLC,Defect Life Cycle and all Testing Methodology

### OTHER CERTIFICATION:

- Diploma in Advanced Computing(**PG-DAC**) from **CDAC**.
- Completed **C/C++** full time course at Accord Mumbai.

### PROJECTS:

#### PG-DAC Project :

<b>Title :</b>	<b>Skill-share(A forever learning platform)</b>
<b>Platform :</b>	<b>J2EE</b>
<b>Description :</b>	Skill-Share(A Forever Learning Platform) is a website where users can share their knowledge and learn something new at the same time.This website is a open platform to everyone to share there knowledge in terms of technical as well as non-technical aspects. The Project is implemented in Java Platform,frontend we have used JSP, Servlet, JDBC and backend we have used Database using Mysql. Users can

<b>Role:</b>	post information in text format or Images. Documentation, Created various module using Servlet and JSP. Database connection, Validate the System.
<b>BE Project:2015-2016</b>	
<b>Title :</b> <b>Platform :</b> <b>Description :</b>	<b>Design And Analysis of Area and Power Efficient Array Multiplier VLSI Design Duration : 12 Months</b> The conventional array multiplier requires more number of transistors hence requiring more area which leads to high power consumption and low speed. In this work we propose the strategies of reducing the area and power consumption of array multipliers. The study of various circuits like half adder, full adder, 2*2 multipliers and 4*4 multipliers using the various logics like static logic, transmission logic and PTL .
<b>Role:</b>	Study of Technical Papers, design of Multipliers.
<b>Mini Project: 2014</b>	
<b>Title :</b> <b>Description :</b>	<b>Electronic Lock</b> An Electronic lock is a device which has an electronic control assembly attached to it. the password is entered by making use of a keypad. The major component are keypad, LCD and the controller AT89C51 which belongs to 8051 microcontroller while unlocking if the entered password is match with the stored password then locks open and if the password is wrong buzzer rings which connected to microcontroller.
<b>Role:</b>	Documentation, PCB connection.

#### PERSONAL SKILLS:

- Always willing to work and expand my knowledge.
- Highly dedicated and committed towards my work with positive attitude.
- Adaptable easily to new places and people.

#### HOBBIES:

- Reading Books.
- Travelling to new places.
- Listening to Music.

#### PERSONAL DETAILS :

**Language:** English, Hindi, Marathi.  
**DOB:** 26 march 1995  
**Address:** Richa Tower, M-Wing/1805, New P.M.G.P Mhada Colony, Mulund(e), Mumbai-81  
**Declaration:** I hereby declare that the information furnished above is true to the best of Knowledge and belief.

Yours Truly,  
Priyanka P. Komple