# NEHA PANPALIYA
Current: Karve Nagar Pune – 411052
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## OBJECTIVE

*"To work with a growing company and to utilize analytical as well as reasoning skills for the organization's development; to achieve defined goals and also to apply them practically in an environment which gives opportunity to learn and grow professionally".*

## EDUCATION

| Course | Institute | University/Board | Year of Passing | Percentage (%) |
|---|---|---|---|---|
| BE (E&TC) | Sinhgad Institute of Technology, Lonavala | Savitribai Phule Pune University | 2013-2017 | 65.59% |
| H.S.C | R.K Junior College, Pulgaon | Maharashtra State Board | 2012 | 81.33% |
| S.S.C | Kendriya Vidyalaya, Pulgaon | Central Board of Secondary Education | 2010 | 7.6 (C.G.P.A) |

## TECHNICAL SKILLS

Programming Language    : C Programming, C++, Basic JAVA, VLSI.
Designing tools    : Proteus, Multisim, Xilinx, MATLAB.
OS Platforms    : Linux, Windows

## MINI PROJECT

**"Contactless Digital Tachometer"**
A tachometer is used for measuring the rotation speed of any device. PIC18F4520 microcontroller was used since it requires no physical contact with the rotating device and can be interfaced with an IR Sensor in conjunction with a photo detecting diode which is easy to use, portable, small size and low cost.

## PROJECT

**"Design of Power Efficient FIR Filter for FPGA Based DSP Applications"**
This project describes the design and implementation of a low power FIR filter for DSP applications. DSP is a highly demanding application for enhanced performance and reduced resource utilization. It has increased features like high flexibility through reconfiguration, reusability, moderate cost and feature extension has resulted in FPGA becoming the preferred platform for evaluating and implementing DSP.

## EXTRA-CURRICULAR ACTIVITIES

- Participated in ARM Workshop.
- Event coordinator of the workshop LET US C LINUX.
- Vice President of IEI Club.
- Participated in Logo Quiz organized by SIT, Lonavala.
- Participated in Techtonic PCB Design.
- Completed course in C++ Programming with Grade A at Global InfoTech.
- Participated in Linux test organized by IIT Bombay under spoken tutorial.
- Participated in C++ test organized by IIT Bombay under spoken tutorial.
- Participated in Cultural Festival Surbhi as Anchoring Coordinator.
- ANCHORING coordinator in IETE Forum for year 2014-15.
- Participated in LaTeX.

## TRAINING

At "All India Radio" Nagpur.

## QUALITIES

Leadership Qualities, High Moral & Ethical Values, Friendly, Ready to learn new things and can work individually as well as in a team.

## PERSONAL DETAILS

Date of Birth        : $27^{th}$ April 1994
Nationality          : Indian
Marital Status       : Single
Languages Known      : English, Hindi, Marathi, and Maheshwari.
Hobbies              : Making creative things, Listening Music, Painting and Drawing.

**DECLARATION:** I hereby declare that the above written particulars are true to the best of my knowledge and belief.

Place: Pune

Date:   /   /2017                                    NEHA PANPALIYA