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REC | Mohamed Ridwan Kalia, Lecturer, CSE is presenting

**Race Condition**

count++ could be implemented as

```
register1 = count
register1 = register1 + 1
count = register1
```

count-- could be implemented as

```
register2 = count
register2 = register2 - 1
count = register2
```

Consider this execution interleaving with "count = 5" initially:

Step	Process	Operation	Value
S0	producer	register1 = count	5
S1	producer	register1 = register1 + 1	6
S2	consumer	register2 = count	5
S3	consumer	register2 = register2 - 1	4
S4	producer	count = register1	6
S5	consumer	count = register2	4

Handwritten notes on the slide:

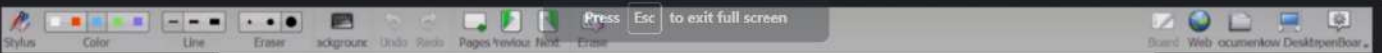
- count = 5
- P: r0 = 5
- S1: r0 = 6
- S2: r1 = 5
- S3: r1 = 4
- S4: c = 6
- S5: c = 4

10:28 AM | hformat365

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## Peterson's Solution for process $P_i$

$i$  indicates the index of flag array

$j$  indicates the value of the turn variable.

Process 0:  $i=0$ ;  $j=1-i=1$

```
do {  
    flag[0] = TRUE ;  
    turn = 1 ;  
    while ( flag[1] && turn == 1 ) ;
```

CRITICAL SECTION

```
    flag [0] = FALSE ;
```

REMAINDER SECTION

```
+  
    } while (TRUE) ;
```

Process 1:  $i=1$ ;  $j=1-i=0$

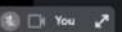
```
do {  
    flag[1] = TRUE ;  
    turn = 0 ;  
    while ( flag[0] && turn == 0 ) ;
```

CRITICAL SECTION

```
    flag [1] = FALSE ;
```

REMAINDER SECTION

```
    } while (TRUE) ;
```



Style

Color

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Eraser

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1. Mutual Exclusion

2. Progress

3. Bounded Waiting

$P_0$ : 😊

$P_1$ : 😞

# Peterson's Solution for process $P_i$

$i$  indicates the index of flag array  
 $j$  indicates the value of the turn variable.

**Process 0:  $i=0; j=1-i=1$**

```
do {
  S0 flag[0] = TRUE;
  S1 turn = 1;
  S2 while ( flag[1] && turn == 1 );
  CRITICAL SECTION
  flag [0] = FALSE;
  REMAINDER SECTION
} while (TRUE);
```

**Process 1:  $i=1; j=1-i=0$**

```
do {
  S0 flag[1] = TRUE;
  S1 turn = 0;
  S2 while ( flag[0] && turn == 0 );
  CRITICAL SECTION
  flag [1] = FALSE;
  REMAINDER SECTION
} while (TRUE);
```

## CS solution with locks using h/w

- A simple tool requires-lock
- Race conditions are prevented by requiring that critical section be protected by locks.

```
do {  
    acquire lock  
    critical section  
    release lock  
    +remainder section  
} while (TRUE);
```

Figure 6.3 Solution to the critical-section problem using locks.

## Synchronization Hardware

- Many systems provide **hardware support** for critical section code
- Uniprocessors – could disable interrupts while modified a shared variable
  - Currently running code would execute without preemption. So no unexpected modifications could be made to the shared variable.
  - This is the approach taken by non-preemptive kernels.
  - Generally too inefficient on multiprocessor systems
    - Operating systems using this not broadly scalable-time consuming.
- Modern machines provide special atomic hardware instructions: Test And Set ; Swap ;
  - Atomic = uninterruptable
  - Either test memory word and set value
  - Or swap contents of two memory words

$P_0 \rightarrow CS$

## Solution using TestAndSet

- Shared boolean variable lock, initialized to false.

Solution:

```
do {  
    while ( TestAndSet (&lock) );  
    // critical section  
    lock = false;  
    // remainder section  
} while ( true );
```

Definition:

```
boolean TestAndSet (boolean *target){  
    boolean rv = *target;  
    *target = true;  
    return rv;  
}
```

$P_0$ : lock = F  
Test+Set (F)  
rv = F  
lock = T  
return rv;  
}

$P_1$ : lock = T  
+  
 $P_0$ : ✓ ✓  
 $P_1$ : ✗ ✗

Stylus

Color

Line

Eraser

Background

Undo

Redo

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Previous

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Pages

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Next

Erase

2 Progress

X { Included Waiting

# Solution using TestAndSet

- Shared boolean variable **lock**, initialized to false.
- Solution:

```
do {  
    while ( TestAndSet (&lock) );  
    // critical section  
    lock = false;  
    // remainder section  
} while ( true );
```
- Definition:

```
boolean TestAndSet (boolean *target){  
    boolean rv = *target;  
    *target = true;  
    return rv;  
}
```

$P_0$ : lock = F  
Test+Set ( F )  
rv = F  
lock = T  
return rv;  
}

$P_1$ : lock = T  
 $P_0$ : ✓ ✓  
 $P_1$ : ✗ ✗

## Compare And Swap Instruction

- Shared boolean variable lock., initialized to 0.

- Solution:

```
do {  
    while ( compareAndswap(&lock, 0, 1 ));  
    // critical section  
    lock = 0;  
    // remainder section  
} while ( true);
```

- Definition:

```
int compareAndswap(int *v, int e, int n){  
    int temp = *value;  
    if(*value == expected)  
        *value = new_value;  
    return temp;  
}
```

+





## Complete Solution using Test And Set

```

do{
    waiting[i] = true;
    key = true;
    while(waiting[i] && key)
        key = testAndSet(&lock);
    waiting[i] = false;
    // critical section
    j = (i + 1) % n;
    while((j == i) && !waiting[j])
        j = (j + 1) % n;
    if (j == i)
        lock = false;
    else
        waiting[j] = false;
    // remainder section
} while(true);

```

Boolean waiting[n];  
Boolean lock;

$n=2$   
 $P_0 \quad P_1$

	$i$	waiting	lock	key	$j$
$P_0$	0	T	F	T	1
$P_1$	1	T	T	F	0

## Complete Solution using Test And Set

```

do{
    waiting[i] = true;
    key = true;
    while(waiting[i] && key)
        key = testAndSet(&lock);
    waiting[i] = false;
    // critical section
    j = (i + 1) % n;
    while((j != i) && !waiting[j])
        j = (j + 1) % n;
    if (j == i)
        lock = false;
    else
        waiting[j] = false;
    // remainder section
}while(true);

```

Boolean waiting[n];  
Boolean lock;

$n=2$

	$i$	waiting	lock	key	$j$
$P_0$	0	T	F	T	1
$P_1$	1	T	F	T	0

# Complete Solution using Test And Set

```
do{
    waiting[i] = true;
    key = true;
    while(waiting[i] && key)
        key=testAndset(&lock);
    waiting[i] = false;
    // critical section
    j = (i + 1) % n;
    while((j!=i) && !waiting[j])
        j = (j+1)%n;
    if (j==i)
        lock = false;
    else
        waiting[j] = false;
    // remainder section
}while(true);
```

Boolean waiting[n];  
Boolean lock;

$n=2$   
 $P_0 \quad P_1$

	i	waiting	lock	key	j
$P_0$	0	T	F	T	1
$P_1$	1	T	F	T	0

✓ Mutual Exclusion  
2. Progress  
2. Bounded Waiting

## Complete Solution using Test And Set

```

do{
    waiting[i] = true; →
    key = true; →
    while(waiting[i] && key)
        → key=testAndSet(&lock);
    → waiting[i] = false;
    → // critical section
    j = (i + 1) % n;
    while((j!=i) && !waiting[j])
        j = (j+1)%n;
    if (j==i)
        ✓ lock = false;
    else
        ↓
        waiting[j] = false; ✓
    // remainder section
}while(true);
    
```

Boolean waiting[n];  
 Boolean lock;

$n=2$   
 $P_0 \quad P_1$

	$i$	Waiting	lock	key	$j$
$P_0$	0	T	F	T	1
$P_1$	1	T	F	T	0

✓ Mutual Exclusion  
 ✓ Progress  
 2. Bounded Waiting



## Mutex Locks

- Calls to either `acquire()` or `release()` must be done atomically.
- Main disadvantage is busy waiting.
- When one process is in its critical section others must loop continuously in the call to `acquire()`.
- This type of mutex lock is also known as spin lock as the process literally spins while waiting for the lock to become available.
  - Spin lock is a problem in real multiprogramming situation, where a single CPU is shared among many processes. Busy waiting wastes CPU cycles that some other processes might be able to use productively.
  - One advantage of spin lock is that, no context switching is required when a process must wait on a lock. Thus, when locks are expected to be held for a short time, spin locks are useful.
  - They are often employed on multi-processor systems where one thread can spin on one processor while another thread performs its critical section on another processor.

## Semaphore Usage

- Consider two concurrently running processes
  - $P_1$  with statement  $S_1$  →
  - $P_2$  with statement  $S_2$  →
- Suppose we require  $S_2$  to be executed only when  $S_1$  has finished execution.
- To do this, we allow  $P_1$  and  $P_2$  to share a semaphore synch initialized to 0 and the processes  $P_1$  and  $P_2$  are defined as follows.

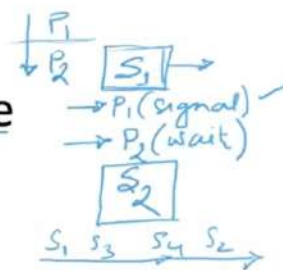
```
 $P_1(\{$   
   $S_1;$   
   $\text{signal}(\text{synch});$   
 $\}$ 
```

```
 $P_2(\{$   
   $\text{wait}(\text{synch});$   
   $S_2;$   
 $\}$ 
```

- Because synch is initialized to 0,  $P_2$  will execute  $S_2$  only after  $P_1$  has invoked  $\text{signal}(\text{synch})$ , which is after statement  $S_1$  has been executed.



## Semaphore Usage



- Consider two concurrently running processes
  - $P_1$  with statement  $S_1$
  - $P_2$  with statement  $S_2$
- Suppose we require  $S_2$  to be executed only when  $S_1$  has finished execution.
- To do this, we allow  $P_1$  and  $P_2$  to share a semaphore synch initialized to 0 and the processes  $P_1$  and  $P_2$  are defined as follows.

```

P1() {
    S1;
    signal(synch);
}

P2() {
    wait(synch);
    S2;
}
    
```

- Because synch is initialized to 0,  $P_2$  will execute  $S_2$  only after  $P_1$  has invoked `signal(synch)`, which is after statement  $S_1$  has been executed.

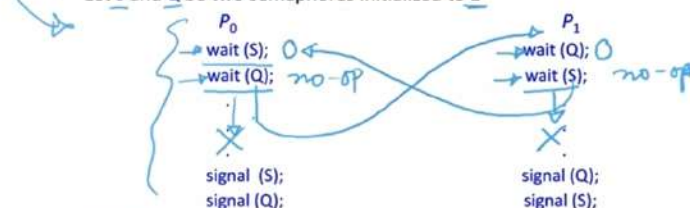
while (S <= 0);

S--  
S++

## Deadlock and Starvation

Deadlock – two or more processes are waiting indefinitely for an event that can be caused by only one of the waiting processes

- Let S and Q be two semaphores initialized to 1



- Starvation – indefinite blocking. A process may never be removed from the semaphore queue in which it is suspended.

+

- Bounded-Buffer Problem
- + Readers and Writers Problem
- Dining-Philosophers Problem

- 