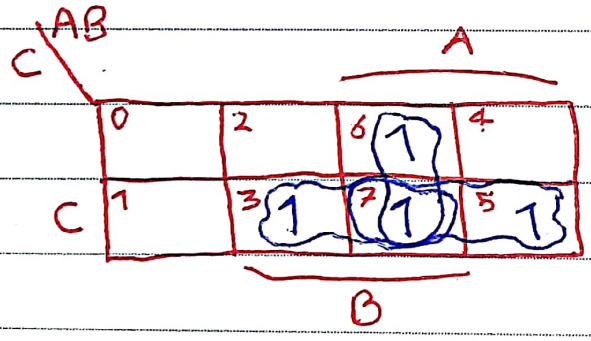
Truth table

A	B	C	F	
0	0	0	0	
0	0	1	0	
0	1	0	0	
0	1	1	1	m_3
1	0	0	0	
1	0	1	1	m_5
1	1	0	1	m_6
1	1	1	1	m_7

Karnaugh map



simplify

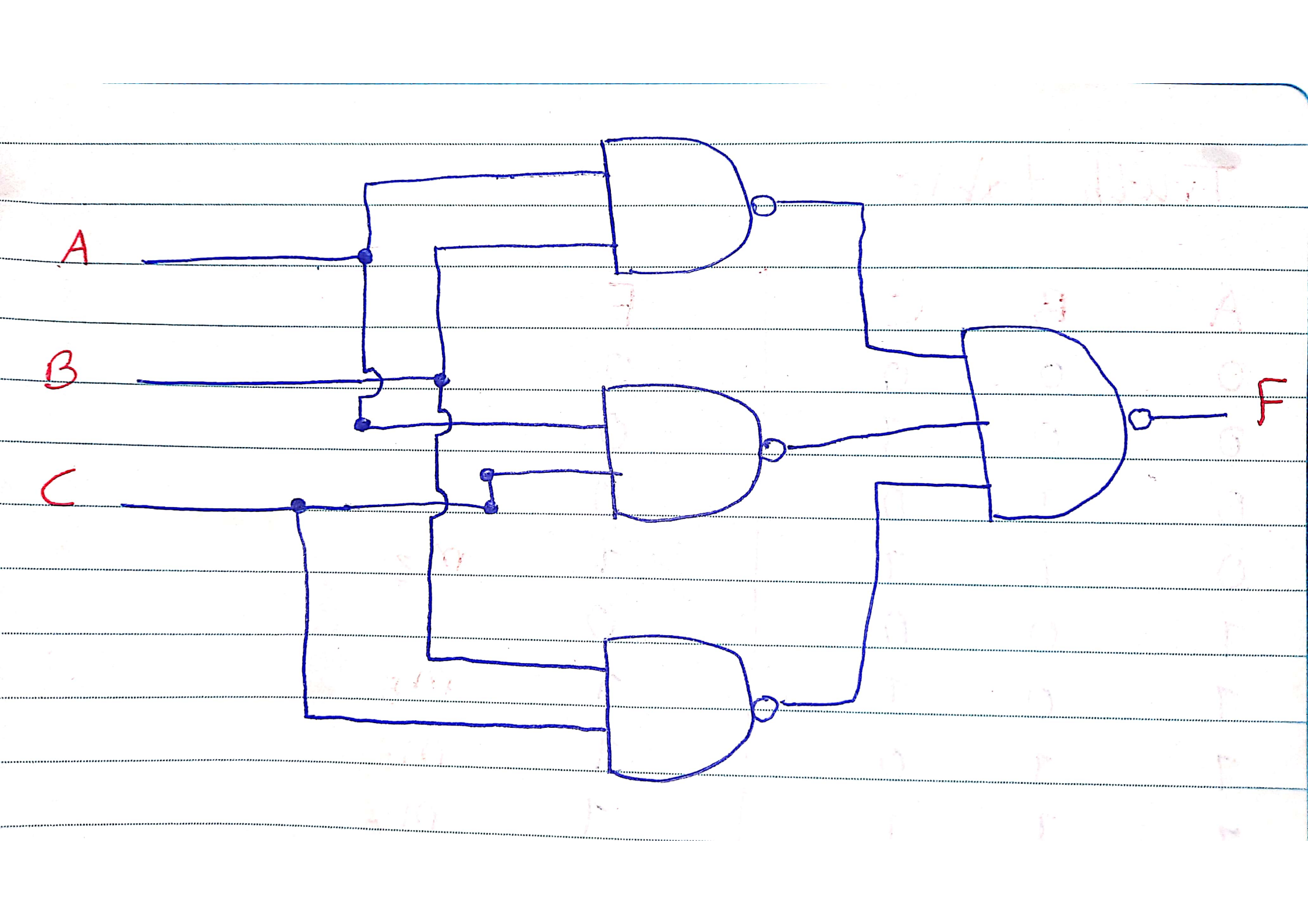
$$F = AB + AC + BC$$

implementation with NAND gates.

$$\overline{\overline{F}} = \overline{(\overline{AB + AC + BC})}$$

$$F = \overline{(\overline{(\overline{AB})}\,\overline{(\overline{AC})}\,\overline{(\overline{BC})})}$$

$$F = \text{NAND}(\text{NAND}(A,B), \text{NAND}(A,C), \text{NAND}(B,C))$$


A
B
C
F