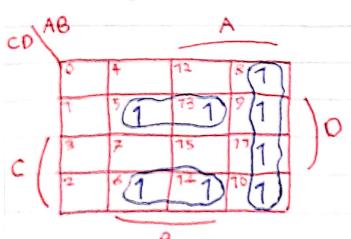
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Karnaugh map



sim plify

F= AB + BCO + BCD

implementation with NAND gates

$$F = ((\overline{AB})(\overline{BCD})(\overline{BCD}))$$

Assume that inputs complements are available.

