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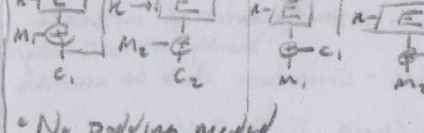
- Kerckhoffs Principle: Sys should remain secure when internal details are known to attacker (aside secret key)

- Any deterministic scheme isn't IND-CPA

Given $H(M)$, can produce $H(M || M')$ $\forall M'$

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Security Principles

- Know your threat model.
Who, what resources they have
- Consider Human Factors. Latencies and accidents bound to occur
- Security is Economics. Attack effort will be proportional to cost
→ Focus only improving weakest link
- Detect if can't Prevent. Min. response time; indicate/alert to tampering
- Defense in Depth. Layer various types of defenses, requiring all must be breached
- Least Privilege. Only as much as absolutely necessary
- Separation of Responsibility. Require numerous parties to sign-off
- Ensure complete mediation.
When entering access control, check every access to every object
- Shannon's Maxim. Attacker knows details about sys. they're attacking
→ No security through obscurity
- Fail-safe Defaults. If sys fails, security isn't weakened (e.g. whitelisting)
- Design Security in from the start. Difficult to change retroactively

Trusted Computing Base (TCB):

Portion of sys. that must operate correctly to provide security assurances

- Unbypassable - must go thru TCB
- Tamper-resistant - external influence shouldn't invalidate integrity
- Verifiable - Correctness should be ascertainable

Time-of-Check Time-of-Use.

Non-atomic ops lead to race cond.
Value changes after check but before being used

Memory & Stack Frame:

```
void orbit(int arg1, int arg2) {
    char buf[8];
    gets(buf);
    int main(void) {
        orbit(1, 2);
        return 0;
    }
}
```

• Little Endian: LSB stored at lowest address *break @ gets

→ Address 0xDEADBEEF ⇒ EF BE AD DE

• Global vars (outside fctn) put on heap
→ Last defined has highest address

• Structs have 1st member at lowest addr.

• char[M] is 1 byte words = M bytes

• int[L] is 4 bytes words = 4L bytes

Off-by-one:

- Change SFP LSB s.t. points to 'A'x4 + 2 SHELLCODE
- 1st ret will move EBP to address it
- 2nd ret moves EBP to 'A'x4 (don't care!) then EIP to 2 SHELLCODE

Integer conversion: $0xFFFF = 2^{16} - 1 = 65535$
when unsigned (like 'size_t') but -1 when interpret as signed

- int = -1 will pass [int] > K VKZO
- Also consider overflow

String Format: spec. for hex & bytes

Begins 8 bytes above printf's RIP

- %c: character (read 4, but print 1 byte)
- %[b]u: print [b]-bytes starting from arg
- %s: dereference arg, print string value at that addr until '\0' - byte
- %n: dereference arg, write # chars printed thus far at said address

Calling Convention:

1. Push args to stack, reverse order
2. Push old EIP onto stack (RIP)
3. Move EIP to new fctn code
4. Push old EBP (SFP)
5. Move EBP down to ESP
6. Move ESP down for new frame
7. Execute fctn
8. Move ESP up to EBP
9. Restore EBP; pop SFP
10. Restore EIP; pop RIP
11. Remove args from stack

Mitigation: For mem. safety vulns

Memory safe lang: Many modern langs can prevent 100% of mem vulns

Writing Mem. safe Code: Defensive programming

Data pre: post conditions, invariants!

Building Secure Software: Use tools to analyze/patch vulns; Find mem leaks, freeze

Explicit Mitigations: Cause crashes when unsafe behavior occurs; defense in depth

- Non-executable pages. Either writable or executable memory; limit where EIP can point
→ counter: return to lib: overwrite RIP w/ library fctn we can harness, ex execv
- Counter: return-oriented programming put chain of return addresses at RIP that end in 'ret'

- Stack Canary. Place dummy vals below saved registers, above local vars
• If val changes during execution, crash
- Dummy val determined at runtime
→ Counter: Brute force 2^{24} values (last byte = '\0'; feasible for 32-bit)
- Counter: Leak canary, log format string vuln to print the stack

- Pointer Authentication (PAC). 64-bit long unused bytes for ptrs; store dummy val here

GDB:

- Info Frame lists stack addresses at SFP & RIP
- printing: 1st byte will be lowest address; rightmost byte in word

	n1	n2	n3
1	cccc	0	
2	cccc	1	
4	cccc	2	
8	cccc	3	
16	cccc	4	
32	cccc	5	
64	cccc	6	
128	cccc	7	
256	cccc	8	
512	cccc	9	
1024	cccc	A	
2048	cccc	B	
4096	cccc	C	
8192	cccc	D	
16384	cccc	E	
32768	cccc	F	

Address-space Layout Randomization (ASLR)

- Shuffle locations of stack/heap/code/static causing absolute addresses of sys/local vars/ code (libs) to be random each run
→ Counter: Guess. 32-bit has 16 bit entropy
→ Counter: Leak address, e.g. print stack and use relative offsets