Meiqi Yu

M.ASC, B.SC

A new graduate student specialized in Analog Integrated Circuit Design; Actively seeking a full-time position as a Development Engineer or Verification Engineer in the field of RF/Mixed- signal/Analog IC Design .

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778-929-7549

Vancouver

COMPUTER SKILLS

Circuit Design Software

Cadence Virtuoso,Cadence Spectre, Cadence Calibre, Altium Designer, Multisim

Efficiency Software

Microsoft Office, Microsoft Visual Studio, Github

Programming Languages

Matlab, C++, C#, Verilog HDL

Operating Systems

Linux, Windows

EDUCATION

M.ASC, Electrical and Computer Enginnering

University of British Columbia (UBC)

09/2017 – Present

B.SC, Optoelectronic Science

University of Electronic Science and Technology of China (UESTC)

09/2013 – 06/2017

RESEARCH EXPERIENCE

Research Assistant in RF/Analog Circuit Design

A 2.4GHZ/5GHZ Quadrature Voltage Controlled Oscillator(QVCO) Design for IOT Applications

09/2017 – Present

UBC Soc Lab

- Developing and designing a dual-band, ultra-low power QVCO circuit in 65nm CMOS process
- Performing pre-layout and post-layout simulations(DRC, LVS and PEX) using Cadence Spectre and Calibre
- Minimizing parasitic capacitance, accomplishing layout floor plan, routing and tape out
- Accomplishing PCB design and layout by Altium Designer

Research Intern

Diode Laser Saturated Absorption Spectroscopy

05/2016 – 08/2016

TRIUMF

- Constructing the Quadrupole Mass Spectrometer (QMS) and mounting the spectroscopy system
- Analyzing the absorption lines of targets

Research Assistant

Low-cost Photovoltaic Lighting System

05/2015 – 07/2017

Graphic Image and Signal processing Lab

- Drawing the image of solar trajectory function by Matlab
- Collecting the data of light intensity indoor and sending the data to FPGA
- Developing an embedded FPGA by Multisim to control the LED lights to balance the indoor illumination intensity; programming by Verilog HDL

CORE QUALIFICATIONS

- Strong knowledge of transistor-level analog IC design using Cadence tools
- Analog IC design experience encompassing circuit design, circuit simulation, and physical layout
- Solid understanding of ASIC design flow – logic design, verification, rtl coding, synthesis, timing and backend
- Strong knowledge in analyzing and designing switched capacitor and vco circuits
- Excellent analytical and problem solving skills
- Strong communication (written and verbal) and documentation skills