

# **C3D08065E**Silicon Carbide Schottky Diode Z-REC® RECTIFIER

 $V_{RRM}$  = 650 V  $I_F(T_c=135^{\circ}C)$  = 12 A  $Q_c$  = 20 nC

### **Features**

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>F</sub>

### **Benefits**

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

## **Applications**

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives

## **Package**







AEC-Q101 Qualified



Part Number	Package	Marking	
C3D08065E	TO-252-2	C3D08065	

# **Maximum Ratings** ( $T_c = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	650	V		
$V_{RSM}$	Surge Peak Reverse Voltage	650	V		
V <sub>DC</sub>	DC Blocking Voltage	650	V		
I <sub>F</sub>	Continuous Forward Current	25.5 12 8	А	T <sub>c</sub> =25°C T <sub>c</sub> =135°C T <sub>c</sub> =155°C	Fig. 3
I <sub>FRM</sub>	Repetitive Peak Forward Surge Current	34 25	А	$T_c$ =25°C, $t_p$ = 10 ms, Half Sine Wave $T_c$ =110°C, $t_p$ = 10 ms, Half Sine Wave	
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	71 60	А	$T_c$ =25°C, $t_p$ = 10 ms, Half Sine Wave $T_c$ =110°C, $t_p$ = 10 ms, Half Sine Wave	Fig. 8
$I_{\text{F,Max}}$	Non-Repetitive Peak Forward Surge Current	650 530	А	$T_c$ =25°C, $t_p$ = 10 $\mu$ s, Pulse $T_c$ =110°C, $t_p$ = 10 $\mu$ s, Pulse	Fig. 8
P <sub>tot</sub>	Power Dissipation	120 52	W	$T_c=25$ °C $T_c=110$ °C	Fig. 4
$T_{\scriptscriptstyle J}$ , $T_{\scriptscriptstyle stg}$	Operating Junction and Storage Temperature	-55 to +175	°C		



## **Electrical Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.5 2.1	1.8 2.4	V	$I_F = 8 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 8 \text{ A } T_J = 175^{\circ}\text{C}$	Fig. 1
$I_R$	Reverse Current	10 20	50 200	μΑ	$V_R = 650 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 650 \text{ V } T_J = 175^{\circ}\text{C}$	Fig. 2
$Q_{c}$	Total Capacitive Charge	20		nC	$V_R = 650 \text{ V, } I_F = 8A$ $di/dt = 500 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	Fig. 5
С	Total Capacitance	395 37 32		pF	$V_R = 0 \text{ V, } T_J = 25^{\circ}\text{C, f} = 1 \text{ MHz}$ $V_R = 200 \text{ V, } T_J = 25^{\circ}\text{C, f} = 1 \text{ MHz}$ $V_R = 400 \text{ V, } T_J = 25^{\circ}\text{C, f} = 1 \text{ MHz}$	Fig. 6
E <sub>c</sub>	Capacitance Stored Energy	3.0		μJ	V <sub>R</sub> = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Unit	Note
$R_{_{ heta JC}}$	Thermal Resistance from Junction to Case	1.25	°C/W	Fig. 9

## **Typical Performance**

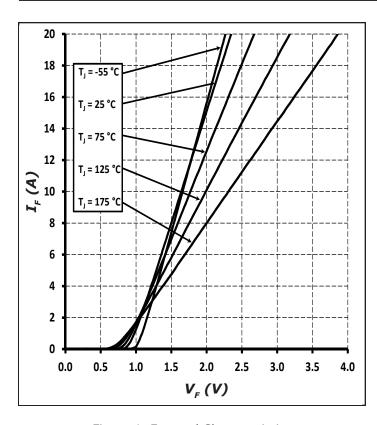


Figure 1. Forward Characteristics

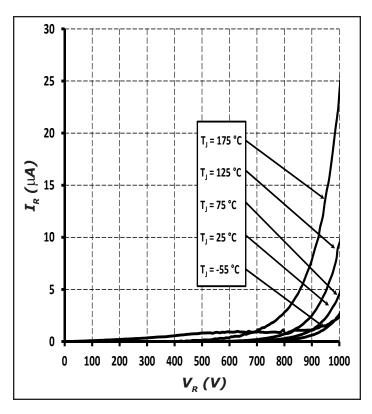
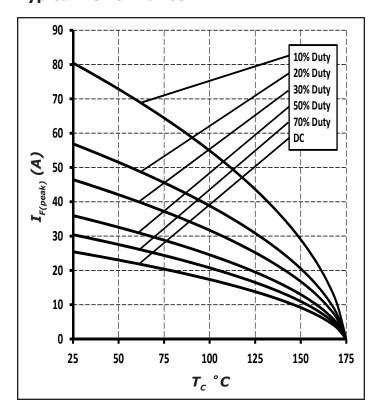


Figure 2. Reverse Characteristics



## **Typical Performance**



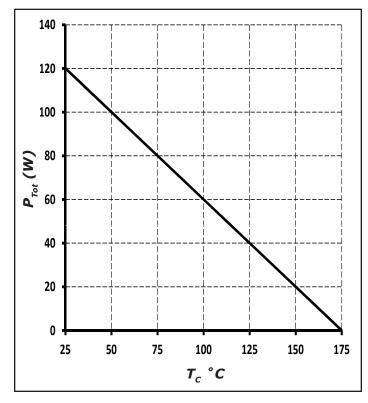


Figure 3. Current Derating

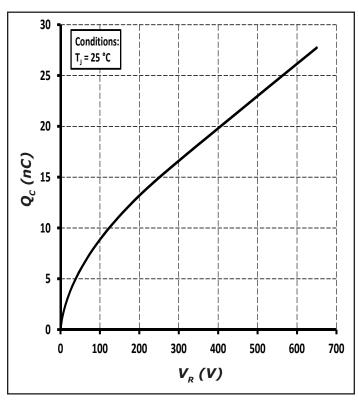


Figure 5. Total Capacitance Charge vs. Reverse Voltage

Figure 4. Power Derating

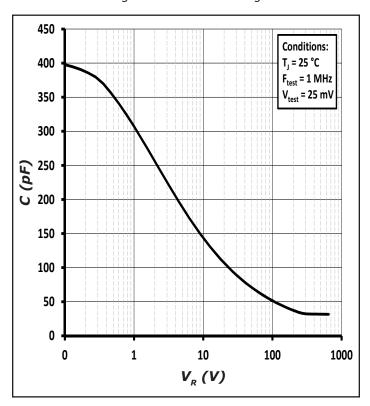


Figure 6. Capacitance vs. Reverse Voltage



## **Typical Performance**

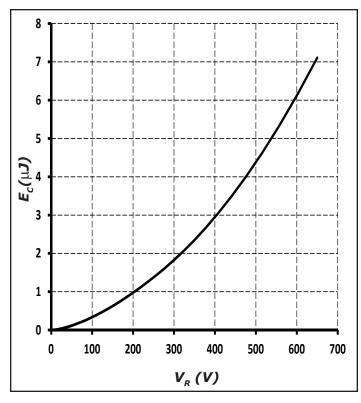


Figure 7. Capacitance Stored Energy

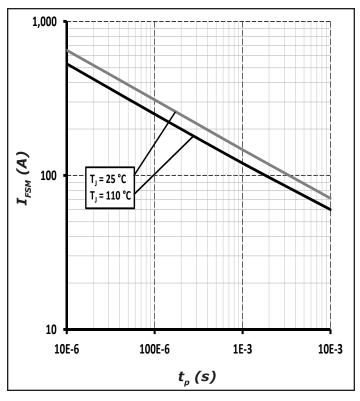


Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform)

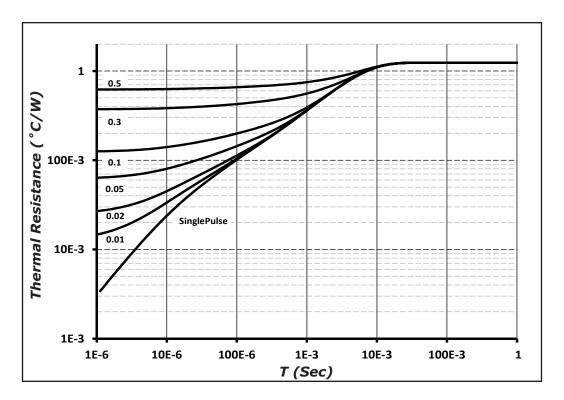
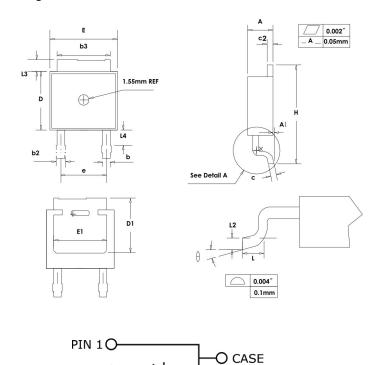


Figure 9. Transient Thermal Impedance



## **Package Dimensions**

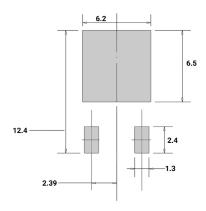
Package TO-252-2



CVMDOL	MILLIMETERS			
SYMBOL	MIN	MAX		
Α	2.159	2.413		
A1	0	0.13		
b	0.64	0.89		
b2	0.653	1.143		
b3	5.004	5.6		
С	0.457	0.61		
c2	0.457	0.864		
D	5.867	6.248		
D1	5.21	ı		
E	6.35	7.341		
E1	4.32	-		
e	4.58 BSC			
Н	9.65	10.414		
L	1.106	1.78		
L2	0.51 BSC			
L3	0.889	1.27		
L4	0.64	1.01		
θ	0°	8°		

## **Recommended Solder Pad Layout**

PIN 2O-



TO-252-2

Part Number	Package	Marking	
C3D08065E	TO-252-2	C3D08065	

Note: Recommended soldering profiles can be found in the applications note here: http://www.wolfspeed.com/power\_app\_notes/soldering





## **Diode Model**

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$$Vf_T = V_T + If * R_T$$

$$V_T = 0.95 + (T_J * -1.2*10^{-3})$$

$$R_T = 0.054 + (T_1 * 5.5*10^{-4})$$

Note: T<sub>j</sub> = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

#### **Notes**

#### RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Ecology section of our website at http://www.wolfspeed.com/power/tools-and-support/product-ecology.

#### REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into
the human body nor in applications in which failure of the product could lead to death, personal injury or property
damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines,
cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control
systems, or air traffic control systems.

#### **Related Links**

- Cree SiC Schottky diode portfolio: http://www.wolfspeed.com/Power/Products#SiCSchottkyDiodes
- Schottky diode Spice models: http://www.wolfspeed.com/power/tools-and-support/DIODE-model-request2
- SiC MOSFET and diode reference designs: http://go.pardot.com/l/101562/2015-07-31/349i