

NI SPEEDY-33

User Manual

Signal Processing Engineering Educational Device for Youth

Worldwide Technical Support and Product Information

ni.com

National Instruments Corporate Headquarters

11500 North Mopac Expressway Austin, Texas 78759-3504 USA Tel: 512 683 0100

Worldwide Offices

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Consult the FCC Web site at www.fcc.gov for more information.

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Changes or modifications not expressly approved by NI could void the user's authority to operate the equipment under the FCC Rules.

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Federal Communications Commission

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Canadian Department of Communications

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* The CE marking Declaration of Conformity contains important supplementary information and instructions for the user or installer.

Conventions

The following conventions are used in this manual:

<>

Angle brackets that contain numbers separated by an ellipsis represent a range of values associated with a bit or signal name—for example, AO <3..0>.

»

The » symbol leads you through nested menu items and dialog box options to a final action. The sequence **File»Page Setup»Options** directs you to pull down the **File** menu, select the **Page Setup** item, and select **Options** from the last dialog box.



This icon denotes a note, which alerts you to important information.



This icon denotes a caution, which advises you of precautions to take to avoid injury, data loss, or a system crash.

bold

Bold text denotes items that you must select or click in the software, such as menu items and dialog box options. Bold text also denotes parameter names.

italic

Italic text denotes variables, emphasis, a cross reference, or an introduction to a key concept. Italic text also denotes text that is a placeholder for a word or value that you must supply.

monospace

Text in this font denotes text or characters that you should enter from the keyboard, sections of code, programming examples, and syntax examples. This font is also used for the proper names of disk drives, paths, directories, programs, subprograms, subroutines, device names, functions, operations, variables, filenames, and extensions.

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NI SPEEDY-33 Overview

The NI SPEEDY-33 (Signal Processing Engineering Educational Device for Youth) featuring the Texas Instruments VC33 DSP is a self-contained, high-performance, programmable product for signal processing applications. It boasts an easy-to-use, fast Digital Signal Processor (DSP) along with a number of features important to many signal processing applications. The NI SPEEDY-33 onboard flash memory, together with an easy-to-learn, easy-to-use software programming tool, supports the quick creation of standalone DSP-based products.

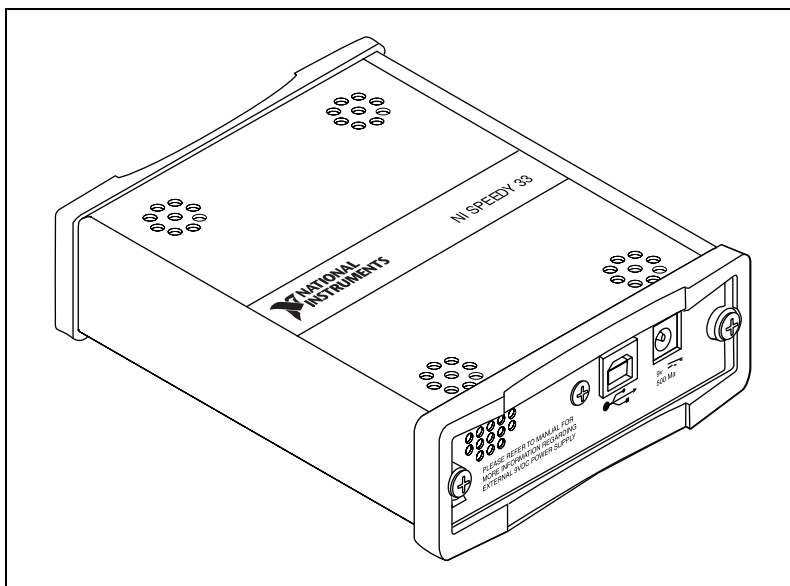


Figure 1-1. The NI SPEEDY-33

Theory of Operation

The NI SPEEDY-33 is a low-cost, high-performance floating-point TMS320VC33-based DSP system connected to a standard PC through the USB port. This easy-to-use system supports a variety of DSP processing, including audio applications with two input/output analog channels sampled at 48 kHz, and other applications with onboard digital I/O for controlling motors/servos. The NI SPEEDY-33 features $34\text{ K} \times 32$ words of on-chip memory. The $512\text{ K} \times 8$ onboard flash memory allows for storage of both the program application (programmed with the LabVIEW DSP Module), as well as data such as tables, sound waveforms, and so on.

The NI SPEEDY-33 has eight lines of digital I/O, arranged as an eight-bit switch input port, and eight digital output LEDs. The digital I/O lines can be programmed with the LabVIEW DSP Module software. The eight inputs and eight outputs are also accessible through the simple expansion digital I/O connector. After the flash memory is programmed, the NI SPEEDY-33 can be unplugged from the PC and run in standalone mode.

Safety Information

The following section contains important safety information that you must follow when installing and using the module.

Do not operate the module in a manner not specified in this document. Misuse of the module can result in a hazard. You can compromise the safety protection built into the module if the module is damaged in any way. If the module is damaged, return it to National Instruments (NI) for repair.

Do not substitute parts or modify the module except as described in this document. Use the module only with the chassis, modules, accessories, and cables specified in the installation instructions. You must have all covers and filler panels installed during operation of the module.

Do not operate the module in an explosive atmosphere or where there may be flammable gases or fumes. If you must operate the module in such an environment, it must be in a suitably rated enclosure.

If you need to clean the module, use a soft, nonmetallic brush. Make sure that the module is completely dry and free from contaminants before returning it to service.

Operate the module only at or below Pollution Degree 2. Pollution is foreign matter in a solid, liquid, or gaseous state that can reduce dielectric strength or surface resistivity. The following is a description of pollution degrees:

- Pollution Degree 1 means no pollution or only dry, nonconductive pollution occurs. The pollution has no influence.
- Pollution Degree 2 means that only nonconductive pollution occurs in most cases. Occasionally, however, a temporary conductivity caused by condensation must be expected.
- Pollution Degree 3 means that conductive pollution occurs, or dry, nonconductive pollution occurs that becomes conductive due to condensation.

You must insulate signal connections for the maximum voltage for which the module is rated. Do not exceed the maximum ratings for the module. Do not install wiring while the module is live with electrical signals. Do not remove or add connector blocks when power is connected to the system. Avoid contact between your body and the connector block signal when hot swapping modules. Remove power from signal lines before connecting them to or disconnecting them from the module.

Operate the module at or below the measurement category¹ marked on the hardware label. Measurement circuits are subjected to working voltages² and transient stresses (overvoltage) from the circuit to which they are connected during measurement or test. Measurement categories establish standard impulse withstand voltage levels that commonly occur in electrical distribution systems. The following is a description of measurement categories:

- Measurement Category I is for measurements performed on circuits not directly connected to the electrical distribution system referred to as MAINS³ voltage. This category is for measurements of voltages from specially protected secondary circuits. Such voltage measurements include signal levels, special equipment, limited-energy parts of equipment, circuits powered by regulated low-voltage sources, and electronics.
- Measurement Category II is for measurements performed on circuits directly connected to the electrical distribution system. This category

¹ Measurement categories, also referred to as *installation categories*, are defined in electrical safety standard IEC 61010-1.

² Working voltage is the highest rms value of an AC or DC voltage that can occur across any particular insulation.

³ MAINS is defined as a hazardous live electrical supply system that powers equipment. Suitably rated measuring circuits may be connected to the MAINS for measuring purposes.

refers to local-level electrical distribution, such as that provided by a standard wall outlet (for example, 115 AC voltage for U.S. or 230 AC voltage for Europe). Examples of Measurement Category II are measurements performed on household appliances, portable tools, and similar modules.

- Measurement Category III is for measurements performed in the building installation at the distribution level. This category refers to measurements on hard-wired equipment such as equipment in fixed installations, distribution boards, and circuit breakers. Other examples are wiring, including cables, bus bars, junction boxes, switches, socket outlets in the fixed installation, and stationary motors with permanent connections to fixed installations.
- Measurement Category IV is for measurements performed at the primary electrical supply installation (<1,000 V). Examples include electricity meters and measurements on primary overcurrent protection devices and on ripple control units.

Installation

To install and set up the NI SPEEDY-33, refer to the *NI SPEEDY-33 Installation Guide*. You can find this document on the NI SPEEDY-33 User Documentation CD or the National Instruments Web site at ni.com/manuals.

Software, Drivers, and Examples

The NI SPEEDY-33 is supported by the LabVIEW DSP Module. A variety of example DSP applications are standard with the LabVIEW DSP Module.

Refer to the *LabVIEW DSP Module Release and Upgrade Notes* for information about installing the LabVIEW DSP Module software and the NI SPEEDY-33 driver onto your computer. You can find this document on the National Instruments Web site at ni.com/manuals.

For a tutorial on using the LabVIEW DSP Module with the NI SPEEDY-33, refer to the *Getting Started with the LabVIEW DSP Module* document. You can find this document by selecting **Start» All Programs»National Instruments»LabVIEW»LabVIEW Manuals» DSP_Getting_Started.pdf**.

Example applications for the NI SPEEDY-33 can be found in the NI Example Finder; in LabVIEW, select **Help»Find Examples**.

All NI user documentation is available on the National Instruments Web site at ni.com/manuals.

NI SPEEDY-33 Functional Description and Interface

This chapter describes the NI SPEEDY-33 hardware functions and details of interfacing and configuring the device. Figure 2-1 shows the NI SPEEDY-33 layout.

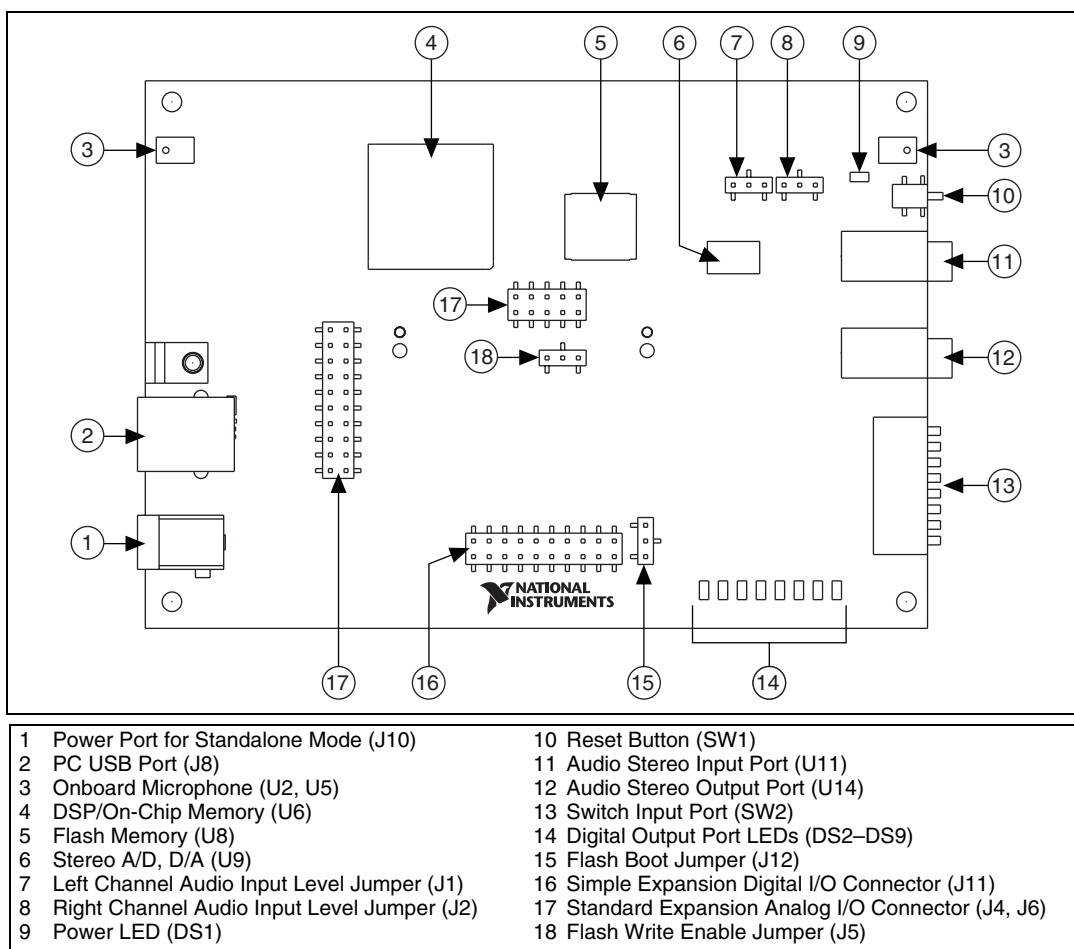


Figure 2-1. NI SPEEDY-33 Top View

DSP

The digital signal processor on the NI SPEEDY-33 is a powerful floating-point, flexible, and easy-to-use processor designed by Texas Instruments.

The VC33 DSP is capable of high performance mathematical operations. It is a 32-bit, floating-point processor manufactured in 0.18 μm four-level-metal CMOS (Timeline™) technology, and is part of the SM320C3x™ generation of DSPs from Texas Instruments.

The VC33 DSP internal busing and special digital signal processing instruction set have the speed and flexibility to execute up to 150 million floating-point operations per second (MFLOPS). The VC33 DSP optimizes speed by implementing functions in hardware that other processors implement through software or microcode. This hardware-intensive approach provides performance previously unavailable on a single chip.



Note Although this DSP is capable of 75 MIPS, 150 MFLOPS, the crystal driving the DSP on the NI SPEEDY-33 is slightly slower than what would be required to achieve this maximum speed, namely 14.7456 MHz as opposed to the maximum 15 MHz. This allows for specific desirable sample rates to be achieved for the A/D and D/A hardware described in the [Stereo A/D, D/A \(Analog Input, Analog Output\)](#) section. Refer to the Appendix A, [Specifications](#), for information about DSP speed.

The VC33 DSP can perform parallel multiply and ALU operations on integer or floating-point data in a single cycle. Each processor possesses a general-purpose register file, a program cache, dedicated ARAUs, internal dual-access memories, one DMA channel supporting concurrent I/O, and a short machine-cycle time, resulting in a high-performance, easy-to-use device.

Typical signal processing applications are enhanced by the large address space, multiprocessor interface, internally- and externally-generated wait states, one external interface port, two timers, one serial port, and a multiple-interrupt structure. The VC33 DSP supports a wide variety of system applications from host processor to dedicated coprocessor. High-level language support is easily implemented through a register-based architecture, large address space, powerful addressing modes, flexible instruction set, and well-supported floating-point arithmetic.

Figure 2-2 shows the architectural details of the VC33 DSP.

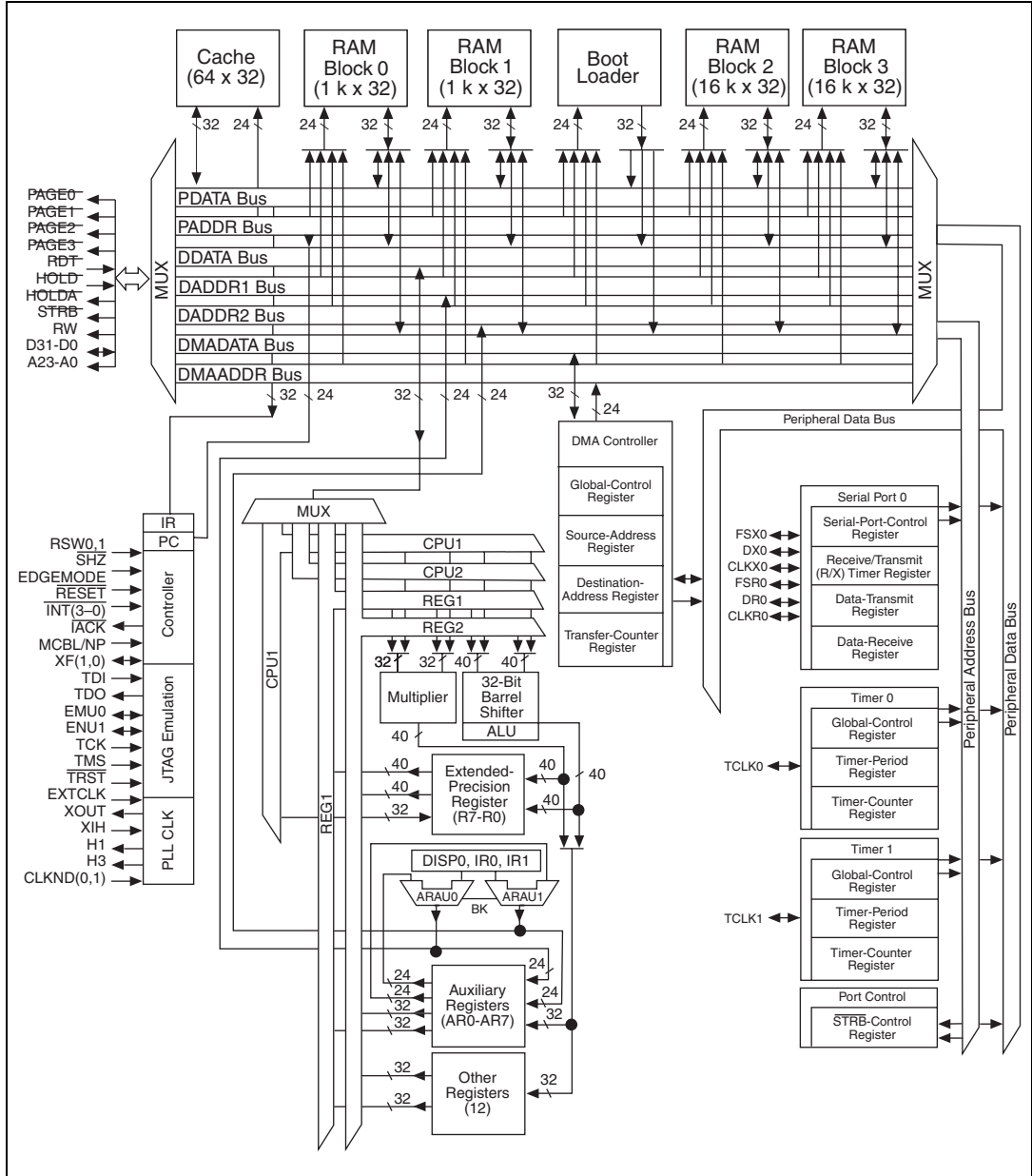


Figure 2-2. VC33 DSP Functional Block Diagram

Memory Map

The DSP runs in microcomputer/bootloader mode on the NI SPEEDY-33. The memory map for the NI SPEEDY-33 is shown in Figure 2-3.

0h	Reserved for Bootloader Operations
FFFh	
1000h	External RAM (1008 KB) 5 Wait States
3FFFFh	
40000h	(Empty)
3FFFFFFh	
400000h	Flash Memory (2048 KB in Space, Byte-Wide, Lowest Byte) 7 Wait States
47FFFFh	
480000h	(Empty)
7FFFFFFh	
800000h	Internal RAM Block 2 (64 KB)
803FFFh	
804000h	Internal RAM Block 3 (64 KB)
807FFFh	
808000h	Peripheral Bus Memory-Mapped Registers (24 KB Internal)
8097FFh	
809800h	Internal RAM Block 0 (4 KB)
809BFFh	
809C00h	Internal RAM Block 1 (4 KB)
809FC0h	
809FC1h	User Program Interrupt and Trap Branch Table
809FFFh	
	(Empty)
80B000h	Board Status/CTL, XXF
80B003h	USB Peripheral Status
80B004h	USB Host EVEN
80B005h	USB Host ODD
80B007h	Switch Read/LED Write
C00000h	
	CompactFlash Control/Data
C0000Fh	
FFF000h	
	USB Boot Area
FFFFFFh	

Figure 2-3. NI SPEEDY-33 Memory Map

Memory

There are two types of available memory on the NI SPEEDY-33, on-chip memory and flash memory.

On-Chip Memory

The DSP uses on-chip memory for algorithms because of its fast speed. There are 136 KB of on-chip RAM that the DSP can access with zero wait states. This memory is used for both program and data space.

Flash Memory

The NI SPEEDY-33 includes flash memory that allows the device to be programmed and run in standalone mode without connection to a PC. This is essential for producing actual products or self-standing prototypes. Refer to the [Flash Boot Jumper](#) section for information on configuring the jumper to enable flash memory boot up for standalone mode.

The flash memory is byte-wide and organized as $512\text{ K} \times 8$, with the DSP mapping it to a $512\text{ K} \times 32$ (2,048 KB) area, only able to read the lowest byte (upper 24 bits are not read by or written to the DSP). Refer to the [Flash Write Enable Jumper](#) section for information on jumper configurations for enabling or disabling flash memory write protection.

Ports

For a diagram of port locations on the NI SPEEDY-33, refer to Figure 2-1.

PC USB Port

The PC USB port (J8) connects the NI SPEEDY-33 (target) to a PC (host) with a standard USB cable. The NI SPEEDY-33 functions as a full-speed USB device when connected to the PC host. The PC USB port is a Type B USB port (peripheral USB port) and conforms to USB Specification 1.1. When the NI SPEEDY-33 is connected to the PC, the USB port supplies power to the device, eliminating the need for the power port (J10), as described in the [Power Port](#) section. When power is supplied to the device, the power LED lights.

Power Port



Caution Do *not* connect the power source to the power port while the NI SPEEDY-33 device is connected to the PC.

The power port (J10) can supply the NI SPEEDY-33 with external power when operating the device in standalone mode, without being connected to the PC through USB. When power is supplied to the device, the power LED lights.

The input voltage must be 9 VDC, at 500 mA, with the outside contact being ground and inner contact being positive VDC. An appropriate 2.5 mm jack can be used in conjunction with a 9 V battery (pack) to optionally supply power for battery-powered applications. Refer to Appendix A, [Specifications](#), for complete power supply specifications.

Audio Stereo Input Port

Audio input can be connected to the audio stereo input port (U11).



Caution Ensure that line level audio signals are *not* connected to the audio input when the NI SPEEDY-33 is configured for microphone level. It is possible to damage the audio input circuitry by overdriving the input.

If an external microphone is used, it should be capable of operating with a standard stereo jack; not all microphones will work in this fashion. Many PC microphones will not work because they require a DC voltage to be driven on one of the inputs. Traditional mono microphones generally work fine.

It is important to ensure that the audio input level jumpers (J1, J2) setting is configured appropriately for microphone level or line level, depending upon the audio source connected to this port. Refer to the [Audio Input Level Jumpers](#) section for more information.



Note The onboard microphones are disconnected by mechanical disconnect when an audio source is connected to the audio stereo input port.

Audio Stereo Output Port

The audio stereo output port (U14) can be connected to a set of stereo amplified speakers. Typical amplified speakers used for PC sound should provide good output. You also can plug external headsets to this port, although there is no hardware control over the output gain or signal level.

Onboard Microphones

Two onboard microphones (U2 and U5) can be used in applications requiring audio input or involving audio signal phase difference measurements. When the microphones are used, the input gain level of each microphone should be set to the microphone level setting described in the [Audio Input Level Jumpers](#) section.



Caution Ensure that line level audio signals are *not* connected to the audio input when the NI SPEEDY-33 is configured for microphone level. It is possible to damage the audio input circuitry by overdriving the input.

The onboard microphones are directly connected to the audio input conditioning circuitry, unless an external microphone is connected at the stereo input port (U11), as described in the [Audio Input Level Jumpers](#) section. If an external microphone is connected, the onboard microphones are disconnected from the input circuitry by mechanical disconnect.

Digital Output Port LEDs

Eight memory-mapped LEDs are located on the NI SPEEDY-33 for general-purpose output. The LEDs are enabled by writing a 1 to the appropriate bit of the LED port. The LED output state is echoed on pins on the simple expansion digital I/O connector (J11), as described in the [Simple Expansion Digital I/O Connector](#) section.

Switch Input Port

There are eight memory-mapped general-purpose switch inputs on the NI SPEEDY-33. These switch inputs are accessed by the DSP through reading the appropriate bit of the switch input port. The switch inputs are connected in parallel to pins on the simple expansion digital I/O connector (J11). All switches should be in the OFF (open) position when connecting the expansion digital I/O connector, as described in the [Simple Expansion Digital I/O Connector](#) section.

Stereo A/D, D/A (Analog Input, Analog Output)

The 16-bit stereo audio CODEC included on the NI SPEEDY-33 allows for up to 48 kHz dual-channel sampling on the input signal. Software components are included in the LabVIEW DSP Module to allow 8 kHz, 18 kHz, 24 kHz, 36 kHz, and 48 kHz sample rates to be used in applications.

Refer to the [Audio Input Level Jumpers](#) section for information on jumper settings that configure the amount of gain, from the onboard or external microphones, applied to the input audio signals.

I/O Connectors

Simple Expansion Digital I/O Connector

The simple 20-pin expansion header (J11) allows for easy interface to external hardware. The header includes power, ground, eight digital inputs, and eight digital outputs under DSP control.

The eight input bits are co-mapped to the switch input port. The eight output bits are co-mapped to the digital output port LEDs, as described in the [Digital Output Port LEDs](#) section.

If the input bits on this connector will be used by an external piece of hardware, the switch input port—described in the [Switch Input Port](#) section—must have all the switches set in the OFF (open) position (all switches up, away from device). An ON (closed) switch position (switch down, towards the device) will effectively short that specific input bit to ground. Switch 1 correlates to IN1 on the connector, switch 2 correlates to IN2, and so on. The connector pinout is shown in Figure 2-4.

5 V (Out)	1	2	3 V (Out)
IN1 (In)	3	4	OUT1 (Out)
IN2 (In)	5	6	OUT2 (Out)
IN3 (In)	7	8	OUT3 (Out)
IN4 (In)	9	10	OUT4 (Out)
IN5 (In)	11	12	OUT5 (Out)
IN6 (In)	13	14	OUT6 (Out)
IN7 (In)	15	16	OUT7 (Out)
IN8 (In)	17	18	OUT8 (Out)
GND	19	20	ResetLow (In)

Figure 2-4. Simple Expansion Digital I/O Connector (J11)

The digital I/O signals are 3.3 V, but the inputs are 5 V tolerant. Although not required, pin 20 (ResetLow) can be driven low to reset the DSP; it is pulled high by the NI SPEEDY-33.

Standard Expansion Analog I/O Connectors

A set of two connectors, J4 and J6, make up the standard expansion analog I/O connectors, that can be used for optional daughter modules. The daughter modules can be used for applications requiring different A/D and/or D/A functionality than that provided by the standard 16-bit stereo CODEC normally found on the NI SPEEDY-33.

Refer to the *Connecting Accessories to the NI SPEEDY-33* section for information on NI and third-party daughter modules for use with the NI SPEEDY-33.

Connecting Accessories to the NI SPEEDY-33

Refer to Figure 2-1 to connect accessories to the NI SPEEDY-33 as described in Table 2-1.



Caution Do *not* connect the power source to the power port while the NI SPEEDY-33 device is connected to the PC.

Table 2-1. NI SPEEDY-33 Accessories

Accessory	NI SPEEDY-33 Port
Microphone, CD player, MP3 player, PDA, cell phone, or any peripheral with mini jack connector output	Audio stereo input port
Audio speakers (included with Infinity Technology Kit) or any audio speakers or headphones with mini jack connector input	Audio stereo output port
Any 9 VDC at 500 mA power port with appropriate safety and EMC Certification marks, which are acceptable in the country in which the product is to be installed	Power port

Jumpers

Audio Input Level Jumpers

The audio input level jumpers (J1, J2) control the amount of gain applied to the input audio signals.

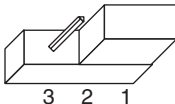
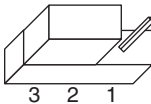
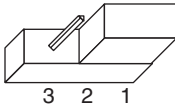
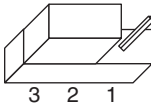
The microphone level setting has the highest gain for the onboard microphones (U2, U5), the line level setting has less gain and is appropriate for the line level audio interface (for example, CD players, MP3 players, and so on).

These settings will affect the gain of the external audio source and should be left in the line level setting if an external microphone or audio source is used at audio stereo input port (U10), described in the [Stereo A/D, D/A \(Analog Input, Analog Output\)](#) section. Table 2-2 shows the possible audio input level settings.



Caution Ensure that line level audio signals are *not* connected to the audio input when the NI SPEEDY-33 is configured in the microphone level setting. It is possible to damage the audio input circuitry by overdriving the input.

Table 2-2. Audio Input Level Jumpers (J1, J2) Settings

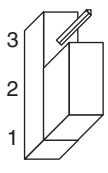
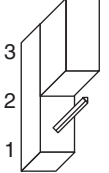
Jumper	Microphone Level (Default Setting)	Line Level
Audio Input Level, Left Channel (J1)	Position 1 	Position 3 
Audio Input Level, Right Channel (J2)	Position 1 	Position 3 

Flash Boot Jumper

The flash boot jumper (J12) controls whether the DSP will attempt to boot from the flash memory. The default setting is for the DSP to boot from flash memory, described in the [Flash Memory](#) section. You must set the NI SPEEDY-33 to the enable flash boot setting to run the device in standalone mode.

If the NI SPEEDY-33 will only be used with the USB connection to PC, the flash boot can be disabled, though it is not recommended. Table 2-3 shows the possible flash boot settings.

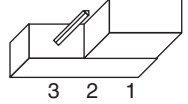
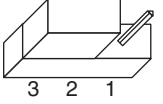
Table 2-3. Flash Boot Jumper (J12) Settings

Jumper	Enable Flash Boot (Default Setting)	Disable Flash Boot
Flash Boot	Position 1 	Position 3 

Flash Write Enable Jumper

The flash write enable jumper (J5) controls whether the DSP writes to the flash memory as described in the [Flash Memory](#) section. This configuration setting is useful for write protecting the DSP algorithm for production purposes. When the flash write is disabled, the DSP cannot modify the contents. Table 2-4 shows the possible flash write enable settings.

Table 2-4. Flash Write Enable Jumper (J5) Settings

Jumper	Enable Flash Write (Default Setting)	Disable Flash Write
Flash Write Enable	Position 1 	Position 3 

NI SPEEDY-33 Enclosure

The NI SPEEDY-33 comes encased in an enclosure for durability. To access hardware features such as the jumpers or the extension I/O connectors, remove the SPEEDY-33 board from the enclosure. To access the SPEEDY-33 board, remove all screws from either the front or back end of the enclosure and remove the metal end plate. When the metal end plate is removed, gently slide the SPEEDY-33 board and the top enclosure panel along the metal rails of the enclosure.



Caution When reassembling the SPEEDY-33 enclosure, be careful not to bend the pins of the extension I/O connectors.

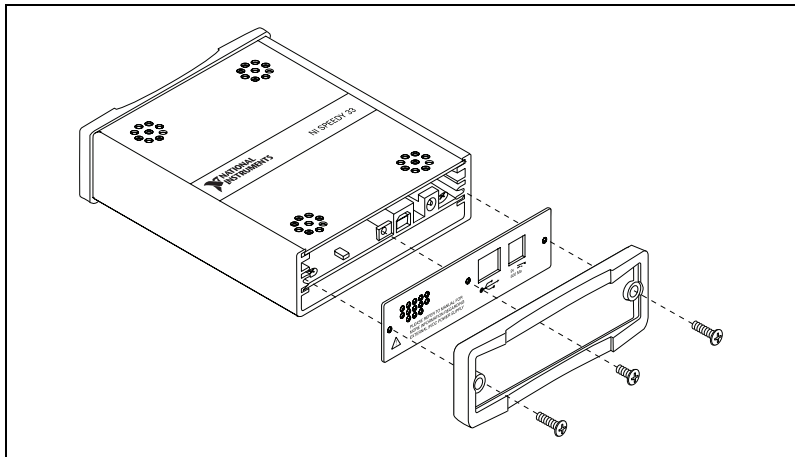


Figure 2-5. NI SPEEDY-33 Assembly View

Reset Button

The reset button (SW1) is a small push button on the NI SPEEDY-33 that manually resets the DSP. The DSP needs to be reset in the event of a software or hardware freeze.

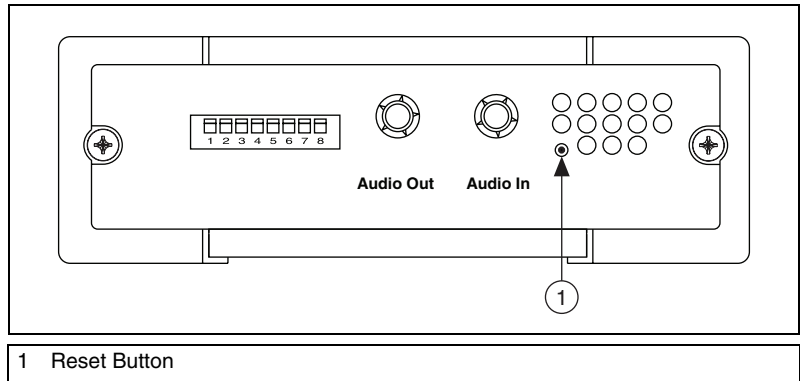


Figure 2-6. NI SPEEDY-33 Front View

Specifications

Specifications listed below are typical at 25 °C unless otherwise noted.

Analog I/O

Input Stereo 48 kHz 16 bit A/D

Output..... Stereo 48 kHz 16 bit D/A

Supported sampling rates 8 kHz, 18 kHz, 24 kHz, 36 kHz,
48 kHz

Maximum working voltage for analog inputs
(signal + common mode) 600 mV

Maximum working voltage for analog outputs
(signal + common mode) 1.41 V

Input impedance

 Left to AI GND..... 30 k Ω

 Right to AI GND..... 30 k Ω

Digital I/O

Input 8 bit switch,
pins <IN1..IN8> on J11

Output..... 8 bit LEDs,
pins <OUT1..OUT8> on J11

Maximum working voltage TTL 5 V signal

Memory

On-chip RAM 34 K \times 32

Flash 512 K \times 8

DSP

DSP.....TMS320VC33

- High-performance floating-point digital signal processor (DSP)
 - 150 million floating-point operations per second (MFLOPS)
 - 75 million instructions per second (MIPS)
- 34 K × 32 bit (1.1-Mbit) on-chip words of dual-access static random-access memory (SRAM) configured in 2 × 16 K plus 2 × 1 K blocks to improve internal performance
- 32-bit high-performance CPU
- 16/32-bit integer and 32/40-bit floating-point operations
- Boot-program loader
- 32-Bit instruction word, 24-Bit Addresses
- Fabricated using the 0.18-μm (l_{eff} -effective gate length) Timeline technology by Texas Instruments (TI)
- On-chip memory-mapped peripherals
 - Direct Memory Access (DMA)
 - Coprocessor for concurrent I/O and CPU operation
- Parallel arithmetic/logic unit (ALU) and multiplier execution in a single cycle
- Supports standalone operation

Bus Interface

USBFull speed 1.1

Power Requirements

Input voltage

External power supply

powered operation9 VDC at 500 mA at power port with appropriate safety and EMC Certification marks, which are acceptable in the country in which the product is to be installed

USB powered operationUSB bus power

Device maximum current0.233 A

Physical

PCB only

Dimensions 8.89 cm × 12.7 cm × 1.91 cm
(3.5 in. × 5 in. × 0.75 in.)

Weight..... 87.9 g (3.1 oz)

With enclosure

Dimensions 11.1 cm × 14.5 cm × 3.9 cm
(4.4 in. × 5.7 in. × 1.5 in.)

Weight..... 380 g (12.8 oz)

USB connector Type B USB port, conforms to
USB Specification 1.1

Connector Standard mini stereo jack

I/O connectors

J1, J2, J5, J12 0.10 in. × 0.10 in. × 0.23 in.
in 1 × 3 rows

J4..... 0.10 in. × 0.10 in. × 0.23 in.
in 2 × 5 rows

J6, J11 0.10 in. × 0.10 in. × 0.23 in.
in 2 × 10 rows

Environmental

The NI SPEEDY-33 device is intended for indoor use only.

Operating temperature..... 0 to 55 °C

Storage temperature –20 to 70 °C

Humidity 10 to 90% RH, noncondensing

Maximum altitude 2,000 m

Pollution Degree 2

If you need to clean the module, use a soft, nonmetallic brush. Make sure that the module is completely dry and free from contaminants before returning it to service.

Safety

This product is designed to meet the requirements of the following standards of safety for electrical equipment for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA 61010-1



Note For UL and other safety certifications, refer to the product label, or visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

Electromagnetic Compatibility

This product is designed to meet the requirements of the following standards of EMC for electrical equipment for measurement, control, and laboratory use:

- EN 61326 EMC requirements; Minimum Immunity
- EN 55011 Emissions; Group 1, Class A
- CE, C-Tick, ICES, and FCC Part 15 Emissions; Class A



Note For EMC compliance, operate this device according to product documentation.

CE Compliance

This product meets the essential requirements of applicable European Directives, as amended for CE Marking, as follows:

- 2006/95/EC; Low-Voltage Directive (safety)
- 2004/108/EC; Electromagnetic Compatibility Directive (EMC)



Note Refer to the Declaration of Conformity (DoC) for this product for any additional regulatory compliance information. To obtain the DoC for this product, visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

Environmental Management

National Instruments is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial not only to the environment but also to NI customers.

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Waste Electrical and Electronic Equipment (WEEE)



EU Customers At the end of their life cycle, all products *must* be sent to a WEEE recycling center. For more information about WEEE recycling centers and National Instruments WEEE initiatives, visit ni.com/environment/weee.htm.

电子信息产品污染控制管理办法（中国 RoHS）



中国客户 National Instruments 符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。关于 National Instruments 中国 RoHS 合规性信息，请登录 ni.com/environment/rohs_china。(For information about China RoHS compliance, go to ni.com/environment/rohs_china.)

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For information about other technical support options in your area, visit ni.com/services, or contact your local office at ni.com/contact.
- **Training and Certification**—Visit ni.com/training for self-paced training, eLearning virtual classrooms, interactive CDs, and Certification program information. You also can register for instructor-led, hands-on courses at locations around the world.
- **System Integration**—If you have time constraints, limited in-house technical resources, or other project challenges, National Instruments Alliance Partner members can help. To learn more, call your local NI office or visit ni.com/alliance.

- **Declaration of Conformity (DoC)**—A DoC is our claim of compliance with the Council of the European Communities using the manufacturer’s declaration of conformity. This system affords the user protection for electromagnetic compatibility (EMC) and product safety. You can obtain the DoC for your product by visiting ni.com/certification.

If you searched ni.com and could not find the answers you need, contact your local office or NI corporate headquarters. Phone numbers for our worldwide offices are listed at the front of this manual. You also can visit the Worldwide Offices section of ni.com/niglobal to access the branch office Web sites, which provide up-to-date contact information, support phone numbers, email addresses, and current events.

Glossary

Symbol	Prefix	Value
μ	micro	10^{-6}
m	milli	10^{-3}
k	kilo	10^3
M	mega	10^6

Numbers/Symbols

%	percent
+	positive of, or plus
–	negative of, or minus
\pm	plus or minus
<	less than
>	greater than
\leq	less than or equal to
\geq	greater than or equal to
/	per
°	degree
Ω	ohm
+5 V (signal)	+5 VDC source signal

A

A	amperes
A/D	Analog-to-Digital
AC	Alternating Current
ADC	Analog-to-Digital Converter—An electronic device, often an integrated circuit, that converts an analog voltage to a digital number.
ADC resolution	The resolution of the ADC measured in bits. A 16-bit ADC has a higher resolution, and thus a higher degree of accuracy, than a 12-bit ADC.
ADE	Application development environment such as LabVIEW, LabWindows™/CVI™, BridgeVIEW, Visual Basic, C, and C++.
AI	Analog Input
analog	A signal whose amplitude can have a continuous range of values.
AO	Analog Output

B

b	bit—One binary digit, either 0 or 1.
B	byte—Eight related bits of data, an eight-bit binary number. Also used to denote the amount of memory required to store one byte of data.
bandwidth	The range of frequencies present in a signal, or the range of frequencies to which a measuring device can respond.
bit	One binary digit, either 0 or 1.
boot	The way you start or restart your computer or device. The procedure that is run immediately following hardware reset or power-up.
buffer	Temporary storage for acquired or generated data (software).

bus	The group of conductors that interconnect individual circuitry in a computer. Typically, a bus is the expansion interface to which I/O or other devices are connected. Examples of PC buses are the ISA bus and PCI bus.
BW	bandwidth
byte	eight related bits
C	
C	Celsius
CE	European emissions control standard
CFR	Code of Federal Regulations
CH	channel
channel	Pin or wire lead to which you apply or from which you read the analog or digital signal. Analog signals can be single-ended or differential. For digital signals, you group channels to form ports. Ports usually consist of either four or eight digital channels.
CMRR	Common-Mode Rejection Ratio—A measure of the ability of a differential amplifier to reject interference from a common-mode signal, usually expressed in decibels (dB).
CMV	Common-Mode Voltage
CODEC	coder-decoder—An electronic device that converts analog signals, such as video and voice signals, into digital form, and then compresses them to conserve bandwidth on a transmission path. The NI SPEEDY-33 CODEC also converts digital signals into analog form.
connector	A fixture (either male or female) attached to a cable or chassis for quickly making and breaking one or more circuits.

D

D/A	Digital-to-Analog
DAQ	<p>data acquisition—(1) Collecting and measuring electrical signals from sensors, transducers, and test probes or fixtures and processing the measurement data using a computer.</p> <p>(2) Collecting and measuring the same kinds of electrical signals with A/D and/or DIO boards plugged into a computer, and possibly generating control signals with D/A and/or DIO boards in the same computer.</p>
DAQ device	A data acquisition device. Examples are DIO, E Series MIO, and Lab/1200 plug-in data acquisition devices.
dB	decibel—The unit for expressing a logarithmic measure of the ratio of two signal levels: $\text{dB} = 20\log_{10} V_1/V_2$, for signals in volts.
DC	Direct Current
DC coupled	Allowing the transmission of both AC and DC signals.
default setting	A default parameter value recorded in the driver. In many cases, the default input of a control is a certain value (often 0) that means <i>use the current default setting</i> .
device	A plug-in data acquisition product, card, or pad that can contain multiple channels and conversion devices. Plug-in products, PCMCIA cards, and devices such as the DAQPad-1200, which connects to your computer parallel port, are all examples of DAQ devices. SCXI modules are distinct from devices, with the exception of the SCXI-1200, which is a hybrid.
digital	An electronic technology where a signal only has two states: off and on, most often called zero and one. In contrast, analog refers to a signal that can have a continuous range of values.
digital I/O	<p>The capability of an instrument to generate and acquire digital signals.</p> <p>Static digital I/O refers to signals where the values are set and held, or rarely change. Dynamic digital I/O refers to digital systems where the signals are continuously changing, often at multi-MHz clock rates. The NI SPEEDY-33 is a static digital I/O device.</p>
digital port	See port .

DIO	Digital Input/Output
DMA	Direct Memory Access—A method by which data is transferred to/from computer memory from/to a device or memory on the bus while the processor does something else. DMA is the fastest method of transferring data to/from computer memory.
DR	Data Receive
drivers/driver software	Software that controls a specific hardware device such as a DAQ device.
DSP	(1) Digital Signal Processing (2) Digital Signal Processor—a specialized microprocessor designed specifically for digital signal processing, generally in real-time. Digital signal processors can also be used to perform general-purpose computation, though they are not optimized for this function.
dual-access memory	Memory that can be sequentially accessed by more than one controller or processor but not simultaneously accessed. Also known as shared memory.

E

EEPROM	Electrically Erasable Programmable Read-Only Memory—ROM that can be erased with an electrical signal and reprogrammed.
EMC	electromagnetic compliance
EMI	electromagnetic interference
event	The condition or state of an analog or digital signal.
expansion connector	A connector on the device for connecting additional signals to the DSP.

F

F	(1) Fahrenheit—A temperature measurement scale. (2) farad—A measurement unit of capacitance.
FIFO	First-In First-Out memory buffer—The first data stored is the first data sent to the acceptor. FIFOs are often used on DAQ devices to temporarily store incoming or outgoing data until that data can be retrieved or output.

flash A form of rewritable memory chip that, unlike a Random Access Memory chip, holds content without maintaining a power supply.

frequency The number of alternating signals that occur per unit time.

ft feet

H

h hour

Hz hertz—Cycles per second of a periodic signal; the unit of measure for frequency.

I

I/O Input/Output—The transfer of data to/from a computer system involving communications channels, operator interface devices, and/or data acquisition and control interfaces.

in. inch or inches

instrument driver A set of high-level software functions that controls a specific GPIB, VXI, or RS232 programmable instrument or a specific plug-in DAQ device. Instrument drivers are available in several forms, ranging from a function callable language to a virtual instrument (VI) in LabVIEW. You can download instrument drivers from the Instrument Driver Network at ni.com/idnet.

J

jack *See* [port](#).

jumper A small rectangular device used to connect two adjacent posts on a circuit board. Jumpers are used on some SCXI modules and terminal blocks to either select certain parameters or enable/disable circuit functionality.

K

k kilo—The standard metric prefix for 1,000, or 10^3 , used with units of measure such as volts, hertz, and meters.

K (1) kelvin
(2) kilo—The prefix for 1,024, or 2^{10} , used with B in quantifying data or computer memory.

kbytes/s A unit for data transfer that means 1,000 or 10^3 bytes/s.

kS 1,000 samples

L

LabVIEW A graphical programming language.

LabVIEW DSP Module A fully-featured graphical DSP design tool based on LabVIEW.

LED Light-Emitting Diode

library A file containing compiled object modules, each comprised of one of more functions, that can be linked to other object modules that make use of these functions.

M

m meters

M (1) Mega, the standard metric prefix for 1 million or 10^6 , when used with units of measure such as volts and hertz.
(2) mega, the prefix for 1,048,576, or 2^{20} , when used with B to quantify data or computer memory.

max maximum

MB megabytes of memory

Mbytes/s A unit for data transfer that means 1 million or 10^6 bytes/s.

memory (1) The high-speed electronic storage components of a computer or instrument product. Typically, access times of electronic memories are less than 100 ns, while hard disk drives have access times in the range of 10 ms. The most common form of electronic memory is Random Access Memory (RAM).
(2) The chips in a computer that can store data when the computer is powered on. Different from storage devices, such as disks or tapes.

memory buffer See [buffer](#).

memory map Maps physical resources such as RAM and EPROM to particular CPU addresses. A software memory map maps particular code segments to particular CPU addresses.

MFLOPS Million floating-point operations per second.

min (1) minutes
(2) minimum

N

NC Normally Closed, or Not Connected

NP No Pullup

O

onboard channels Channels provided by the plug-in data acquisition device.

OUT output signal

P

p pull up (v.),
pull-up (adj.)

PC Personal Computer

peripherals Devices that can be connected to your computer or the NI SPEEDY-33. Common peripheral devices are USB DAQ cards, printers, and keyboards.

port (1) A communications connection on a computer or a remote controller.
(2) A digital port, consisting of four or eight lines of digital input and/or output.

R

ROM Read-Only Memory

S

s seconds

S Samples

S/s Samples per second—Used to express the rate at which a DAQ device samples an analog signal.

sample An instantaneous measurement of a signal, normally using an analog-to-digital convertor in a DAQ device.

SPEEDY-33 Signal Processing Engineering Educational Device for Youth—A self-contained, high-performance, programmable product for signal processing applications.

switch (n.) A device for routing signals between two points.

U

USB Universal Serial Bus—A 480 Mbit/s serial bus with up to 12 Mbps bandwidth for connecting computers to keyboards, printers, and other peripheral devices. USB 2.0 retains compatibility with the original USB specification.

V

V volts

VI Virtual Instrument—(1) A combination of hardware and/or software elements, typically used with a PC, that has the functionality of a classic stand-alone instrument.
(2) A LabVIEW software module (VI), which consists of a front panel user interface and a block diagram program.

volatile memory Memory that loses its contents when the power is turned off.

W

word The standard number of bits that a processor or memory manipulates at one time. Microprocessors typically use 8-, 16-, or 32-bit words.

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