

**Q1. An a.c. voltage of peak value 20 V is connected in series with a silicon diode and load resistance of 500 Ω . If the forward resistance of diode is 10 Ω , find :
 (i) peak current through diode (ii) peak output voltage
 What will be these values if the diode is assumed to be ideal ?**

Solution :

Peak input voltage = 20 V

Forward resistance, $r_f = 10 \Omega$

Load resistance, $R_L = 500 \Omega$

Potential barrier voltage, $V_0 = 0.7 \text{ V}$

The diode will conduct during the positive half-cycles of a.c. input voltage only.

The equivalent circuit is shown in Fig.1(ii)

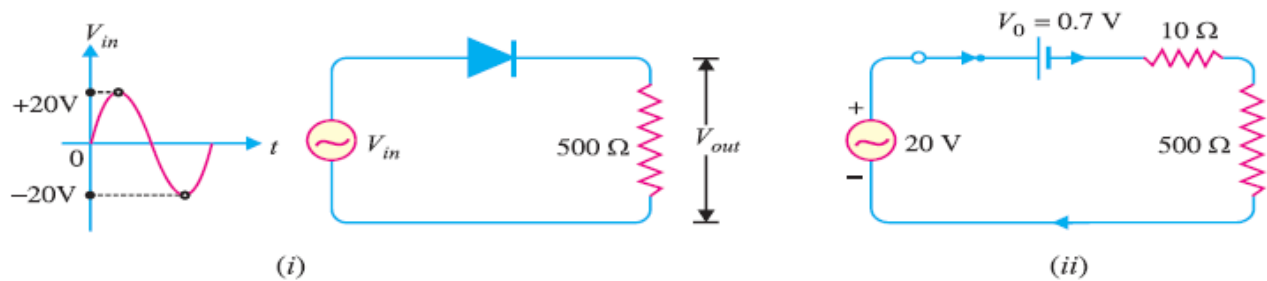


Fig. 1

(i) The peak current through the diode will occur at the instant when the input voltage reaches positive peak i.e. $V_{in} = V_F = 20 \text{ V}$.

$$\therefore V_F = V_0 + (I_f)_{peak} [r_f + R_L] \quad \dots(i)$$

$$\text{or } (I_f)_{peak} = \frac{V_F - V_0}{r_f + R_L} = \frac{20 - 0.7}{10 + 500} = \frac{19.3}{510} \text{ A} = \mathbf{37.8 \text{ mA}}$$

(ii) Peak output voltage :

$$\text{Peak output voltage} = (I_f)_{peak} \times R_L = 37.8 \text{ mA} \times 500 \Omega = \mathbf{18.9 \text{ V}}$$

Ideal Diode Case:

For an ideal diode, put $V_0 = 0$ and $r_f = 0$ in equation (i).

$$V_F = (I_f)_{peak} \times R_L$$

or
$$(I_f)_{peak} = \frac{V_F}{R_L} = \frac{20 \text{ V}}{500 \Omega} = 40 \text{ mA}$$

$$\text{Peak output voltage} = (I_f)_{peak} \times R_L = 40 \text{ mA} \times 500 \Omega = 20 \text{ V}$$

Q2. Find the current through the diode in the circuit shown in Fig. 2(i). Assume the diode to be ideal.

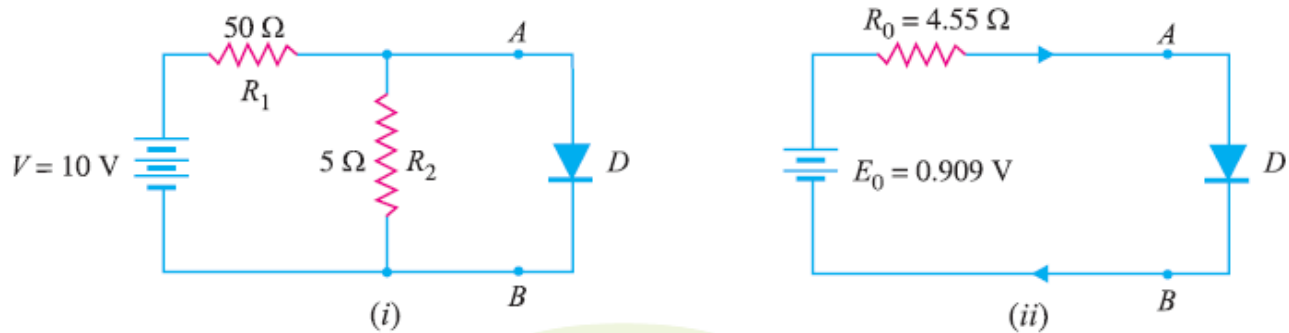


Fig. 2

Solution :

We shall use Thevenin's theorem to find current in the diode. Referring to Fig. 2(i),

$$\begin{aligned} E_0 &= \text{Thevenin's voltage} \\ &= \text{Open circuited voltage across } AB \text{ with diode removed} \\ &= \frac{R_2}{R_1 + R_2} \times V = \frac{5}{50 + 5} \times 10 = 0.909 \text{ V} \\ R_0 &= \text{Thevenin's resistance} \\ &= \text{Resistance at terminals } AB \text{ with diode removed and battery replaced by a short circuit} \\ &= \frac{R_1 R_2}{R_1 + R_2} = \frac{50 \times 5}{50 + 5} = 4.55 \Omega \end{aligned}$$

Fig. 2 (ii) shows Thevenin's equivalent circuit. Since the diode is ideal, it has zero resistance

$$\therefore \text{Current through diode} = \frac{E_0}{R_0} = \frac{0.909}{4.55} = 0.2 \text{ A} = 200 \text{ mA}$$

Q3. Calculate the current through $48\ \Omega$ resistor in the circuit shown in Fig. 3 (i). Assume the diodes to be of silicon and forward resistance of each diode is $1\ \Omega$.

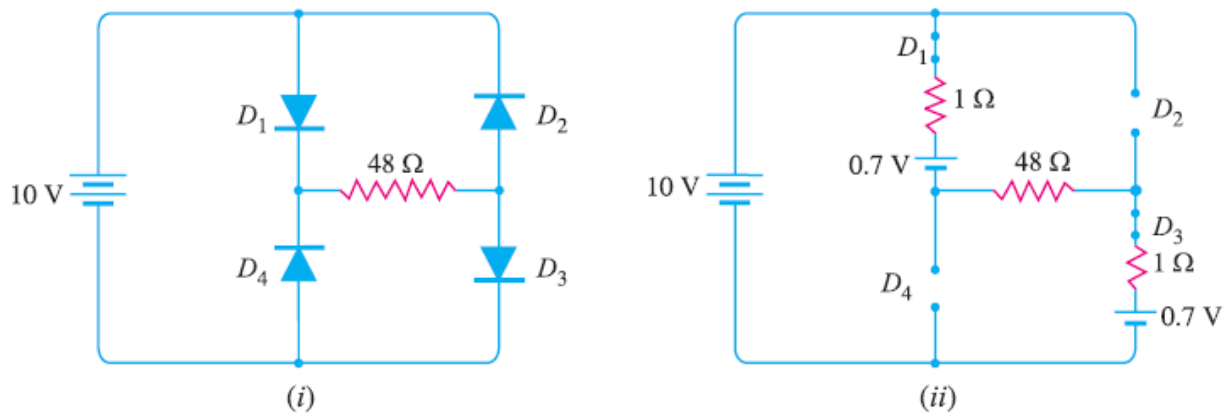


Fig. 3

Solution :

Diodes D_1 and D_3 are forward biased while diodes D_2 and D_4 are reverse biased. We can, therefore, consider the branches containing diodes D_2 and D_4 as “open”.

Replacing diodes D_1 and D_3 by their equivalent circuits and making the branches containing diodes D_2 and D_4 open, we get the circuit shown in Fig. 3 (ii). As we know for a silicon diode, the barrier voltage is 0.7 V .

$$\begin{aligned}\text{Net circuit voltage} &= 10 - 0.7 - 0.7 = 8.6\text{ V} \\ \text{Total circuit resistance} &= 1 + 48 + 1 = 50\ \Omega \\ \therefore \text{Circuit current} &= 8.6/50 = 0.172\text{ A} = \mathbf{172\text{ mA}}\end{aligned}$$

Q4. Determine the current I in the circuit shown in Fig. 4 (i). Assume the diodes to be of silicon and forward resistance of diodes to be zero.

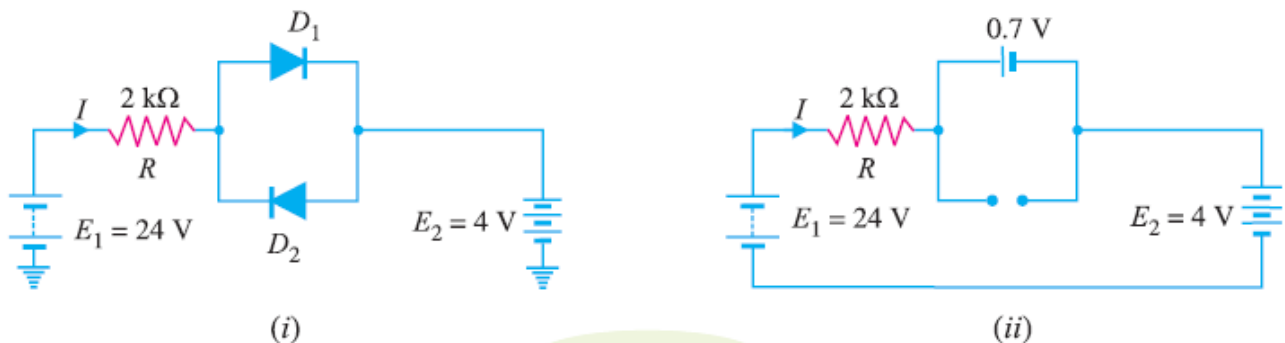


Fig. 4

Solution :

The conditions of the problem suggest that diode D1 is forward biased and diode D2 is reverse biased. We can, therefore, consider the branch containing diode D2 as open as shown in Fig. 4 (ii).

Further, diode D1 can be replaced by its simplified equivalent circuit.

$$I = \frac{E_1 - E_2 - V_0}{R} = \frac{24 - 4 - 0.7}{2 \text{ k}\Omega} = \frac{19.3 \text{ V}}{2 \text{ k}\Omega} = 9.65 \text{ mA}$$

Q5. Find the voltage V_A in the circuit shown in Fig. 5 (i). Use simplified model.

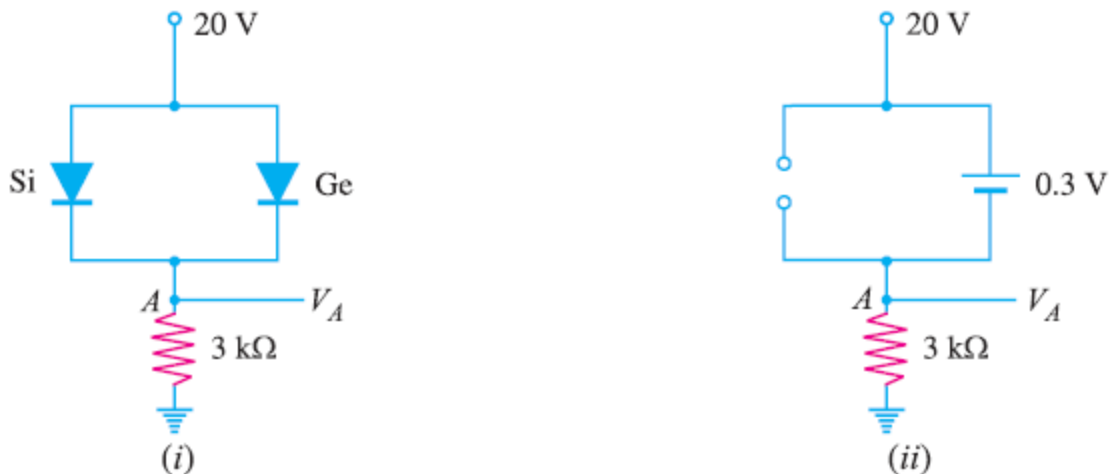


Fig. 5

Solution :

It appears that when the applied voltage is switched on, both the diodes will turn “on”. But that is not so. When voltage is applied, germanium diode ($V_0 = 0.3 \text{ V}$) will turn on first and a level of 0.3V is maintained across the parallel circuit.

The silicon diode never gets the opportunity to have 0.7 V across it and, therefore, remains in open-circuit state as shown in Fig.5(ii).

$$V_A = 20 - 0.3 = 19.7 \text{ V}$$

Q6. Find V_Q and I_D in the network shown in Fig. 6(i). Use simplified model.

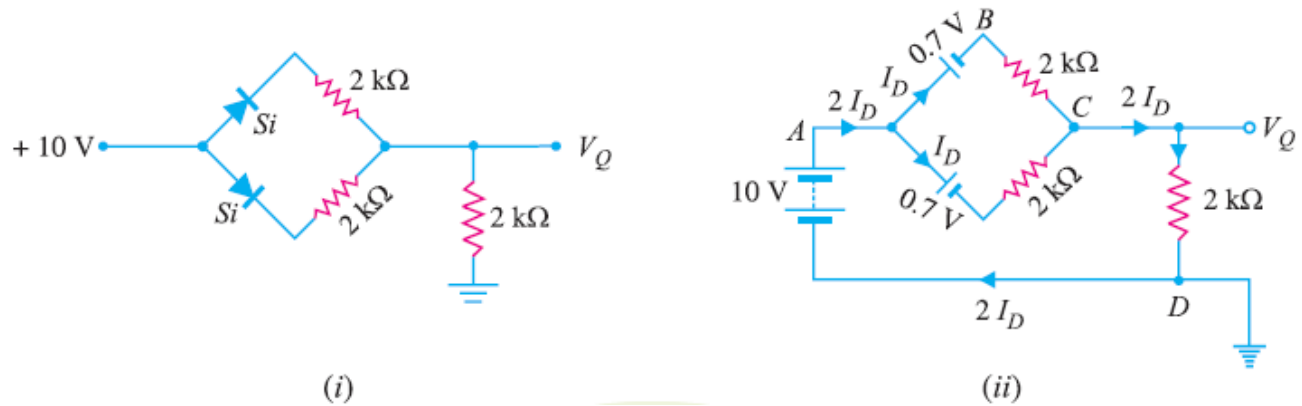


Fig. 6

Solution :

Replace the diodes by their simplified models. The resulting circuit will be as shown in Fig. 6 (ii).

By symmetry, current in each branch is I_D so that current in branch CD is $2I_D$.

Applying Kirchhoff's voltage law to the closed circuit ABCDA, we have,

$$\begin{aligned}
 -0.7 - I_D \times 2 - 2I_D \times 2 + 10 &= 0 & (I_D \text{ in mA}) \\
 \text{or} & 6I_D = 9.3 \\
 \therefore I_D &= \frac{9.3}{6} = \mathbf{1.55 \text{ mA}} \\
 \text{Also} & V_Q = (2I_D) \times 2 \text{ k}\Omega = (2 \times 1.55 \text{ mA}) \times 2 \text{ k}\Omega = \mathbf{6.2 \text{ V}}
 \end{aligned}$$

Q7. Determine current through each diode in the circuit shown in Fig. 7 (i). Use simplified model. Assume diodes to be similar.

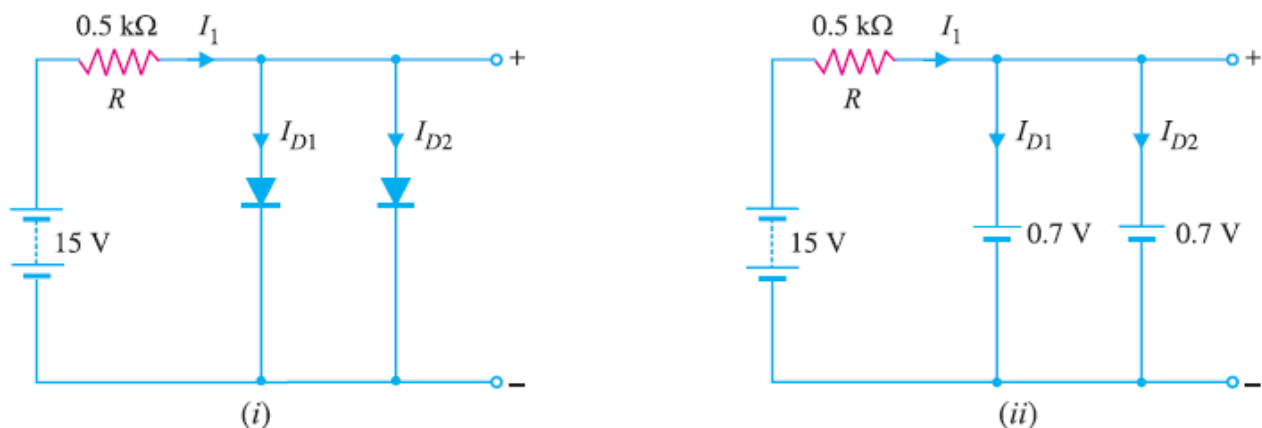


Fig.7

Solution :

The applied voltage forward biases each diode so that they conduct current in the same direction. Fig. 7 (ii) shows the equivalent circuit using simplified model. Referring to Fig. 7 (ii),

$$I_1 = \frac{\text{Voltage across } R}{R} = \frac{15 - 0.7}{0.5 \text{ k}\Omega} = 28.6 \text{ mA}$$

$$\text{Since the diodes are similar, } I_{D1} = I_{D2} = \frac{I_1}{2} = \frac{28.6}{2} = \mathbf{14.3 \text{ mA}}$$

Q8. Determine the currents I_1 , I_2 and I_3 for the network shown in Fig. 8(i). Use simplified model for the diodes.

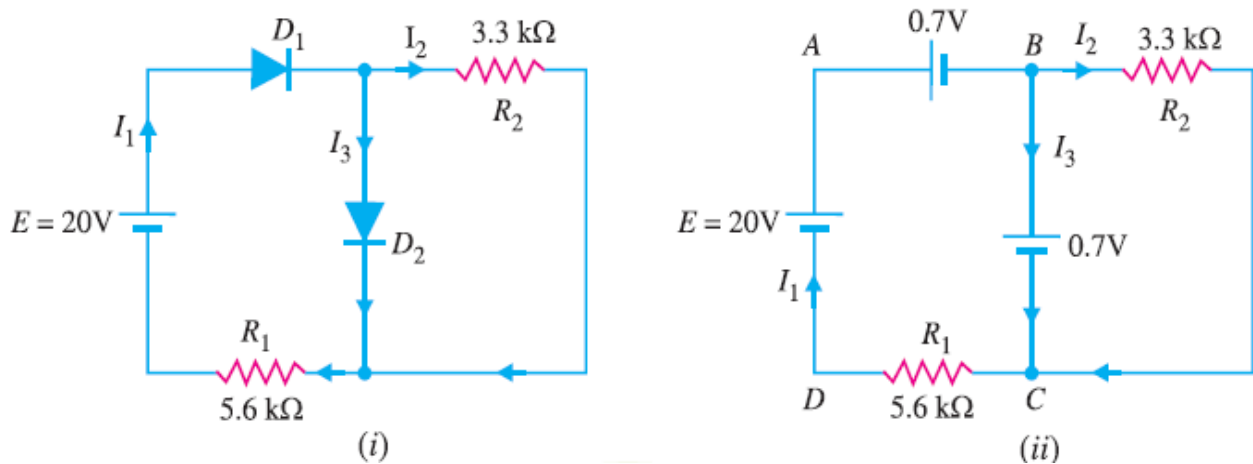


Fig. 8

Solution :

As we can see in Fig. 8 (i) both diodes D_1 and D_2 are forward biased. Using simplified model for the diodes, the circuit shown in Fig. 8(i) becomes the one shown in Fig. 8 (ii).

The voltage across R_2 ($= 3.3 \text{ k}\Omega$) is 0.7V.

$$\therefore I_2 = \frac{0.7 \text{ V}}{3.3 \text{ k}\Omega} = \mathbf{0.212 \text{ mA}}$$

Applying Kirchhoff's voltage law to loop ABCDA in Fig. 8 (ii), we have,

$$\begin{aligned} -0.7 - 0.7 - I_1 R_1 + 20 &= 0 \\ \therefore I_1 &= \frac{20 - 0.7 - 0.7}{R_1} = \frac{18.6 \text{ V}}{5.6 \text{ k}\Omega} = \mathbf{3.32 \text{ mA}} \\ \text{Now } I_1 &= I_2 + I_3 \\ \therefore I_3 &= I_1 - I_2 = 3.32 - 0.212 = \mathbf{3.108 \text{ mA}} \end{aligned}$$

Q9. Determine if the diode (ideal) in Fig. 9 (i) is forward biased or reverse biased.

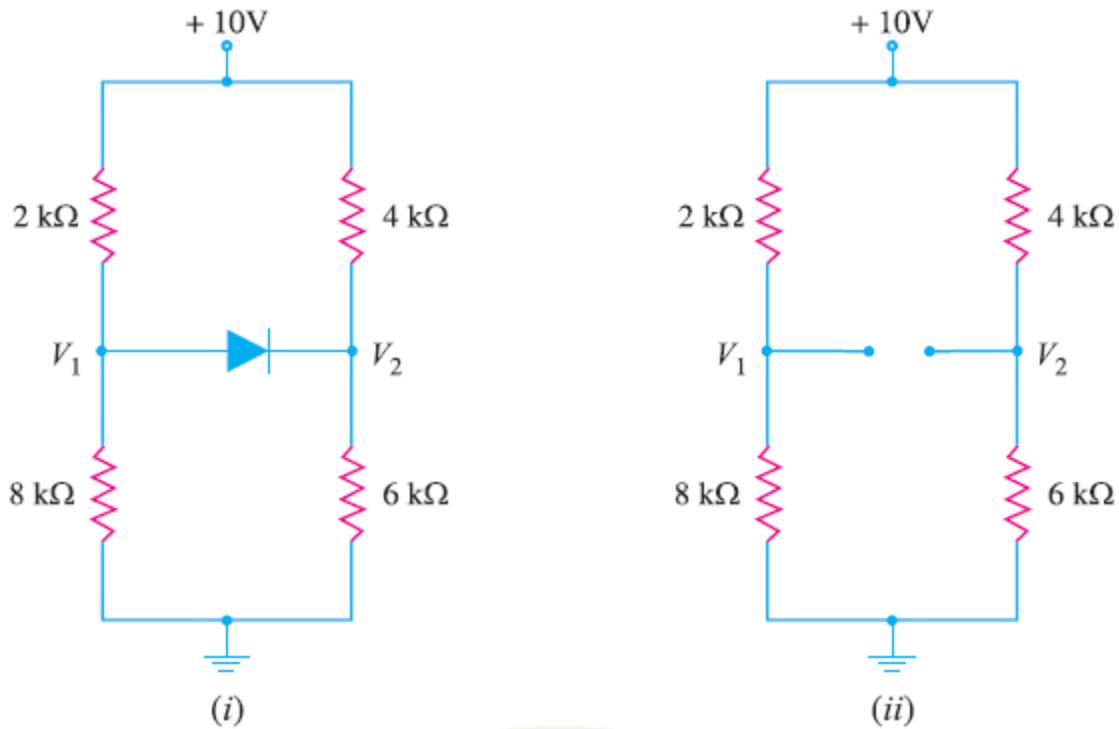


Fig. 9

Solution :

Let us assume that diode in Fig.9 (i) is OFF i.e. it is reverse biased.

The circuit then becomes as shown in Fig. 9(ii). Referring to Fig. 9 (ii), we have,

$$V_1 = \frac{10 \text{ V}}{2 \text{ k}\Omega + 8 \text{ k}\Omega} \times 8 \text{ k}\Omega = 8 \text{ V}$$

$$V_2 = \frac{10 \text{ V}}{4 \text{ k}\Omega + 6 \text{ k}\Omega} \times 6 \text{ k}\Omega = 6 \text{ V}$$

$$\therefore \text{ Voltage across diode} = V_1 - V_2 = 8 - 6 = 2 \text{ V}$$

Now $V_1 - V_2 = 2 \text{ V}$ is enough voltage to make the diode forward biased. Therefore, our initial assumption was wrong, and diode is forward biased.

Q10. Determine the state of diode for the circuit shown in Fig. 10 (i) and find I_D and V_D . Assume simplified model for the diode.

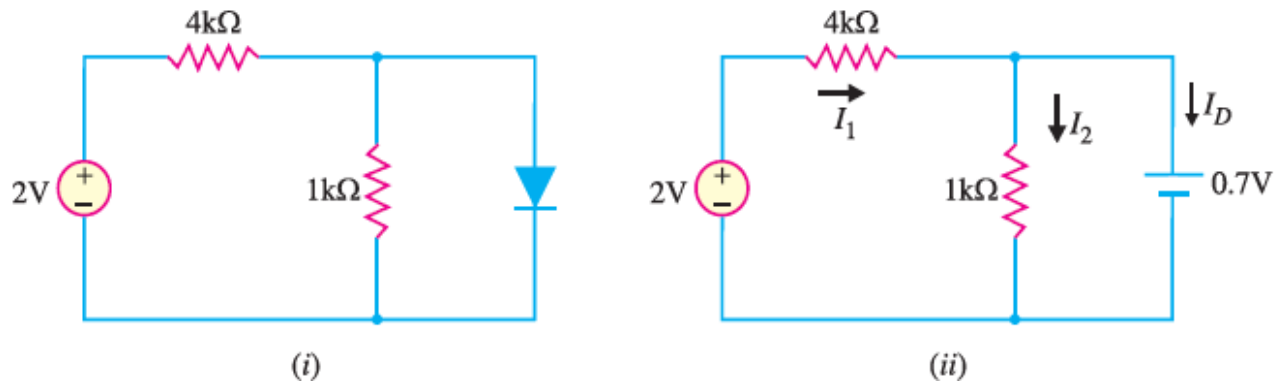


Fig. 10

Solution :

Let us assume that the diode is ON. Therefore, we can replace the diode with a 0.7V battery as shown in Fig. 10 (ii). Referring to Fig.10 (ii), we have,

$$I_1 = \frac{(2 - 0.7) \text{ V}}{4 \text{ k}\Omega} = \frac{1.3 \text{ V}}{4 \text{ k}\Omega} = 0.325 \text{ mA}$$

$$I_2 = \frac{0.7 \text{ V}}{1 \text{ k}\Omega} = 0.7 \text{ mA}$$

$$\text{Now } I_D = I_1 - I_2 = 0.325 - 0.7 = -0.375 \text{ mA}$$

Since the diode current is negative, the diode must be OFF and the true value of diode current is $I_D = 0 \text{ mA}$. Hence our initial assumption was wrong.

In order to analyse the circuit properly, we should replace the diode in Fig. 10 (i) with an open circuit as shown in Fig.10(iii).

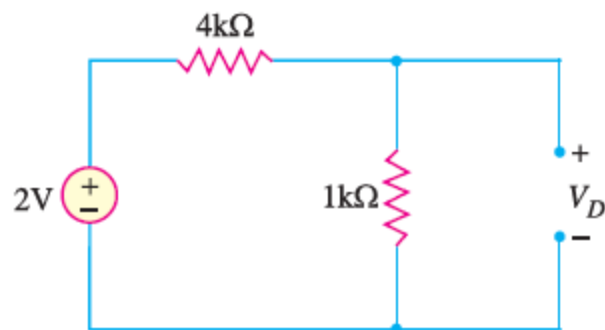


Fig.10 (iii)

The voltage V_D across the diode is :

$$V_D = \frac{2 \text{ V}}{1 \text{ k}\Omega + 4 \text{ k}\Omega} \times 1 \text{ k}\Omega = 0.4 \text{ V}$$

We know that 0.7V is required to turn ON the diode. Since V_D is only 0.4V, the answer confirms that the diode is OFF.