Multiplexers in Verilog

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Objective:

This lab's objective was to design multiplexers utilizing several methods of modeling with ModelSim. That is, the goals of this lab were to create a 4x1 MUX utilizing both gate-level modeling and behavioral modeling. Additionally, hierarchical design was utilized to construct an 8x1 MUX from 4x1 and 2x1 MUX.

Equipment Used:

Verilog ModelSim

ModelSim Circuit Programs:

2x1 MUX:

```
module twoXOneMux (Y, IO, II, S);
1
 2
 3
       output Y;
 4
       input IO, I1, S;
 5
      wire TO, T1, Sbar;
 6
 7
      not (Sbar, S);
 8
       and (T0, I0, Sbar);
      and (T1, I1, S);
 9
10
      or (Y, T0, T1);
11
12
   endmodule
```

2x1 MUX Testbench:

```
pmodule twoXOneMux_tb();
2
      wire Y out;
3
      reg IO_in, Il_in, S_in;
 5
      twoXOneMux uut(.Y(Y_out), .IO(IO_in), .II(II_in), .S(S_in));
 6
 7
      initial
    begin
 8
9
      I0 in=1'b0;
      Il_in=1'b0;
10
11
      S in=1'b0;
12
      end
13
14
      always #20 IO_in=~IO_in;
      always #10 Il_in=~Il_in;
15
      always #5 S_in=~S_in;
17
18
      always@(IO_in or Il_in or S_in)
19
     $monitor("At time %t, I0_in = %b, I1_in = %b, S_in = %b, Y_out = %b", $time, I0_in, I1_in, S_in, Y_out); end
20
21
     endmodule
```

4x1 MUX A:

```
module fourXOneMuxA(Y, I0, I1, I2, I3, S0, S1);
 2
 3
       output Y;
 4
       input I0, I1, I2, I3, S0, S1;
 5
       wire T0, T1, T2, T3, S0bar, S1bar;
 6
 7
       not (S0bar, S0);
 8
       not (Slbar, S1);
 9
       and (TO, IO, Slbar, S0bar);
10
       and (T1, I1, Slbar, S0);
11
       and (T2, I2, S1, S0bar);
       and (T3, I3, S1, S0);
12
13
       or (Y, T0, T1, T2, T3);
14
15
16
     - endmodule
17
```

4x1 MUX A Testbench:

4x1 MUX B:

```
module fourXOneMuxB(Y, I0, I1, I2, I3, S0, S1);
2
       input I0, I1, I2, I3, S0, S1;
3
       output Y;
4
      reg Y;
5
6
      always @ (S1 or S0)
7
     □ case ({S1, S0})
8
               2'b00:
9
                        Y = I0;
10
               2'b01:
11
                        Y = I1:
12
               2'b10:
13
                        Y = I2;
14
               2'b11:
15
                        Y = I3:
     L endcase
16
17
       endmodule
```

4x1 MUX B Testbench:

8x1 MUX:

```
module eightXOneMux(Y, I0, I1, I2, I3, I4, I5, I6, I7, S0, S1, S2);
2
      input IO, I1, I2, I3, I4, I5, I6, I7, S0, S1, S2;
 3
      output Y;
4
      wire TO, T1;
5
6
      fourXOneMuxA FXOA (T0, I0, I1, I2, I3, S0, S1);
      fourXOneMuxA FXOB (T1, I4, I5, I6, I7, S0, S1);
8
      twoXOneMux TXO (Y, T0, T1, S2);
9
      endmodule
10
```

8x1 MUX Testbench:

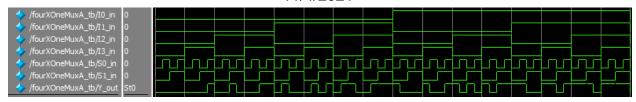
Output:

2x1 MUX:

```
At time
                                  0, I0 in = 0, I1 in = 0, S in = 0, Y out = 0
     At time
                                  5, I0 in = 0, I1 in = 0, S in = 1, Y out = 0
     At time
                                 10, I0 in = 0, I1 in = 1, S in = 0, Y out = 0
     At time
                                 15, IO in = 0, Il in = 1, S in = 1, Y out = 1
     At time
                                 20, I0 in = 1, I1 in = 0, S in = 0, Y out = 1
     At time
                                 25, I0_in = 1, I1_in = 0, S_in = 1, Y_out = 0
    # At time
                                 30, I0_in = 1, I1_in = 1, S_in = 0, Y_out = 1
    # At time
                                 35, I0_in = 1, I1_in = 1, S_in = 1, Y_out = 1
    # At time
                                40, I0 in = 0, I1 in = 0, S in = 0, Y out = 0
    # At time
                                45, I0_in = 0, I1_in = 0, S_in = 1, Y_out = 0
    # At time
                                50, I0_in = 0, I1_in = 1, S_in = 0, Y_out = 0
                                55, IO_in = 0, Il_in = 1, S_in = 1, Y_out = 1
    # At time
                                 60, IO_in = 1, I1_in = 0, S_in = 0, Y_out = 1
    # At time
    # At time
                                 65, I0_in = 1, I1_in = 0, S_in = 1, Y_out = 0
    # At time
                                70, I0_in = 1, I1_in = 1, S_in = 0, Y_out = 1
                                75, I0 in = 1, I1 in = 1, S in = 1, Y out = 1
     At time
     At time
                                80, I0 in = 0, I1 in = 0, S in = 0, Y out = 0
    # At time
                                85, I0 in = 0, I1 in = 0, S in = 1, Y out = 0
                                90, I0 in = 0, I1 in = 1, S in = 0, Y out = 0
    # At time
                                95, I0 in = 0, I1 in = 1, S in = 1, Y out = 1
    # At time
    # At time
                               100, IO_in = 1, Il_in = 0, S_in = 0, Y_out = 1
    # At time
                               105, IO_in = 1, I1_in = 0, S_in = 1, Y_out = 0
    # At time
                               110, I0_in = 1, I1_in = 1, S_in = 0, Y_out = 1
    # At time
                               115, I0 in = 1, I1 in = 1, S in = 1, Y out = 1
    # At time
                               120, IO_in = 0, I1_in = 0, S_in = 0, Y_out = 0
    # At time
                               125, I0 in = 0, I1 in = 0, S in = 1, Y out = 0
                               130, IO_in = 0, Il_in = 1, S_in = 0, Y_out = 0
    # At time
     At time
                               135, IO in = 0, Il in = 1, S in = 1, Y out = 1
    # At time
                               140, I0_in = 1, I1_in = 0, S_in = 0, Y_out = 1
    # At time
                               145, I0_in = 1, I1_in = 0, S_in = 1, Y_out = 0
                                150, IO_in = 1, Il_in = 1, S_in = 0, Y_out = 1
     At time
     At time
                                155, IO_in = 1, II_in = 1, S_in = 1, Y_out = 1
voXOneMux_tb/I0_in
voXOneMux_tb/I1_in
oXOneMux_tb/S_in
oXOneMux_tb/Y_out
        Now 160 ps
```

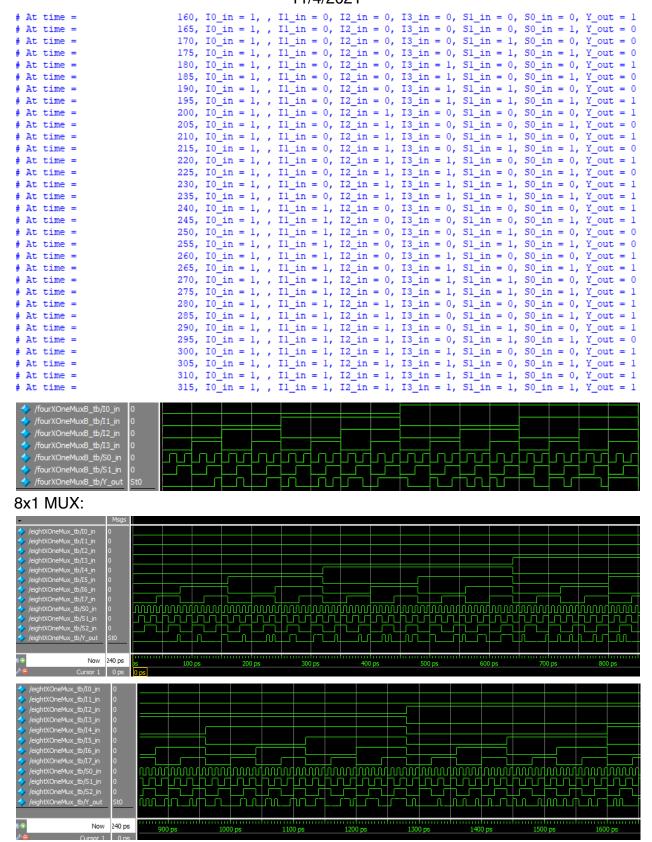
4x1 MUX A:

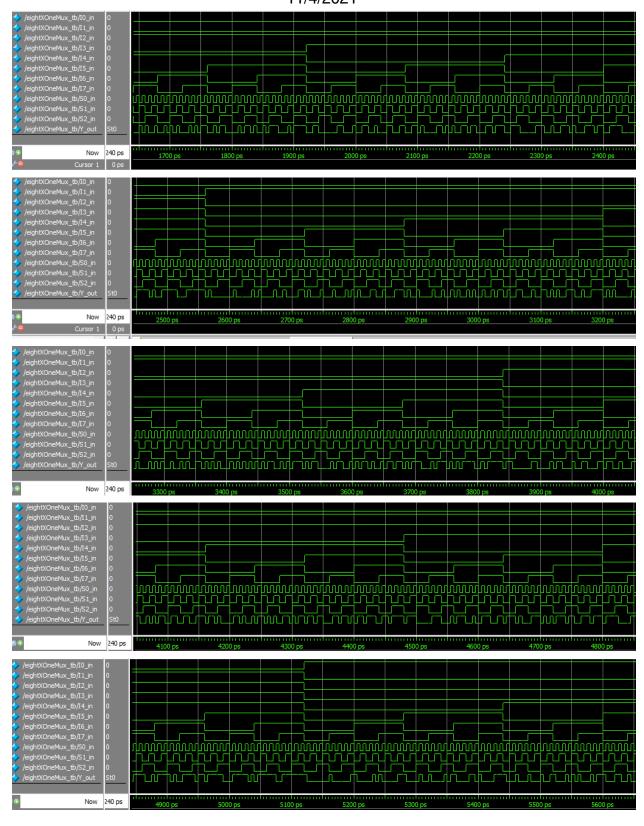
```
0, I0_in = 0, , I1_in = 0, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 0 5, I0_in = 0, , I1_in = 0, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 0
# At time =
# At time =
# At time =
                                  10, I0_in = 0, , I1_in = 0, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 0
                                  15, IO_in = 0, , I1_in = 0, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
# At time =
                                  20, I0_in = 0, , I1_in = 0, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 0
25, I0_in = 0, , I1_in = 0, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 0
# At time =
4 At time =
# At time =
                                  30, I0_in = 0, , I1_in = 0, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 0
                                  35, IO_in = 0, , II_in = 0, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 1, Y_out = 1
40, IO_in = 0, , I1_in = 0, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 0
# At time =
# At time =
                                  45, I0_in = 0, , I1_in = 0, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 0
# At time =
                                  50, IO_in = 0, , Il_in = 0, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 1
# At time =
# At time =
                                  55, IO_in = 0, , I1_in = 0, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
                                  60, IO_in = 0, , II_in = 0, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 0
# At time =
                                  65, IO_in = 0, , II_in = 0, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 0
# At time =
                                   70, IO_in = 0, , I1_in = 0, I2_in = 1, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 1
# At time =
                                   75, IO_in = 0, , II_in = 0, I2_in = 1, I3_in = 1, S1_in = 1, S0_in = 1, Y_out = 1
# At time =
# At time =
                                  80, I0_in = 0, , I1_in = 1, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 0
# At time =
                                  85, IO_in = 0, , Il_in = 1, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 1
# At time =
                                  90, IO_in = 0, , II_in = 1, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 0
                                  95, IO_in = 0, , II_in = 1, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
# At time =
                                 100, IO in = 0, , II in = 1, I2 in = 0, I3 in = 1, S1 in = 0, S0 in = 0, Y out = 0
# At time =
# At time =
                                 105, IO_in = 0, , Il_in = 1, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 1
                                 110, IO_in = 0, , I1_in = 1, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 0
# At time =
                                115, IO_in = 0, , I1_in = 1, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 1, Y_out = 1
# At time =
                                120, IO_in = 0, , II_in = 1, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 0
125, IO_in = 0, , II_in = 1, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 1
# At time =
# At time =
# At time =
                                130, IO_in = 0, , II_in = 1, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 1
# At time =
                                 135, IO_in = 0, , I1_in = 1, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
                                140, IO_in = 0, , II_in = 1, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 0
# At time =
                                145, IO_in = 0, , I1_in = 1, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 1
# At time =
                                150, IO in = 0, , II in = 1, I2 in = 1, I3 in = 1, S1 in = 1, S0 in = 0, Y out = 1
155, IO in = 0, , II in = 1, I2 in = 1, I3 in = 1, S1 in = 1, S0 in = 1, Y out = 1
# At time =
# At time =
                                 160, IO_in = 1, , Il_in = 0, I2_in = 0, I3_in = 0, Sl_in = 0, SO_in = 0, Y_out = 1
# At time =
                               165, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 0
# At time =
# At time =
                                170, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 0
                               175, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
180, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 1
# At time =
# At time =
 # At time =
                               185, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 0
                                190, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 0
195, IO_in = 1, , II_in = 0, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 1, Y_out = 1
 # At time =
# At time =
 # At time =
                                200, IO_in = 1, , I1_in = 0, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 1
 # At time =
                                205, IO_in = 1, , I1_in = 0, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 0
 # At time =
                                 210, I0 in = 1, , I1 in = 0, I2 in = 1, I3 in = 0, S1 in = 1, S0 in = 0, Y out = 1
                                215, IO_in = 1, , I1_in = 0, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
 # At time =
# At time =
                                220, IO_in = 1, , I1_in = 0, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 1
                                 225, IO in = 1, , II in = 0, I2 in = 1, I3 in = 1, S1 in = 0, S0 in = 1, Y out = 0
 # At time =
                                230, IO_in = 1, , II_in = 0, I2_in = 1, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 1
# At time =
 # At time =
                                235, IO_in = 1, , I1_in = 0, I2_in = 1, I3_in = 1, S1_in = 1, S0_in = 1, Y_out = 1
                                240, IO_in = 1, , II_in = 1, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 1
245, IO_in = 1, , II_in = 1, I2_in = 0, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 1
 # At time =
# At time =
                                250, IO_in = 1, , II_in = 1, I2_in = 0, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 0
 # At time =
                                255, IO_in = 1, , II_in = 1, I2_in = 0, I3_in = 0, S1_in = 1, SO_in = 1, Y_out = 0
260, IO_in = 1, , II_in = 1, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 1
# At time =
# At time =
                                265, IO_in = 1, , I1_in = 1, I2_in = 0, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 1
 # At time =
                                270, IO_in = 1, , I1_in = 1, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 0
# At time =
                                 275, IO_in = 1, , I1_in = 1, I2_in = 0, I3_in = 1, S1_in = 1, S0_in = 1, Y_out = 1
 # At time =
                                280, IO_in = 1, , I1_in = 1, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 0, Y_out = 1
 # At time =
 # At time =
                               285, IO_in = 1, , II_in = 1, I2_in = 1, I3_in = 0, S1_in = 0, S0_in = 1, Y_out = 1
                               290, IO_in = 1, , II_in = 1, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 0, Y_out = 1
295, IO_in = 1, , II_in = 1, I2_in = 1, I3_in = 0, S1_in = 1, S0_in = 1, Y_out = 0
 # At time =
# At time =
 # At time =
                               300, IO_in = 1, , II_in = 1, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 0, Y_out = 1
                               305, IO_in = 1, , I1_in = 1, I2_in = 1, I3_in = 1, S1_in = 0, S0_in = 1, Y_out = 1 310, IO_in = 1, , I1_in = 1, I2_in = 1, I3_in = 1, S1_in = 1, S0_in = 0, Y_out = 1
# At time =
# At time =
                                315, IO in = 1, , II in = 1, I2 in = 1, I3 in = 1, S1 in = 1, SO in = 1, Y out = 1
```

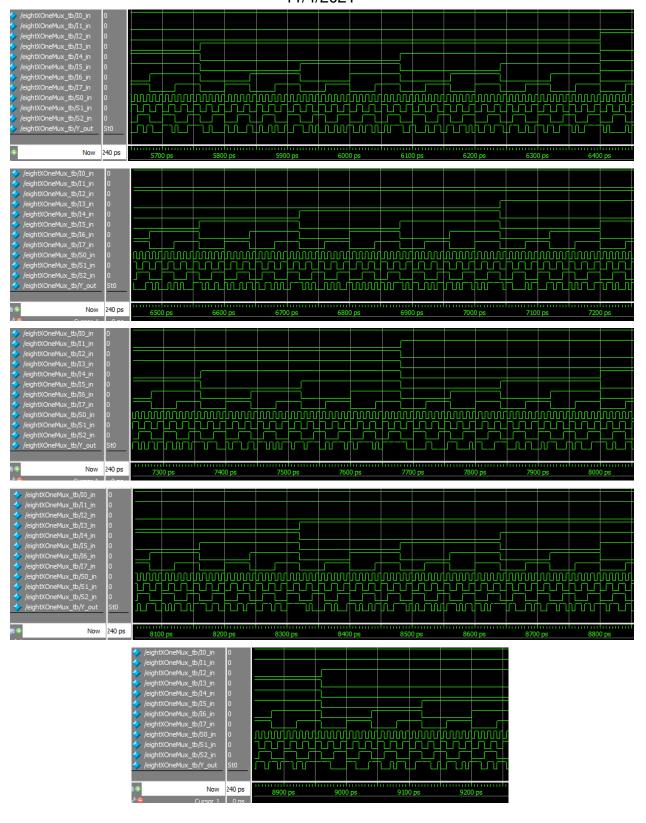


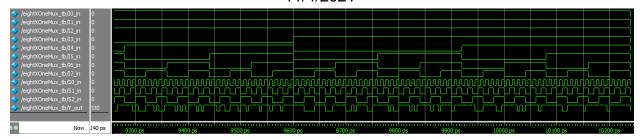
4x1 MUX B:











Conclusion:

A multiplexer only outputs true when the input number is equivalent to the binary number denoted by its select lines. For instance, when there are two select lines, both of which are set to logic 1's (making a binary 3), input I3 controls the output. As demonstrated in this lab, multiplexers can be chained together to function together as a single multiplexer with more inputs and select lines.