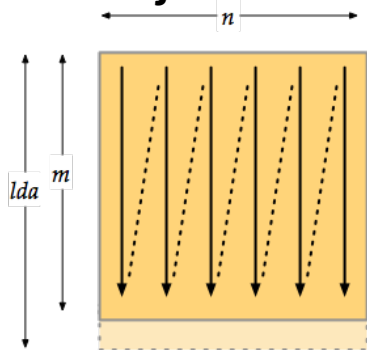
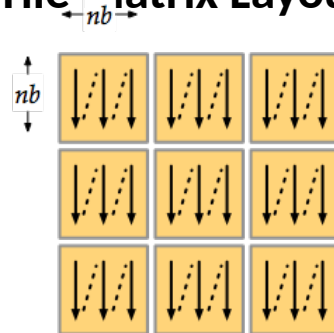


Tile Matrix Layout

Column-major Memory Layout



Tile Matrix Layout



- Each tile is contiguous (column major)
- Enables dataflow scheduling
- Cache and TLB efficient
(reduces conflict misses and false sharing)
- MPI messaging efficiency