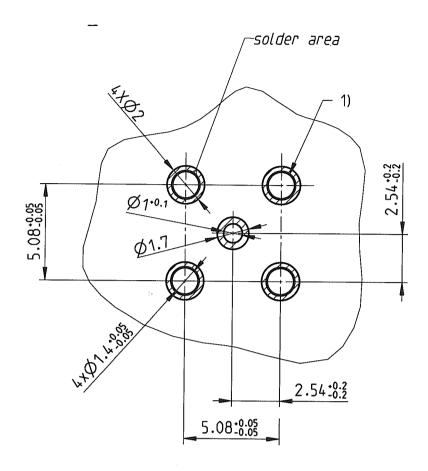
Leiterplatten-Layout PCB layout B 273



1) 5 Bohrungen durchkontaktieren mit Restringen auf Rueckseite. (Restringbreite 0.25 mm min.) 5 drill holes plated inclusive pads on the backside. (padwidth 0.25 mm min.)

A wide variety of transmissionline topologies and pcb-parameters like permittivity, substrate thickness, and board-stackup are applied by customers. These parameters have a strong impact on the high frequency performance of the mounted connector. Please note, that the given layout is not optimised to fit all of the possible board configurations regarding RF-performance, it represents a recommendation for optimum solderability of the connector. In order to guarantee optimum high frequency properties of the connector. an RF-analysis of the connector to board transition is recommended.

Ξ ⊦					general tolerance ISO 2768 RN 006-01 m-H dimensions <0.5 and symmetry				0,5	scale: material:	5:1	weight[g]:. surface(mm²]:.
7				date name drawn 29.08.2007 U_Keehele check. 20.5 coll appr. 20.5 coll dimensioning incl. finish					title:	Leiter <i>P</i>	platten-Layout CB layout	
a00 100		U_Koebele U_Koebele name	19.05.2008 29.08.2007 date	distribu- tion to:	FE X	AZ	JSM	RMT .		drawing-na	M	Sheet: 1 of: 1